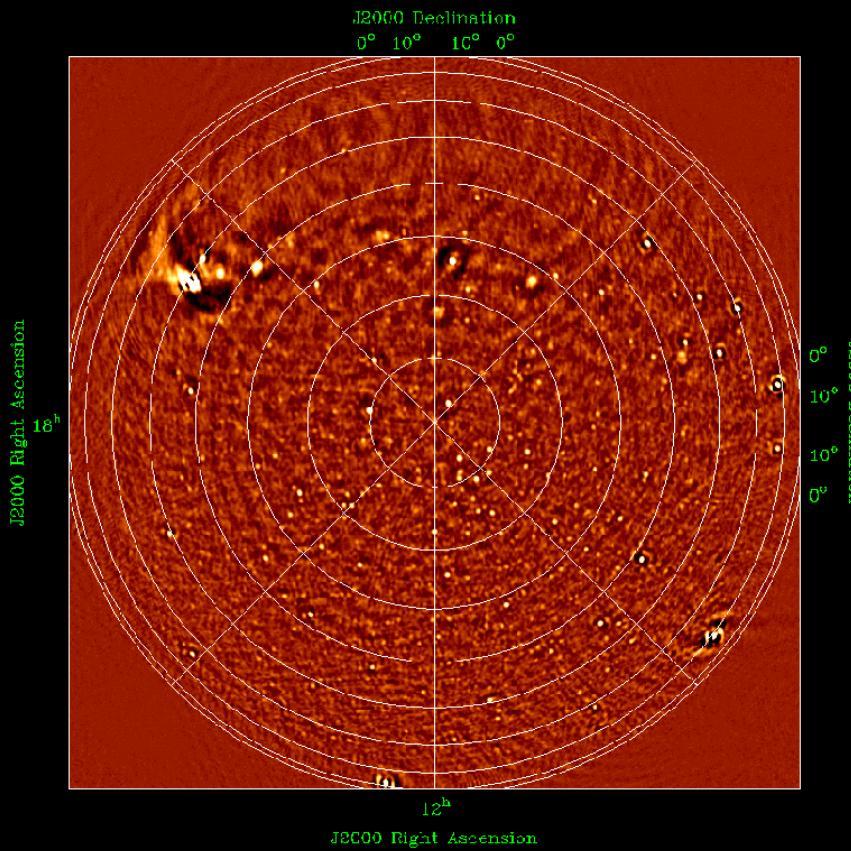
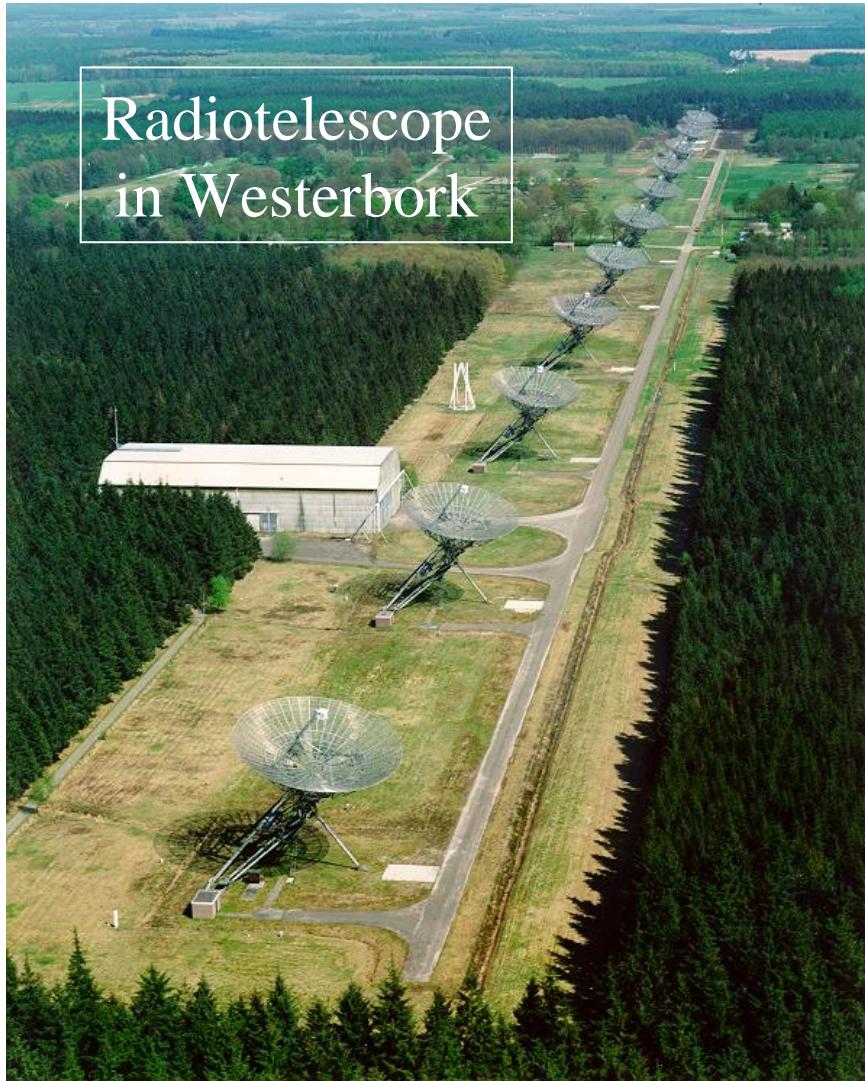


# *Applications of Digital Processing in LOFAR*

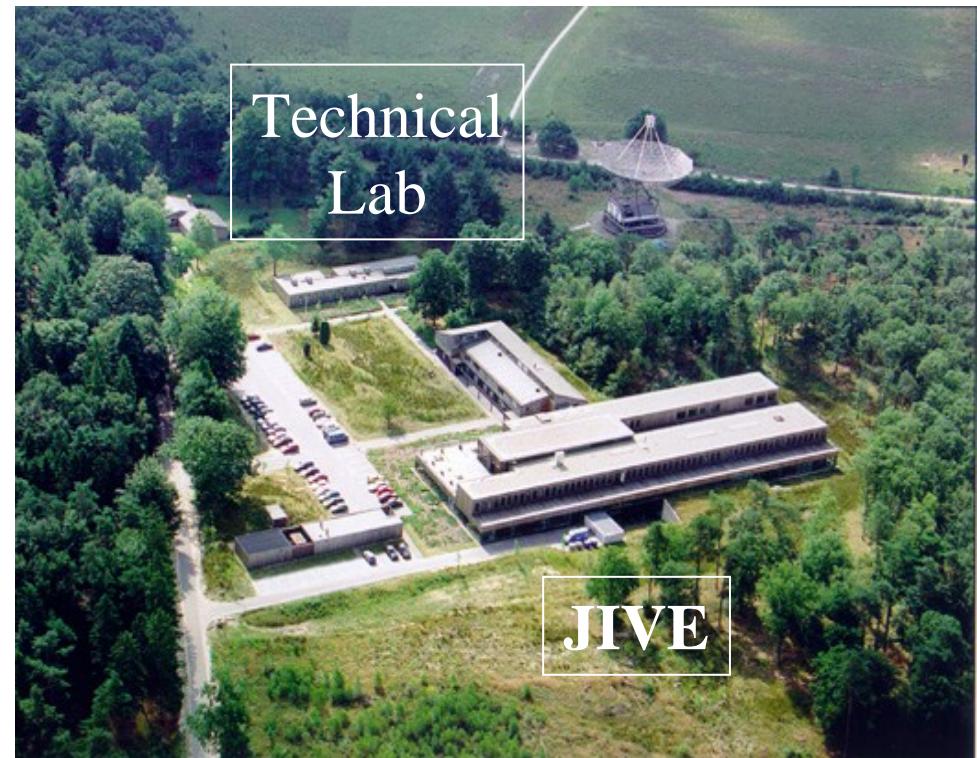
*André W. Gunst*



- Introduction ASTRON
  - LOFAR System Overview
  - LOFAR Station Processing
  - LOFAR Data Transport
  - LOFAR Central Processing
  - Beyond LOFAR
  - Status and Conclusions
-



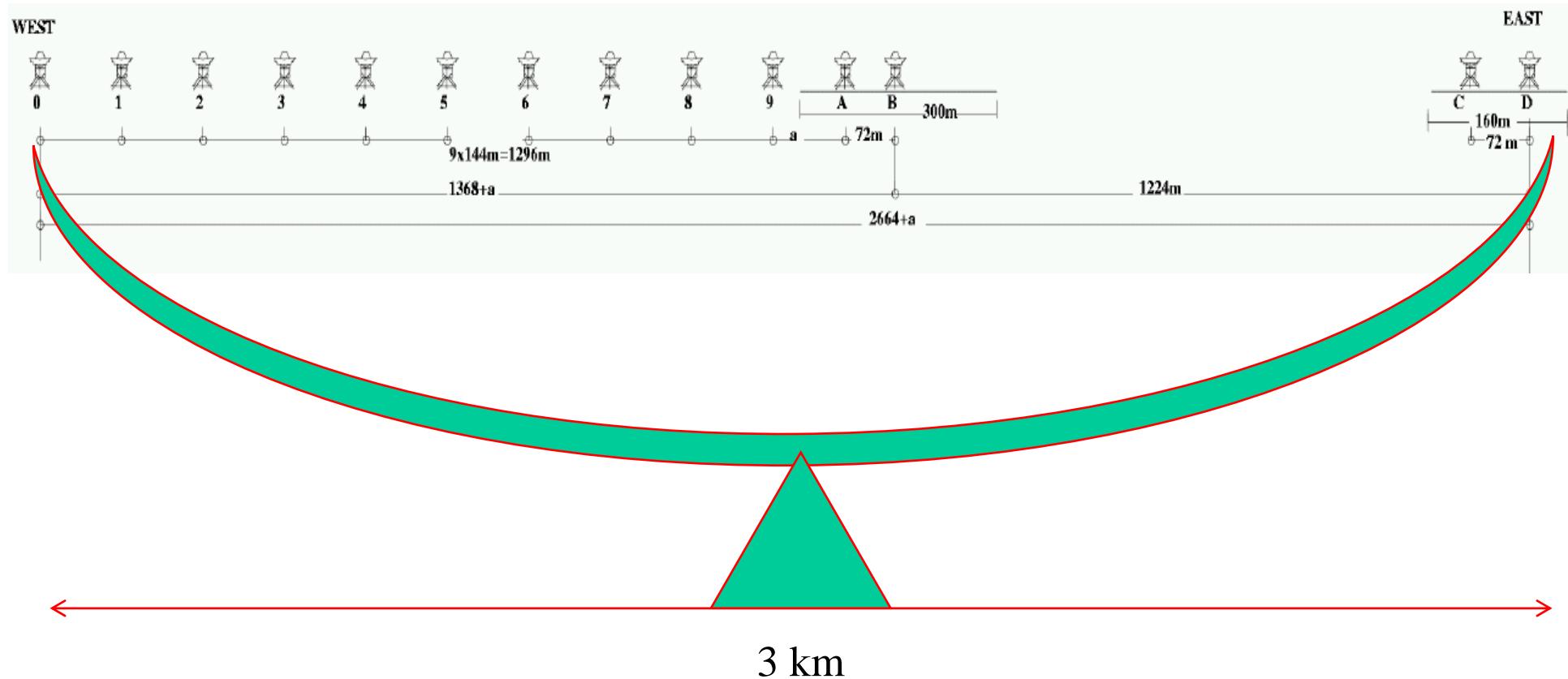
- Radiosterrenwacht
- Technisch laboratorium
- Internationale samenwerking
  - ➥ Joint Institute for VLBI in Europe



- Enabling discovery in astronomy through innovative instrumentation and facilities management.



- Astronomers like:
  - ☞ Better angular resolution
  - ☞ Larger field of view
  - ☞ More sensitivity
  - ☞ Larger instantaneous bandwidth
  - ☞ Observe at multiple frequencies
  
- So, better telescopes ...

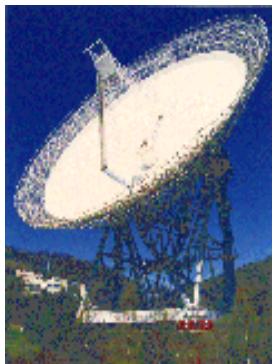


# ASTRON

Westerbork Synthesis Radio Telescope

- Diameter 3 km
- 14 telescopes





Telescopes in Europe and China



Synchronous observations stored on tapes



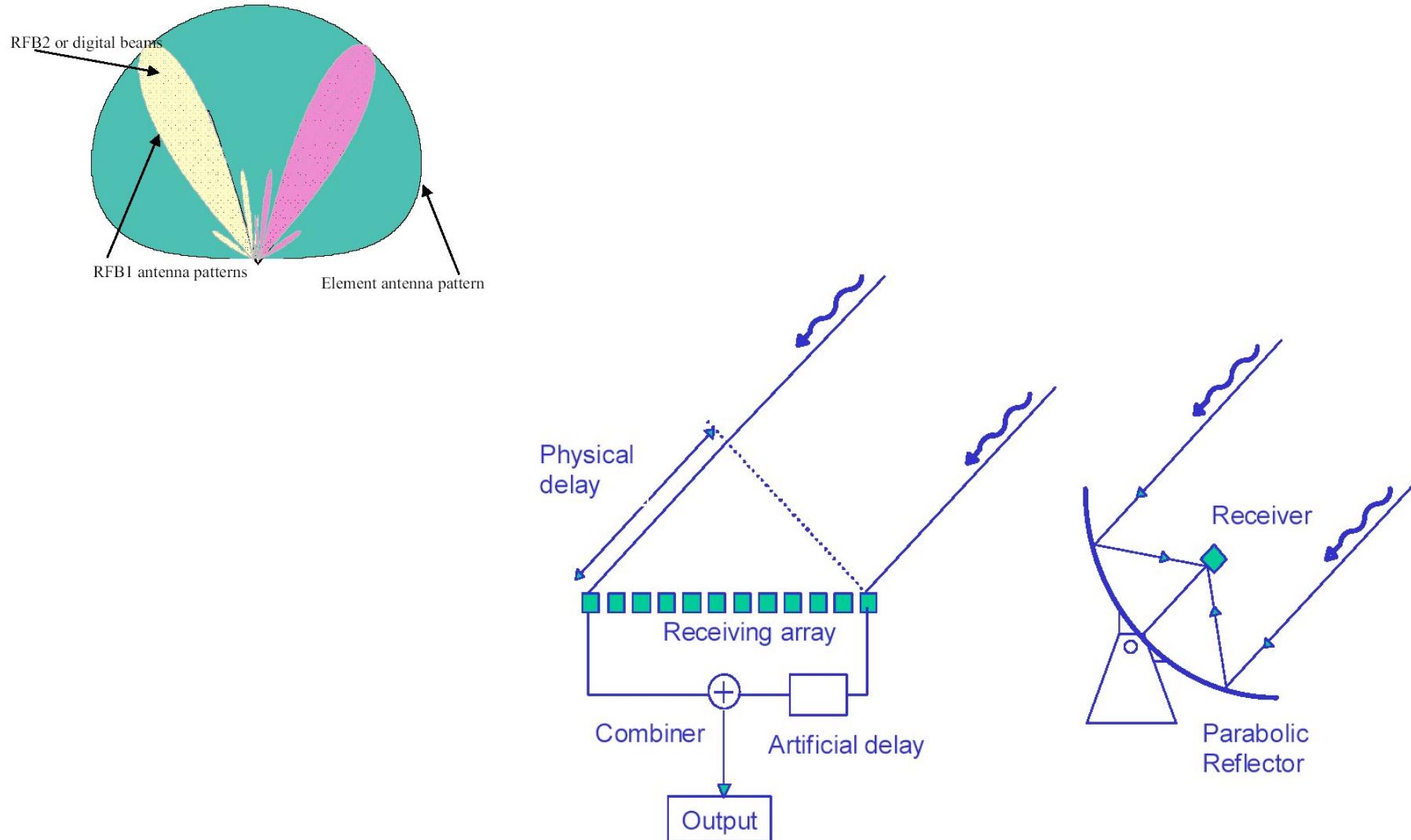
Send to supercomputer in Dwingeloo

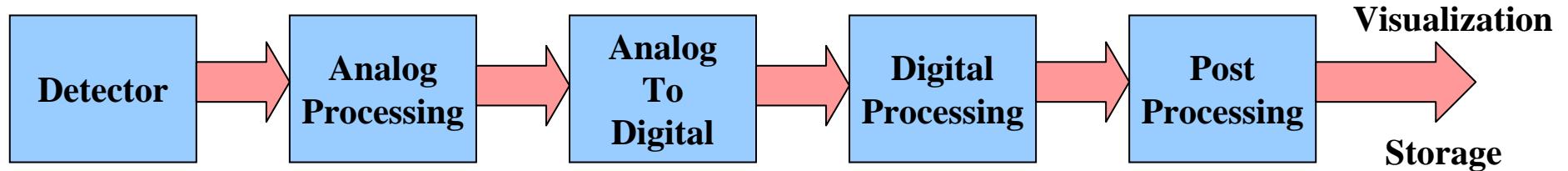


“Telescope” as large as Europe!



- No big dishes anymore:
  - ☞ Many small antennas
    - No maintenance, mass production
  - ☞ Electronically pointing
    - Multiple instantaneous beams
    - Possibility to suppress RFI
- Now is the time:
  - ☞ Computers cheap
  - ☞ Broadband data networks available

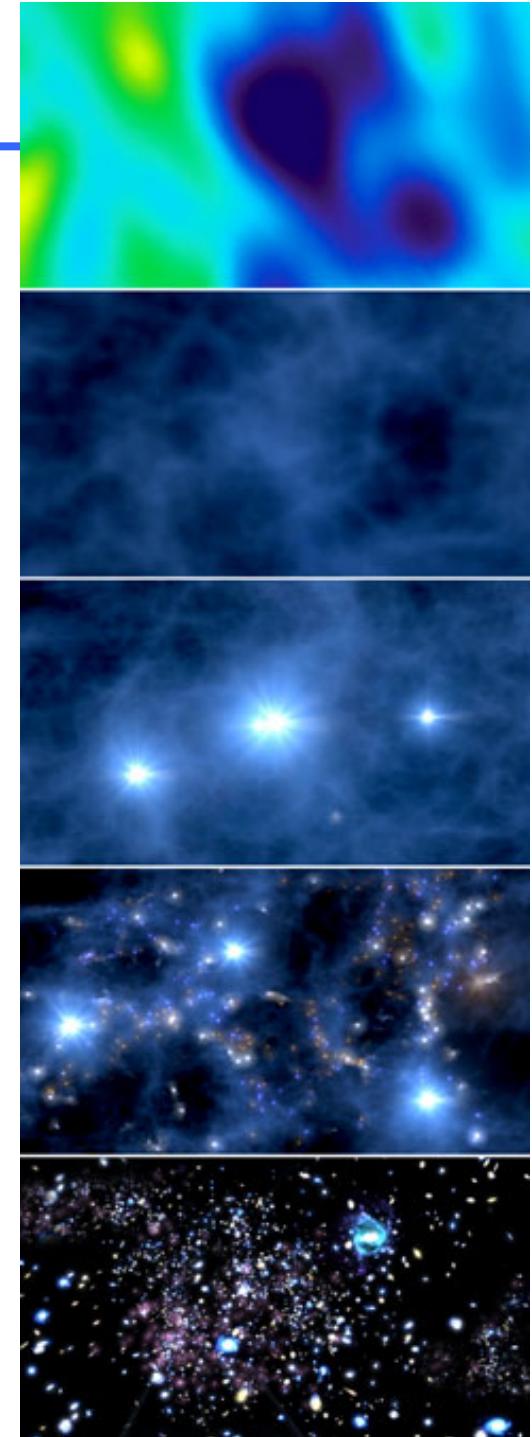
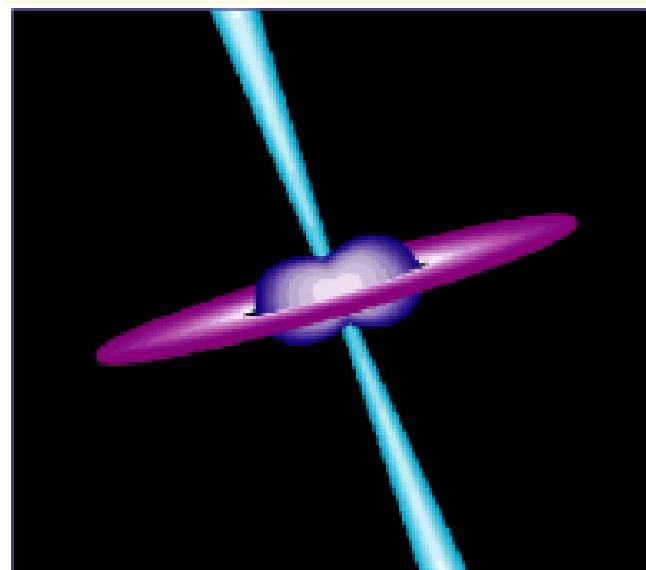
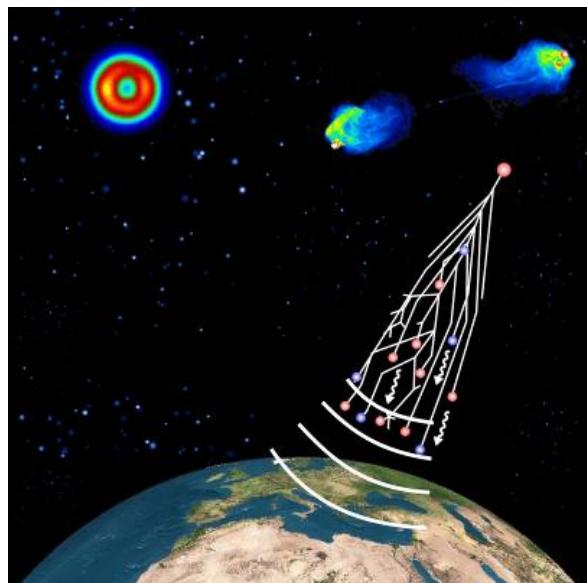




➤ Trend:

☞ Digital hardware moves to more to the antenna

- Epoch of Reionization
- Extragalactic Surveys
- Transients and Pulsars
- Cosmic Rays
- And new discoveries ...

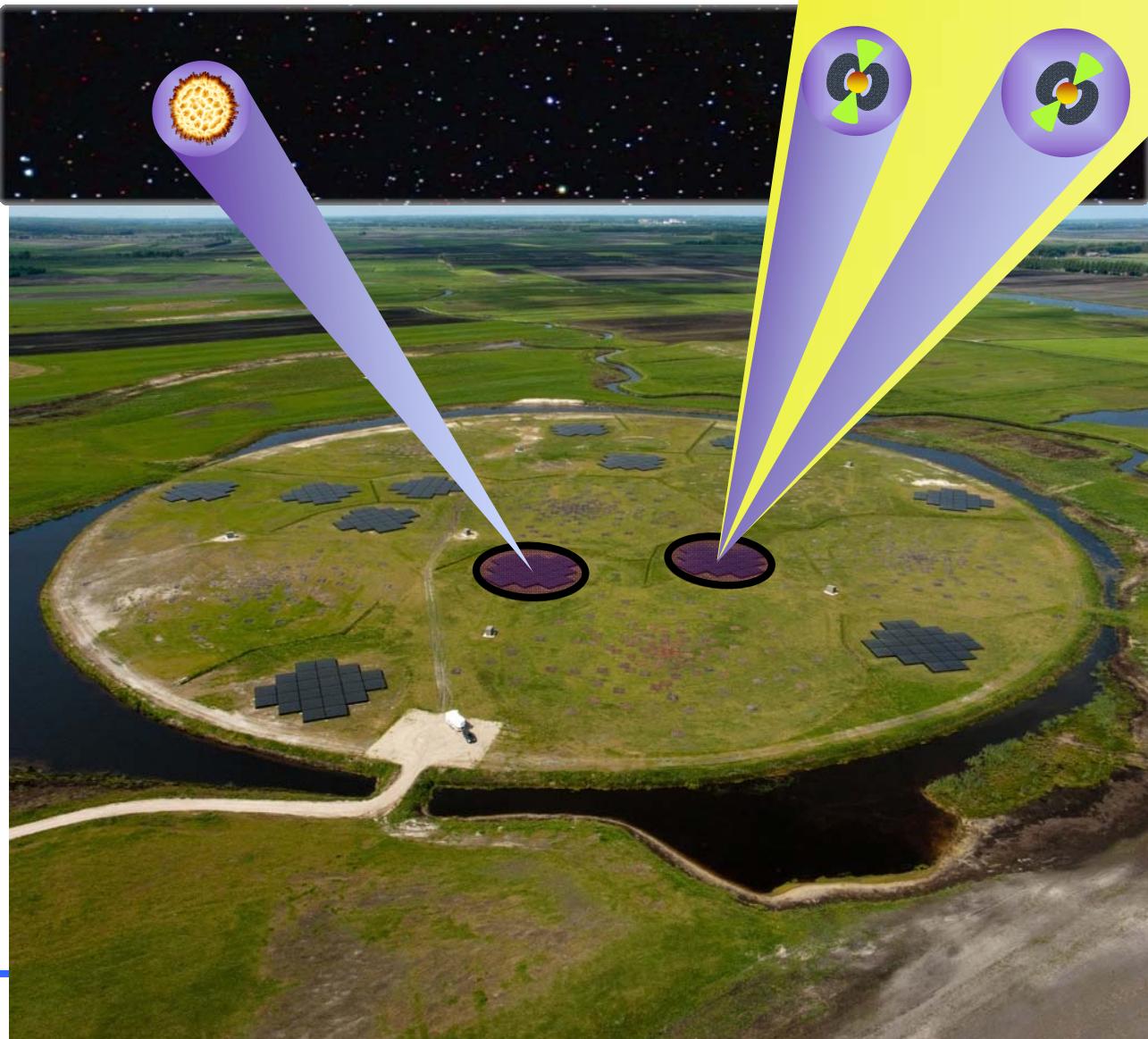


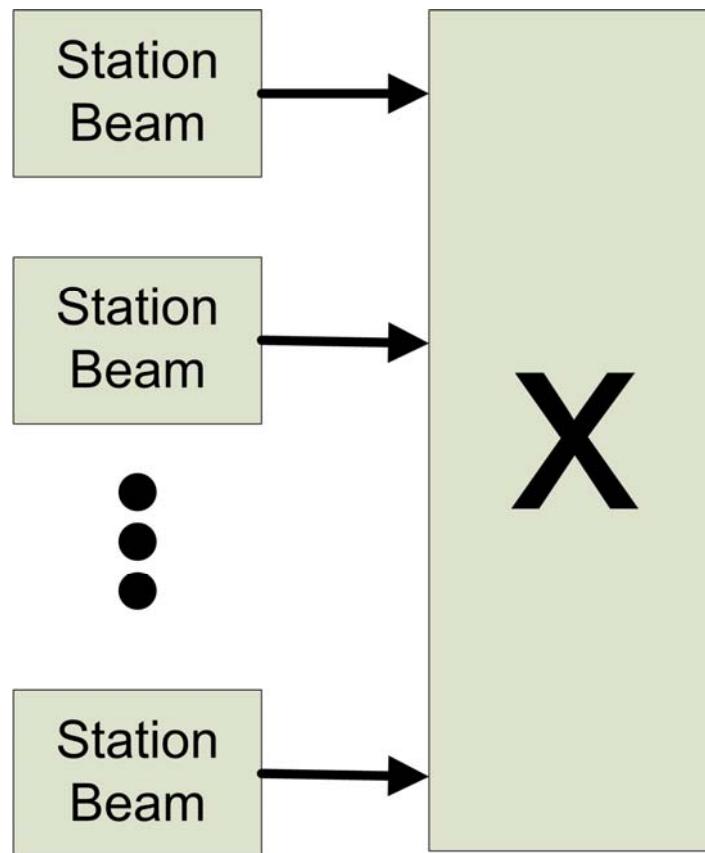


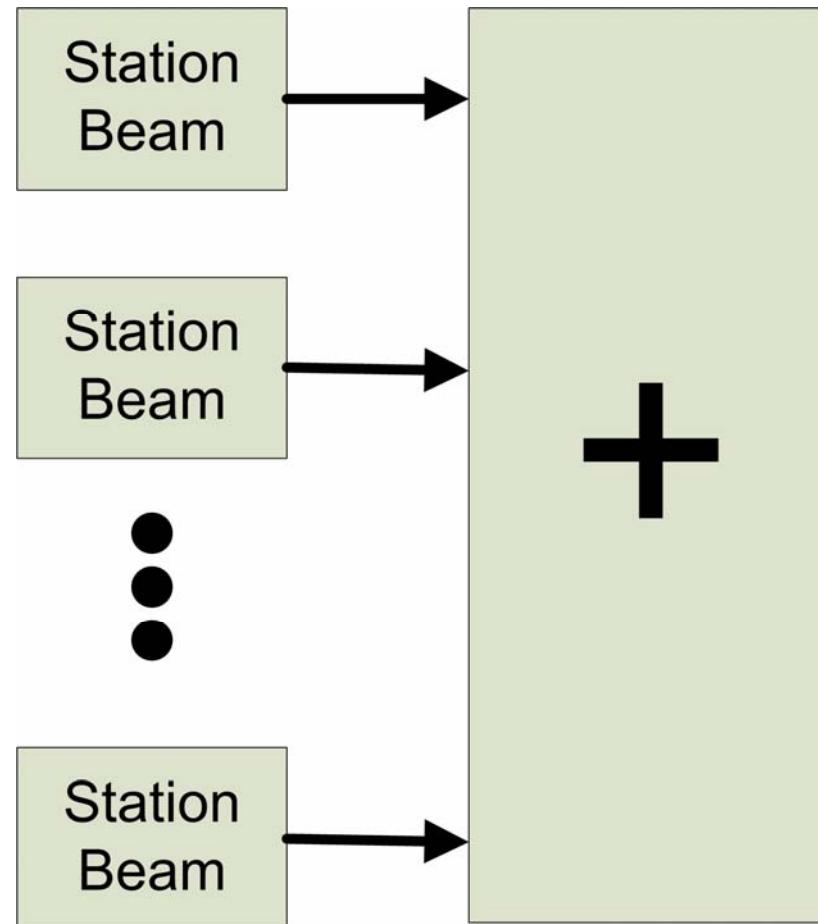


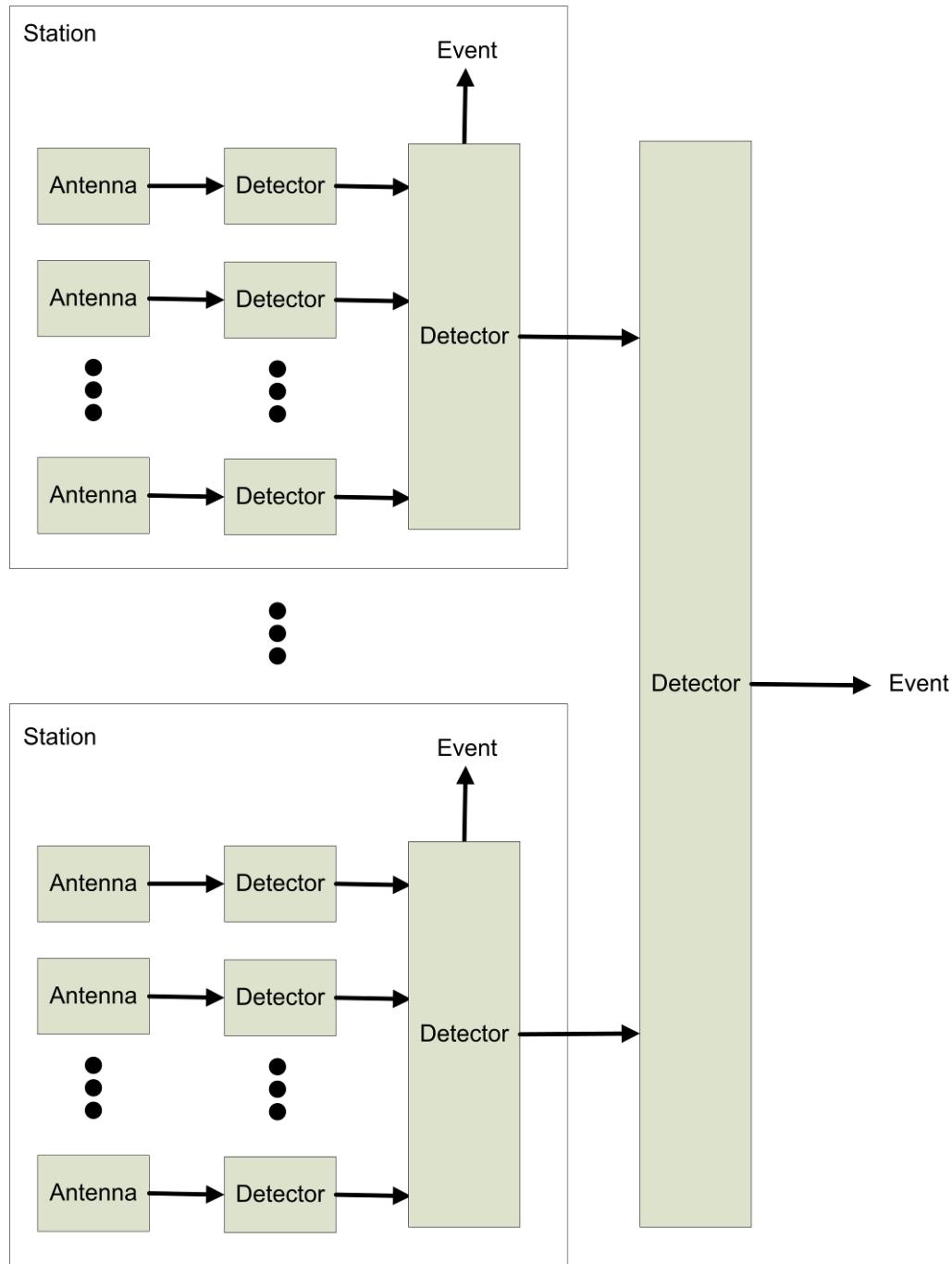


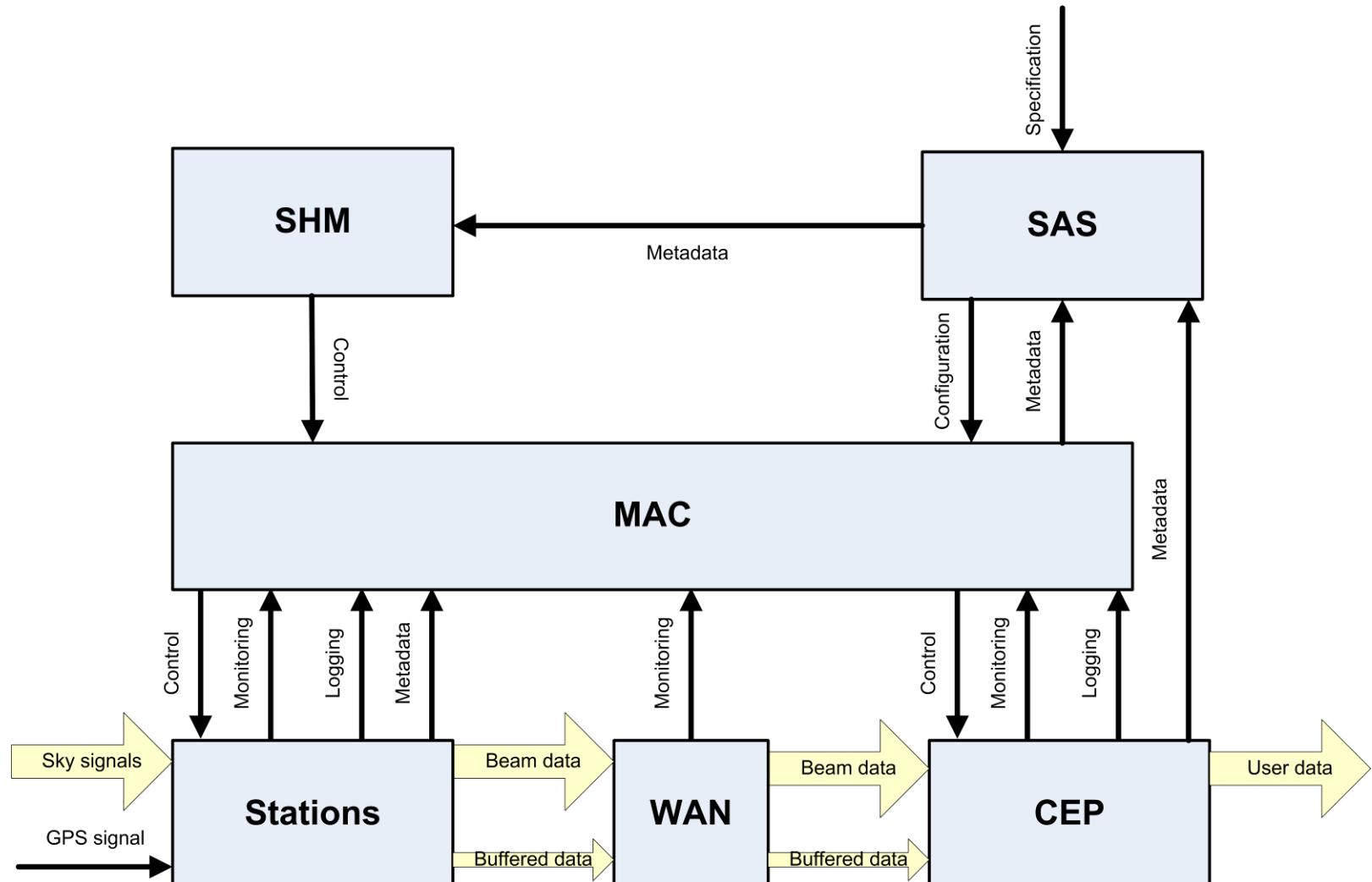


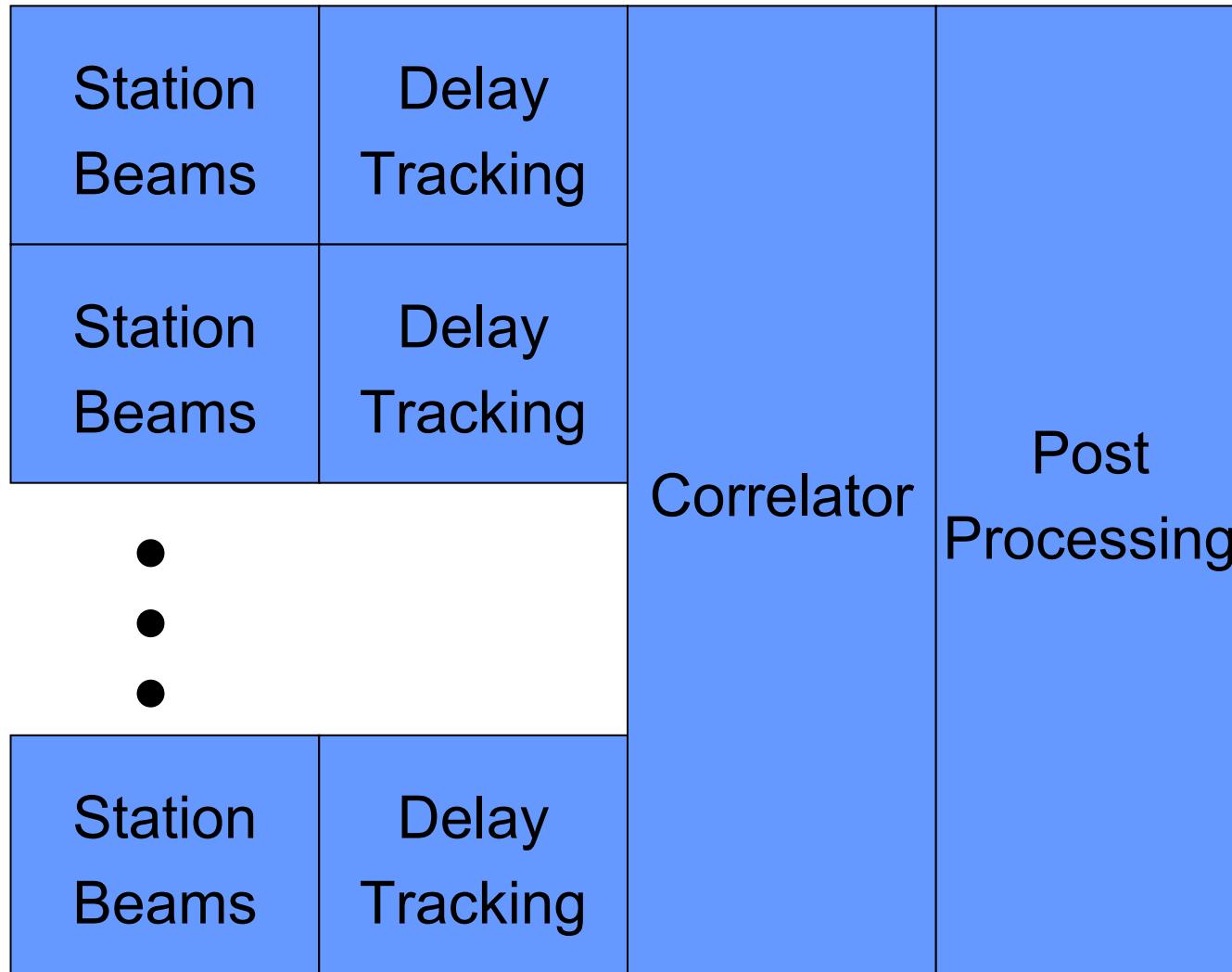


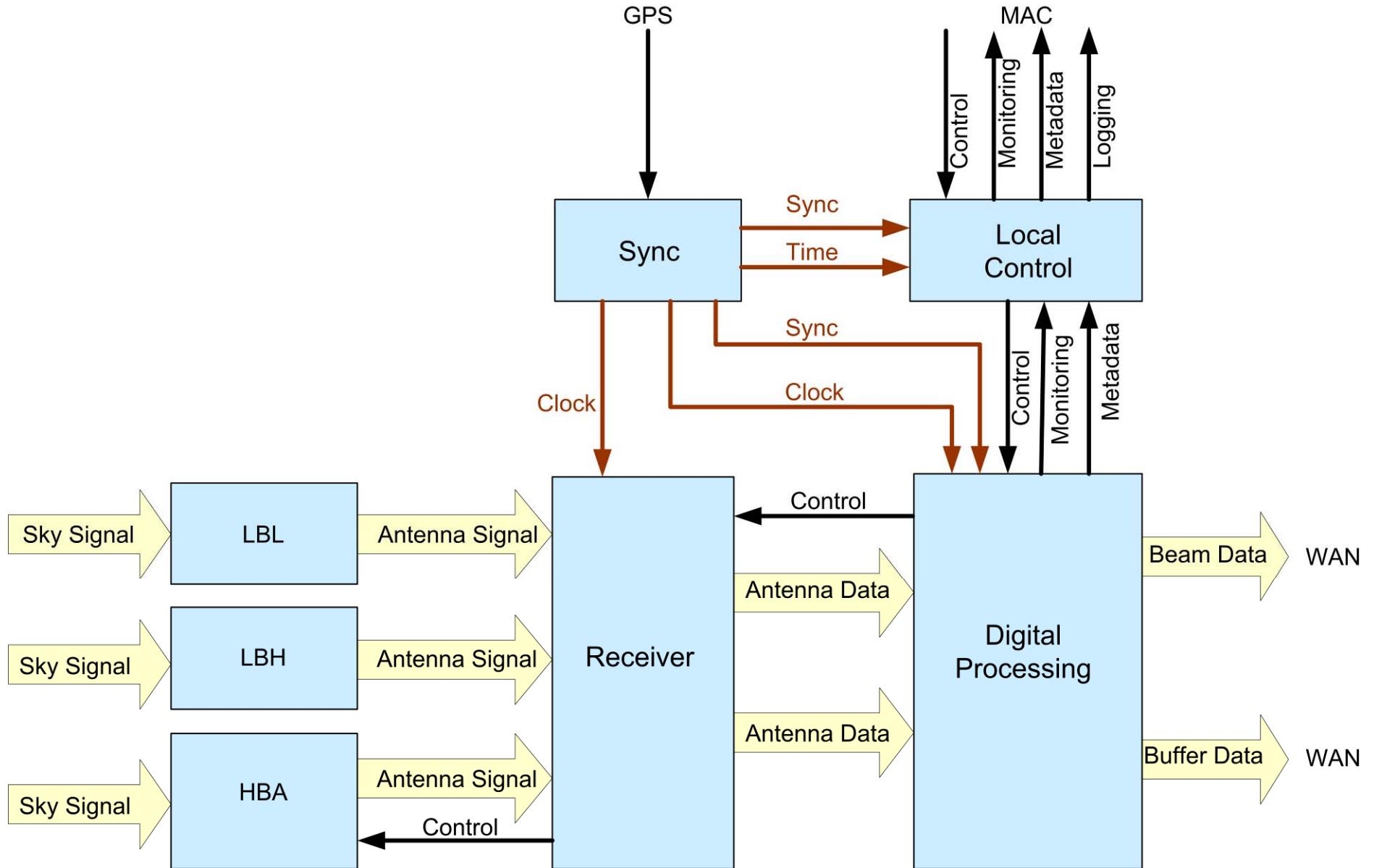


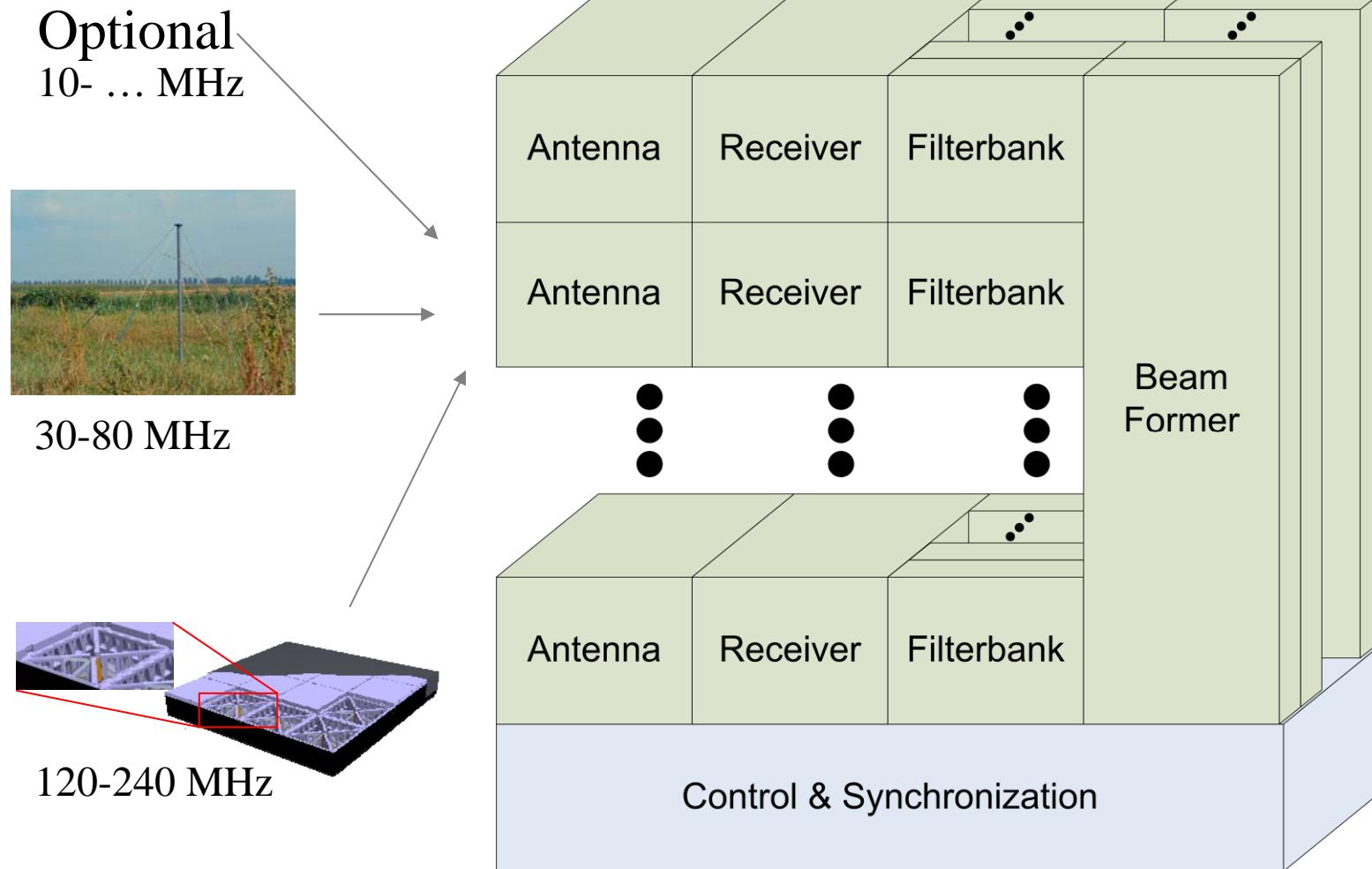


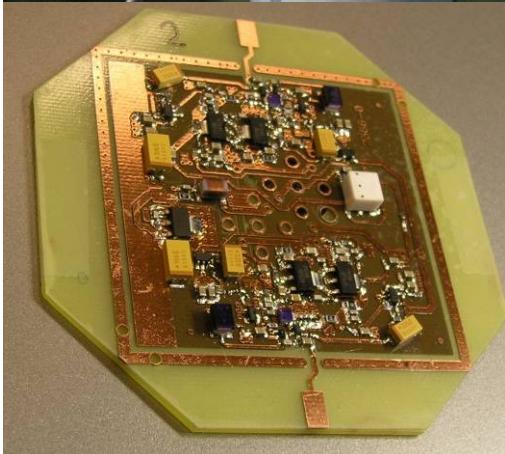


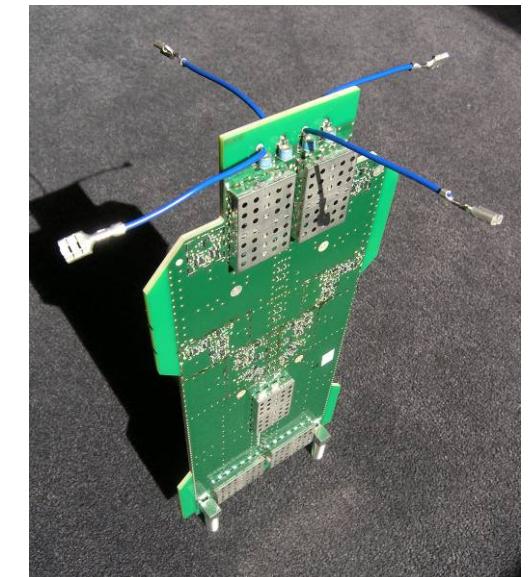
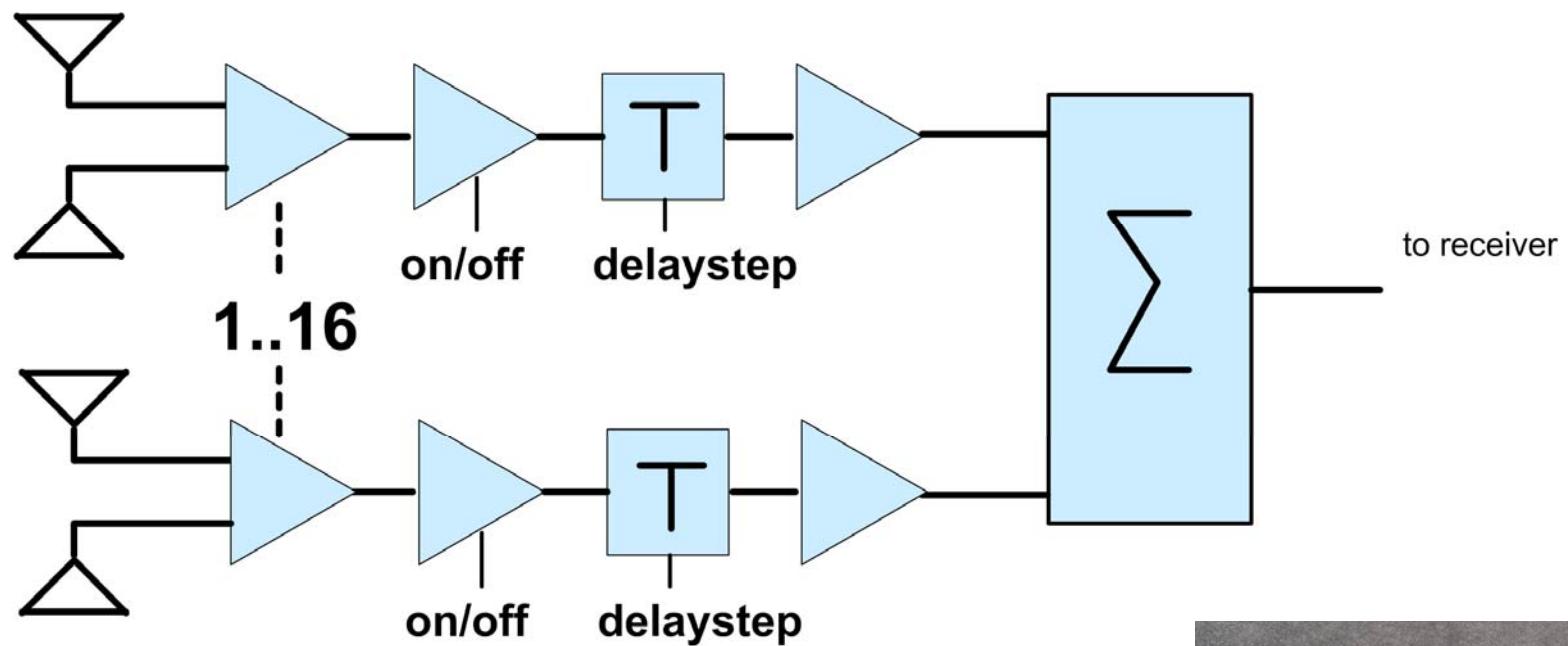












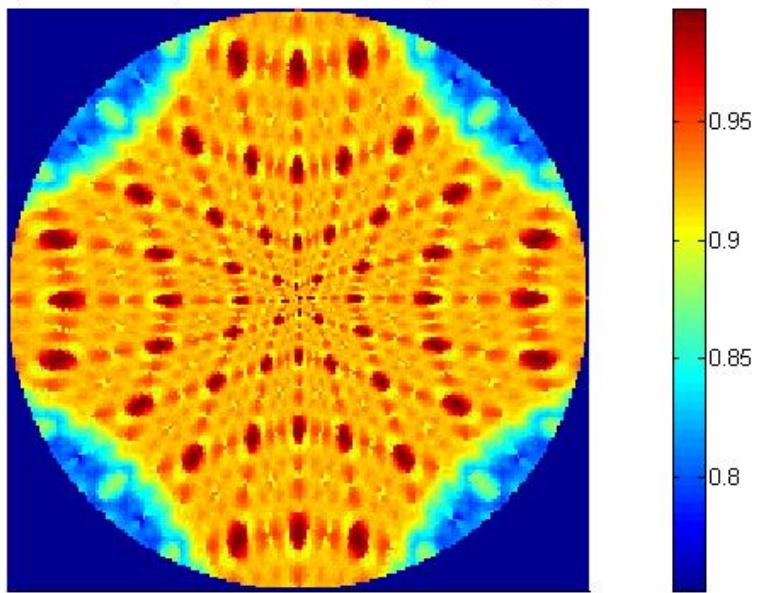
- Delay necessary for 1.25m and regular tile:

$$3 \times 1.25 \times \sqrt{2} = 5.3 \text{ m} \rightarrow 17.6 \text{ ns}$$

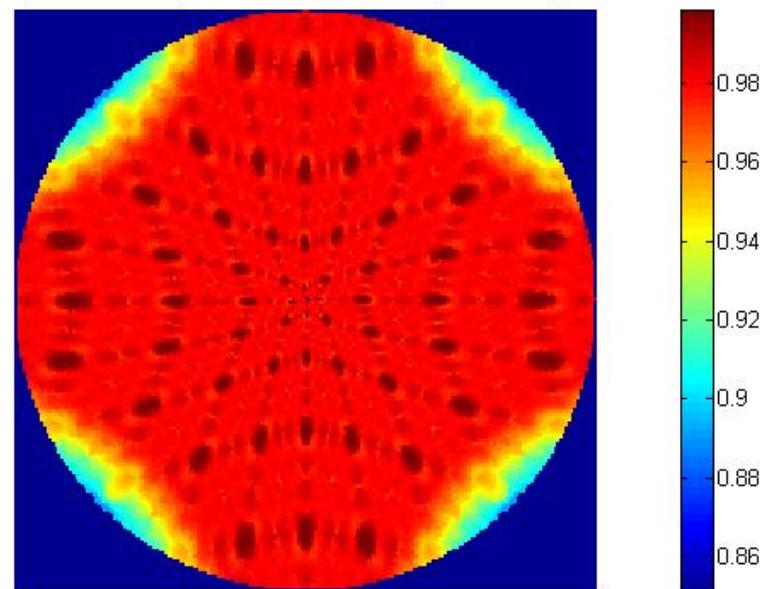
- 85% of that is 15.03 ns

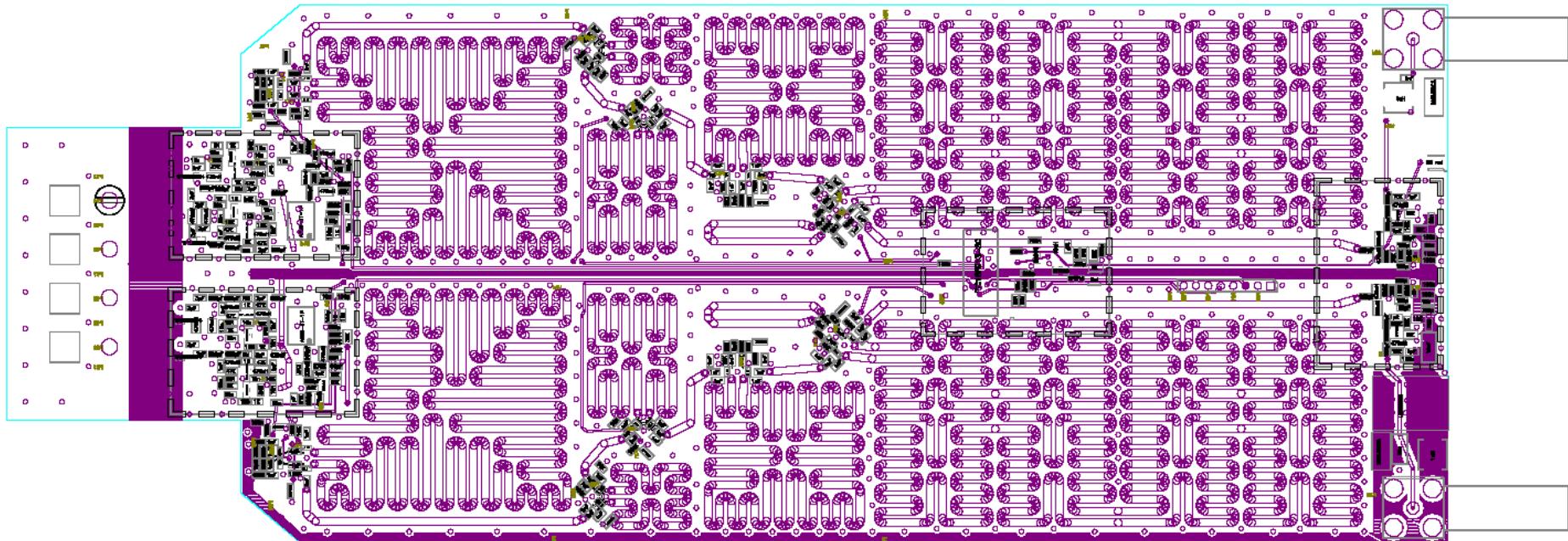
bit	delay (ns)
0	0.47
1	0.94
2	1.88
3	3.76
4	7.51
Total	15.03

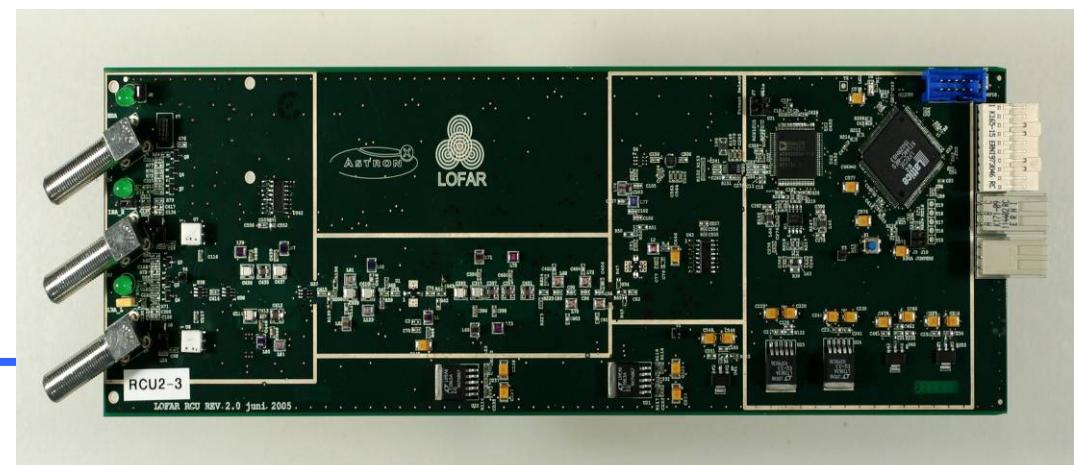
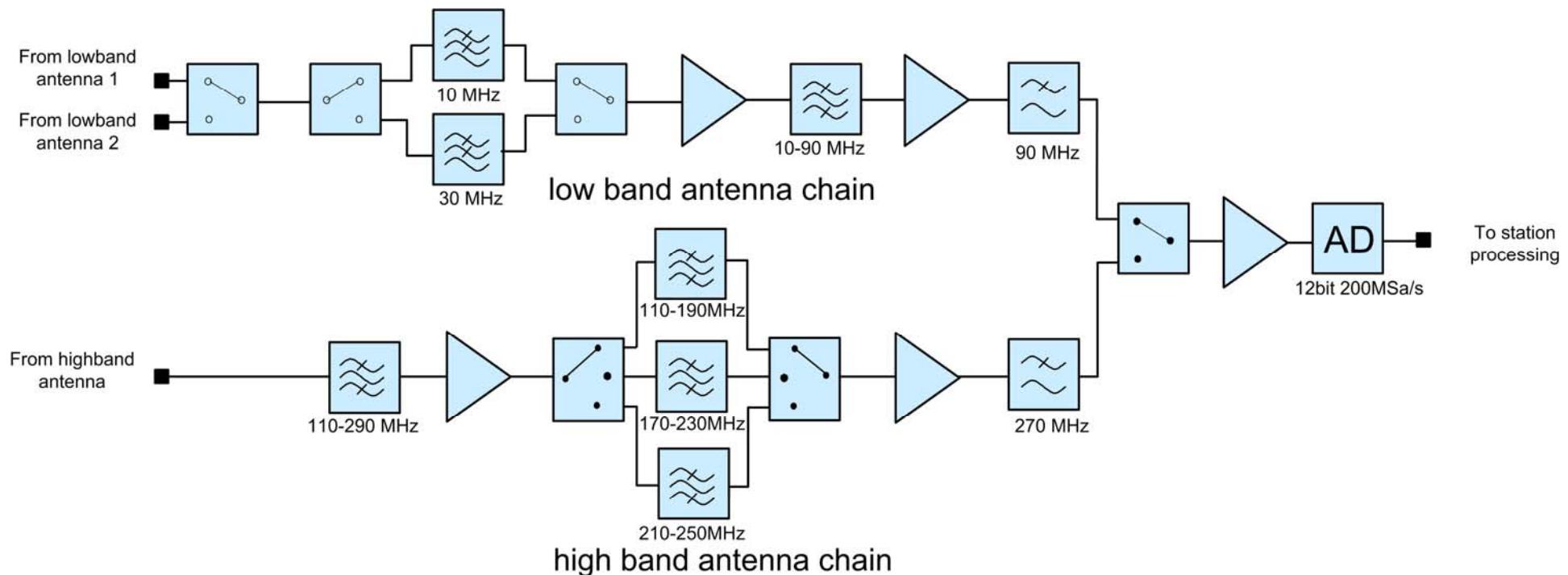
Efficiency for a time delay of 85% the maximal length for 5 bit @ 240 MHz

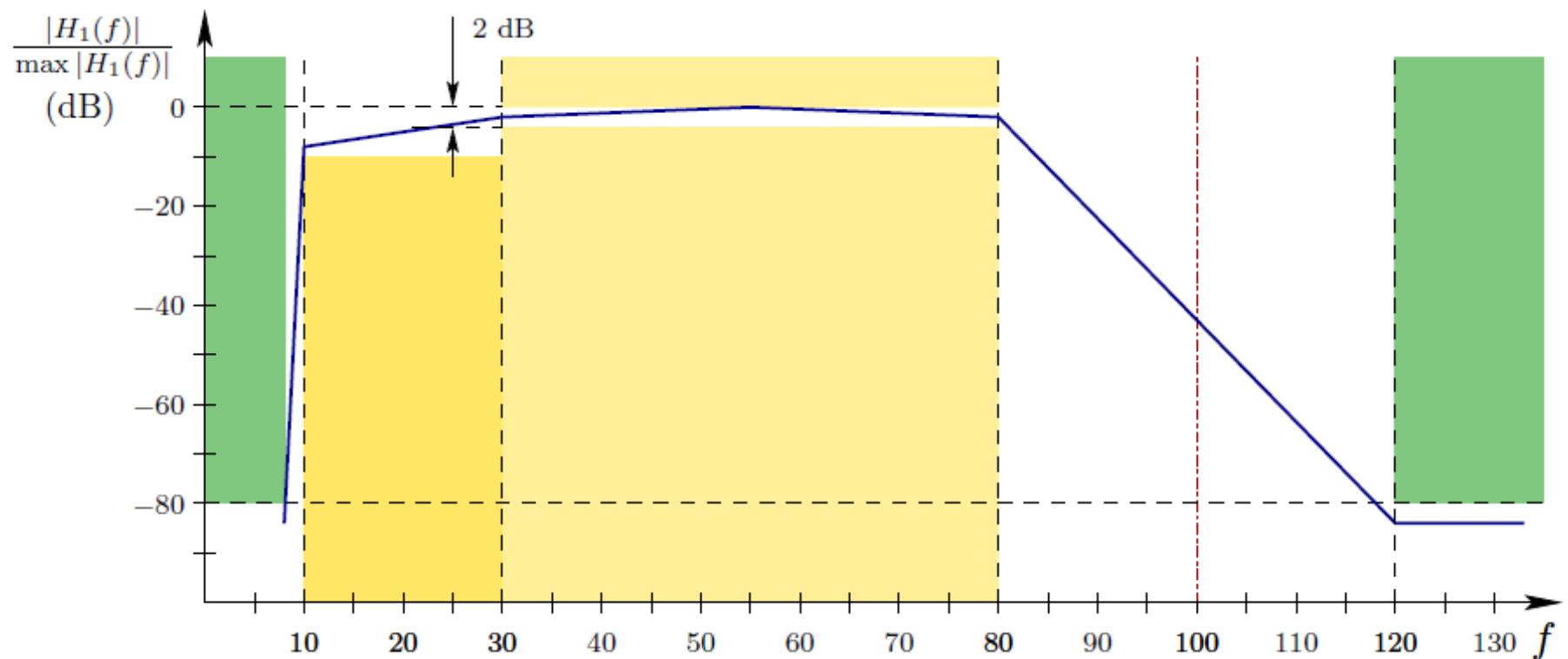


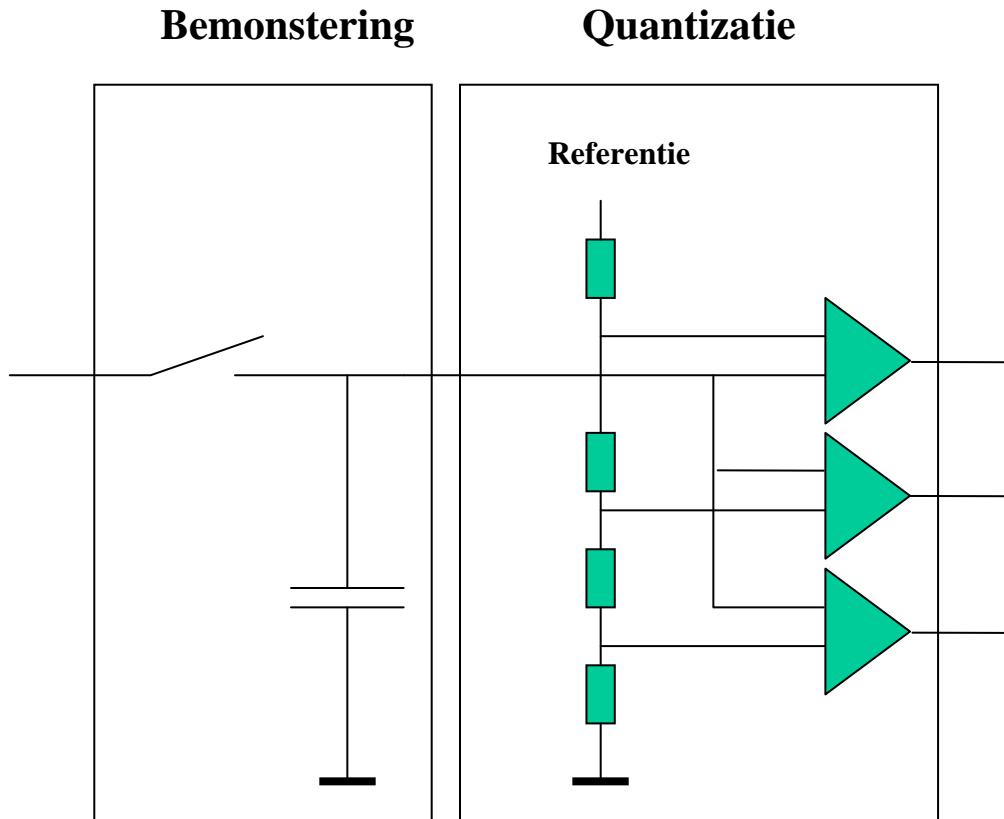
Efficiency for a time delay of 85% the maximal length for 5 bit @ 120 MHz



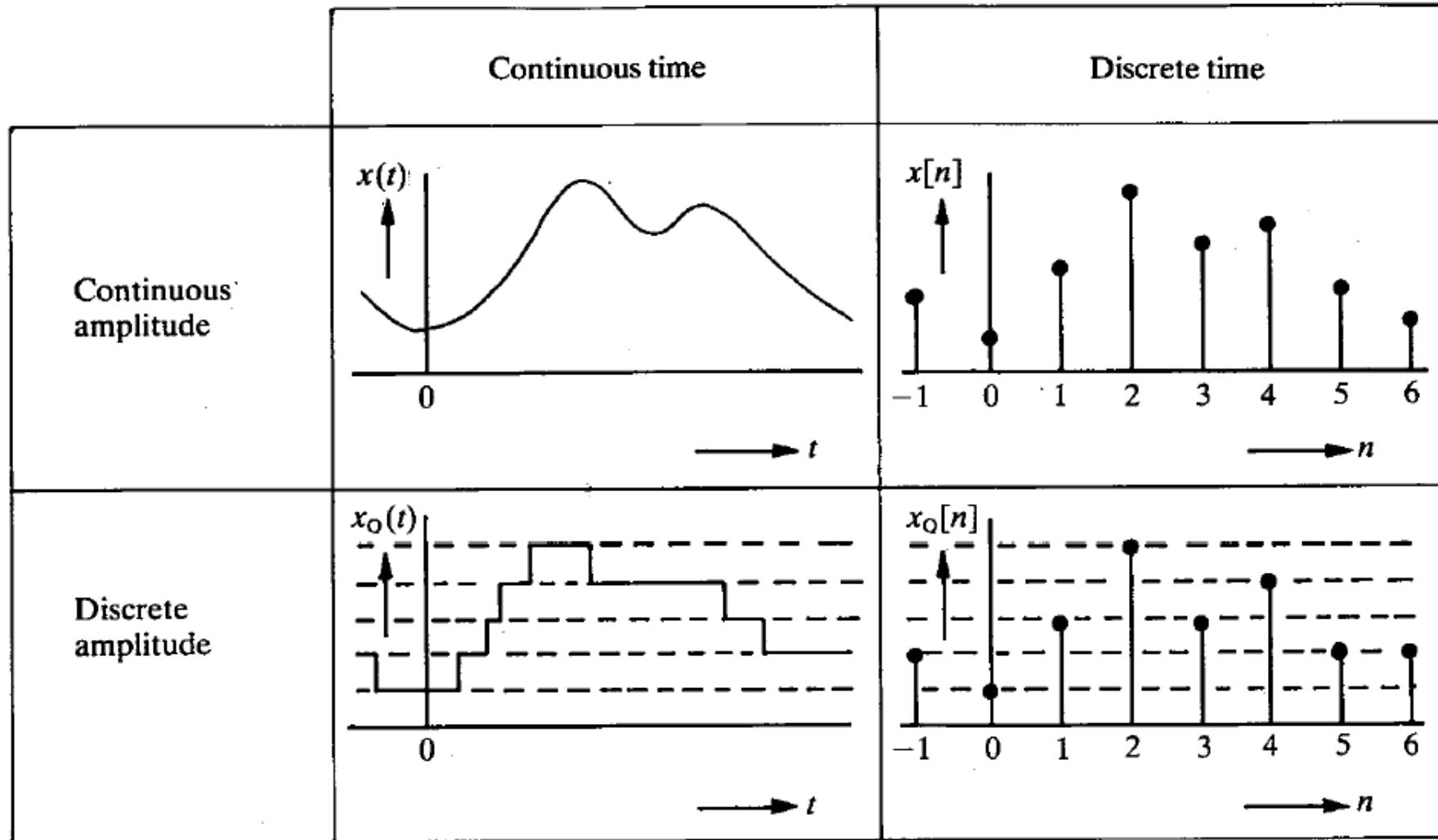


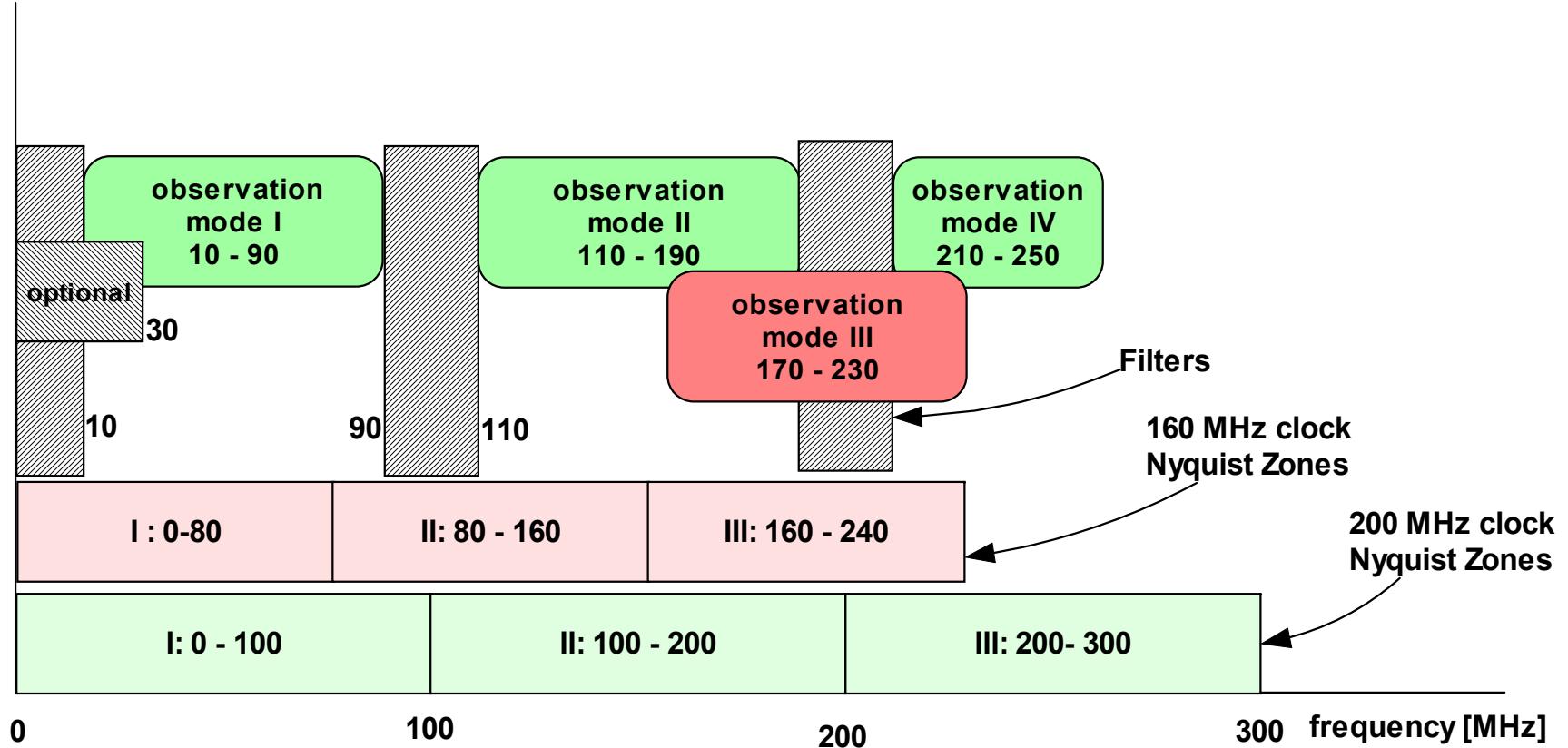


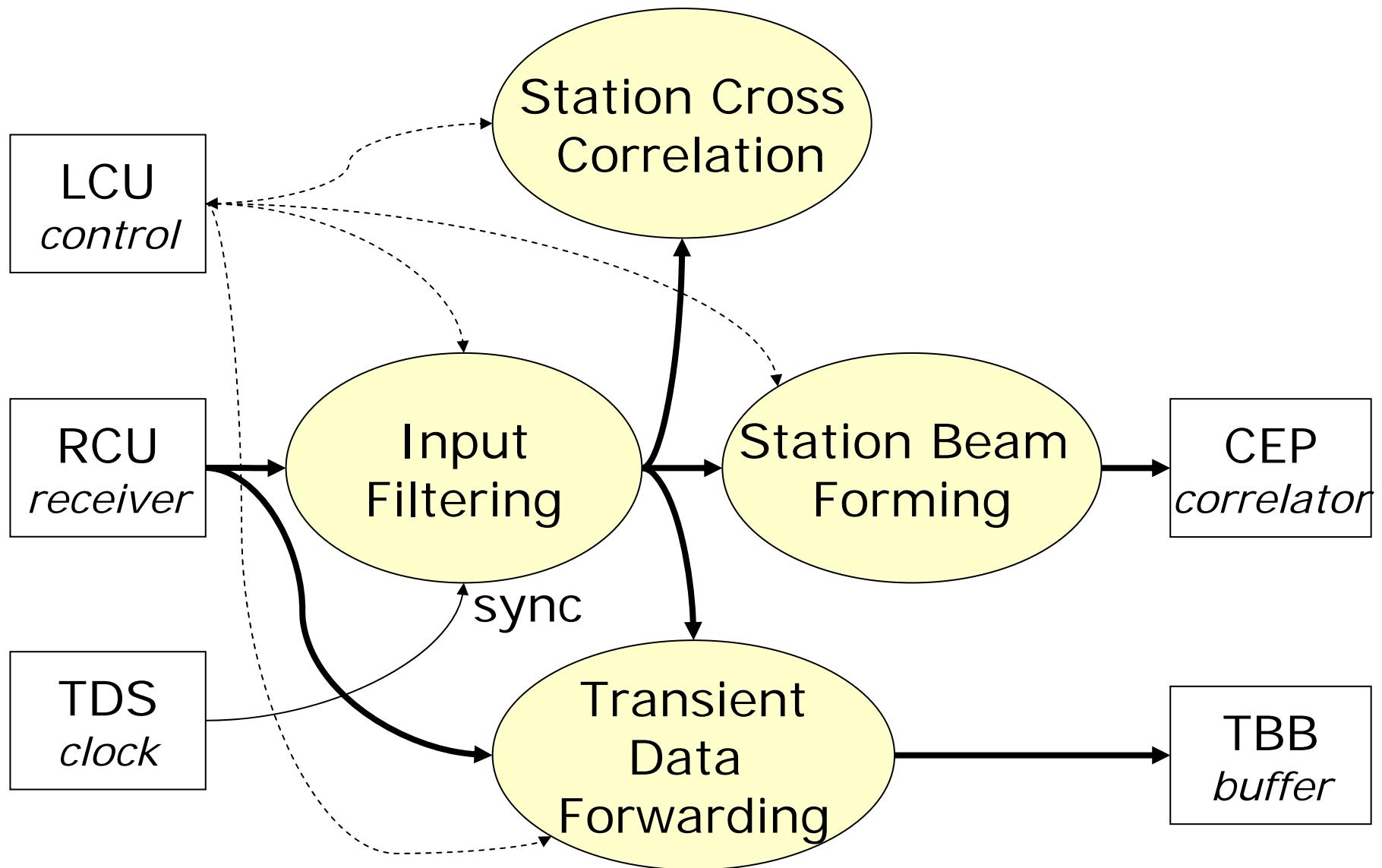




- Number of bits: 12
- Sampling frequency: 200 MHz / 160 MHz

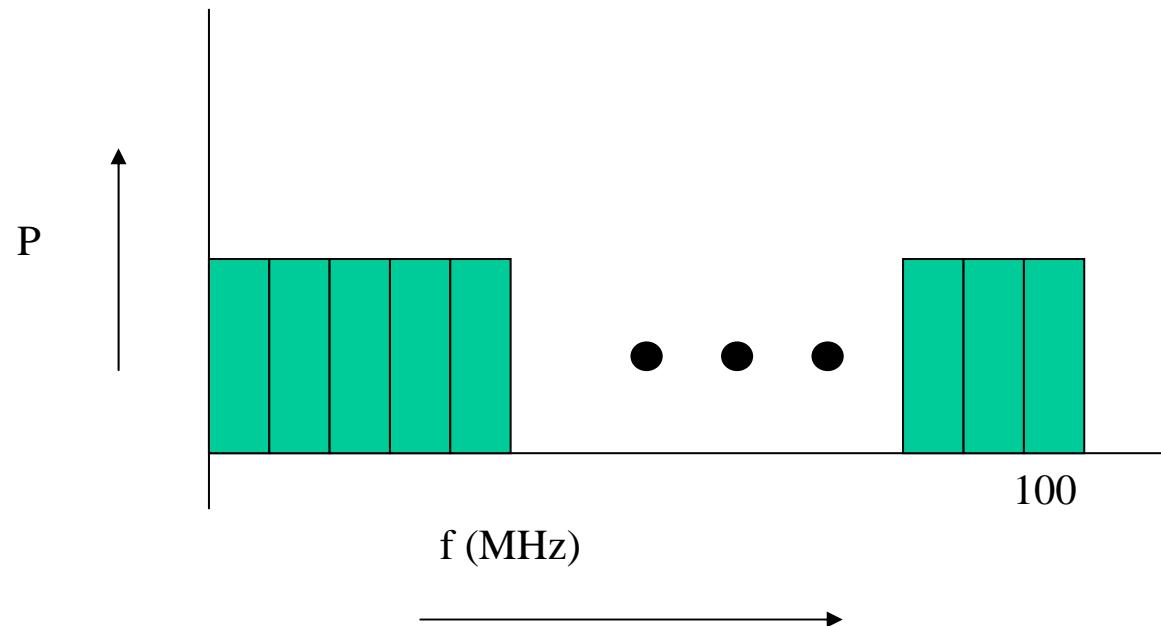




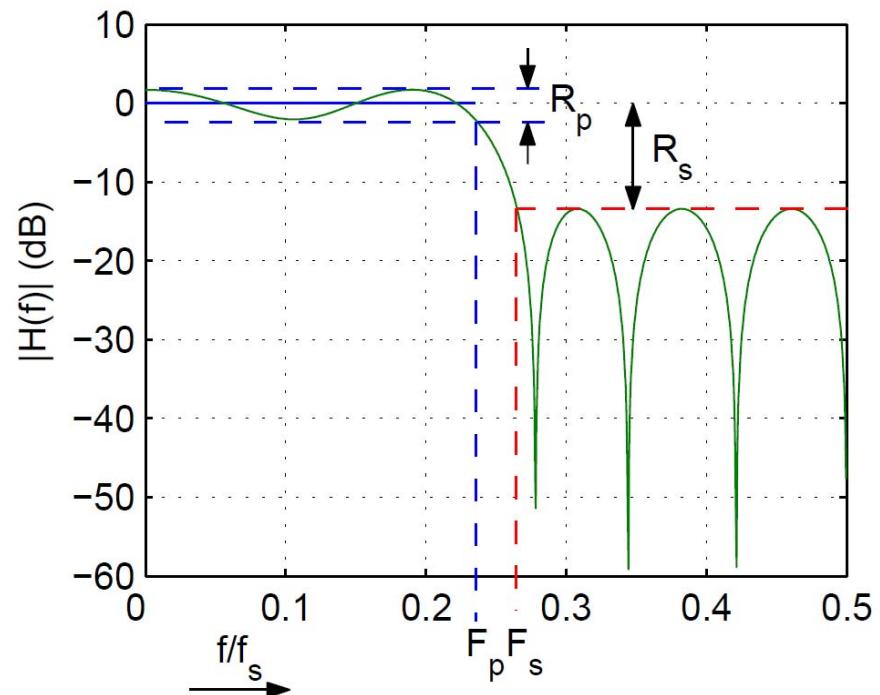


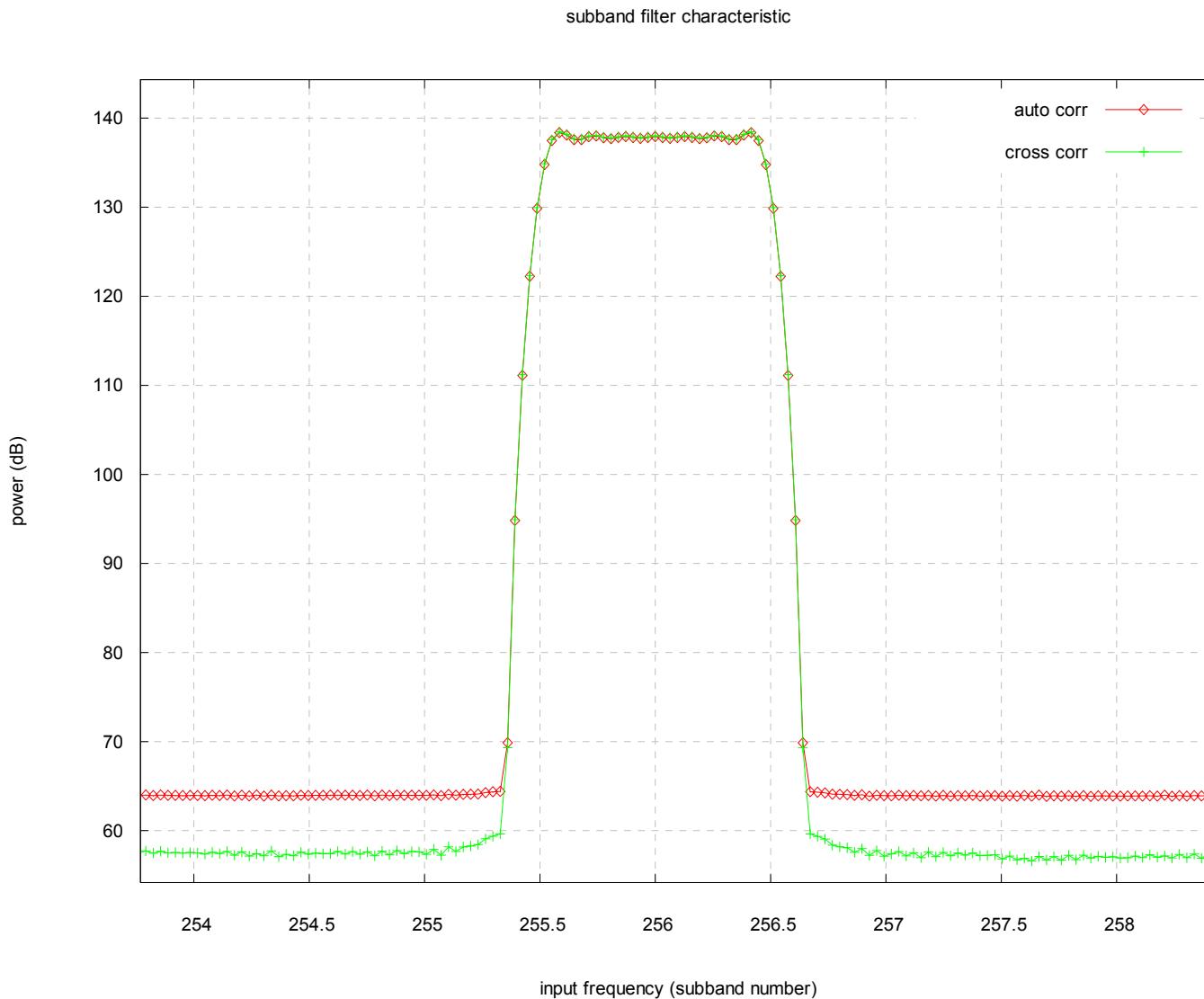


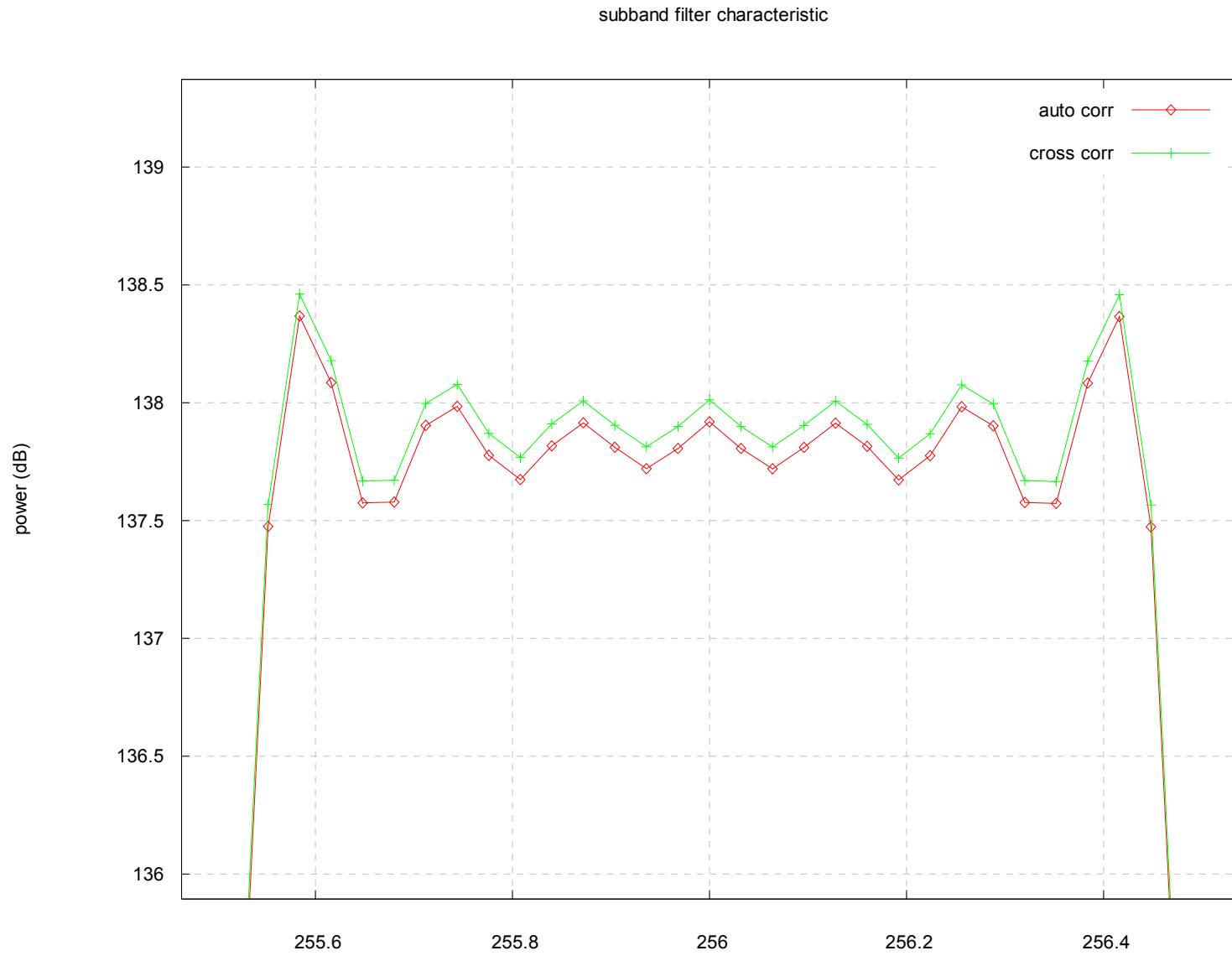
- Sample rate max. 200 MHz
- Number of taps: 4096
- Number of subbands: 512

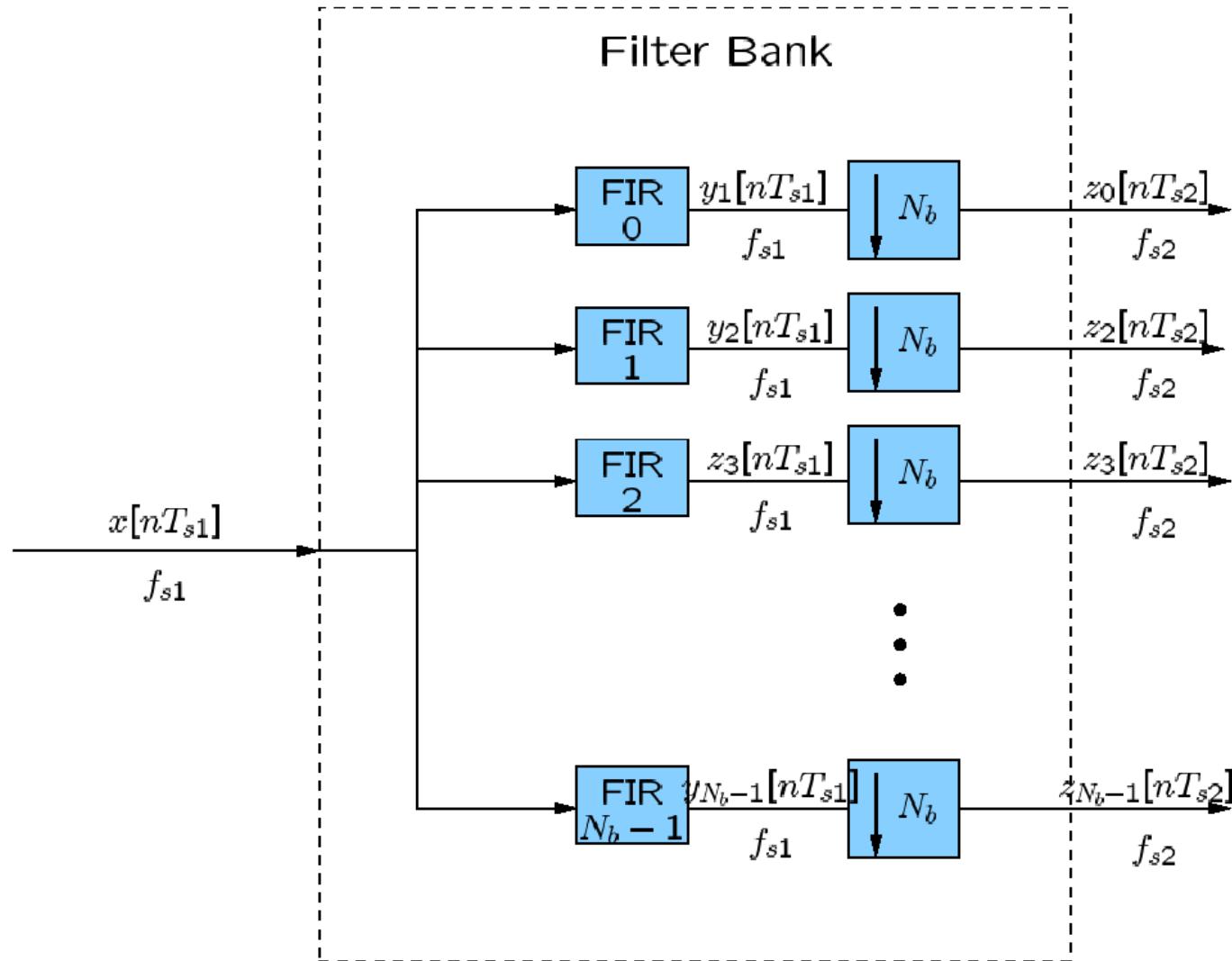


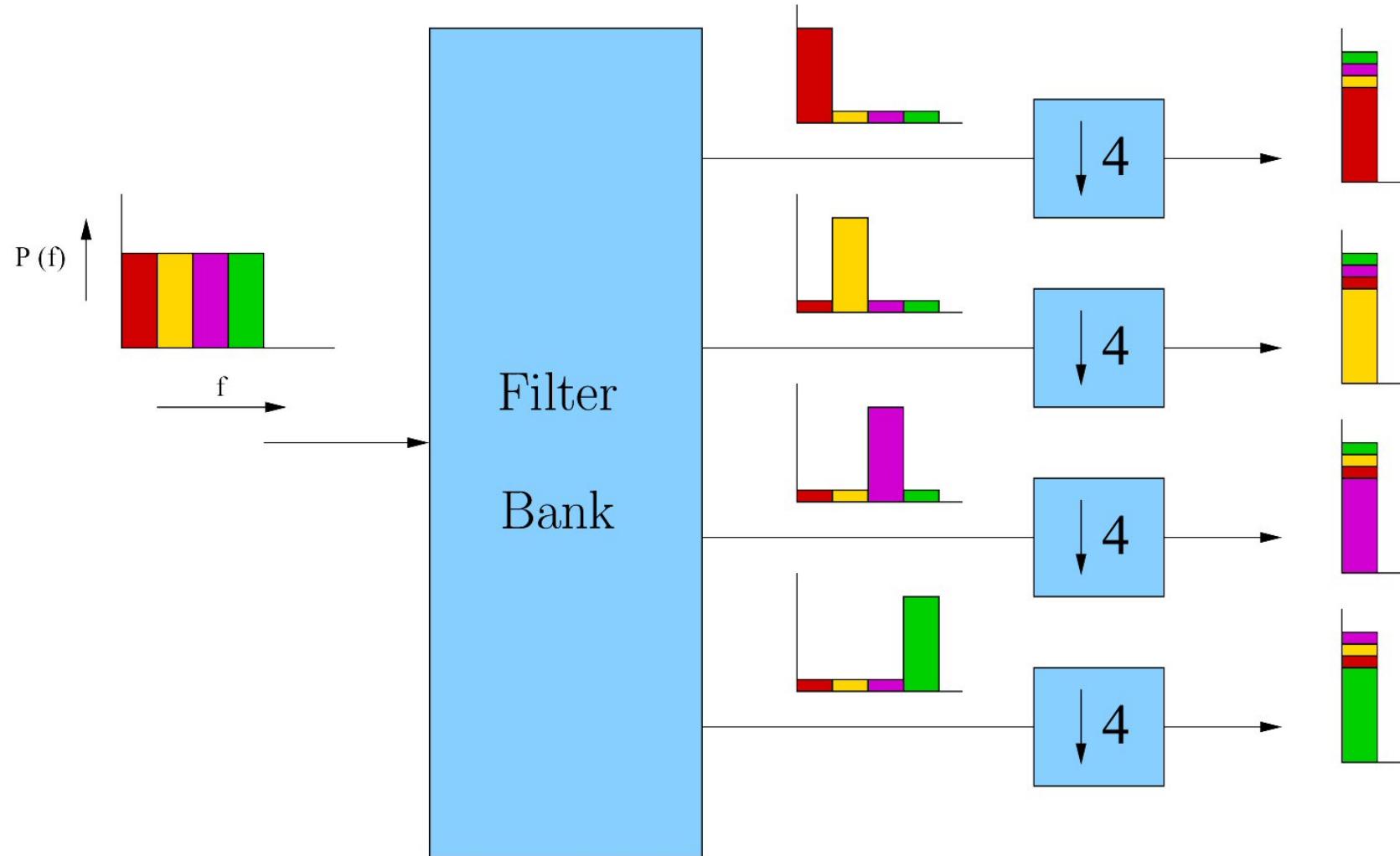
- Stopband attenuation: 80 dB
- Ripple: 0.45 dB
- Transition region: 0.2 subband



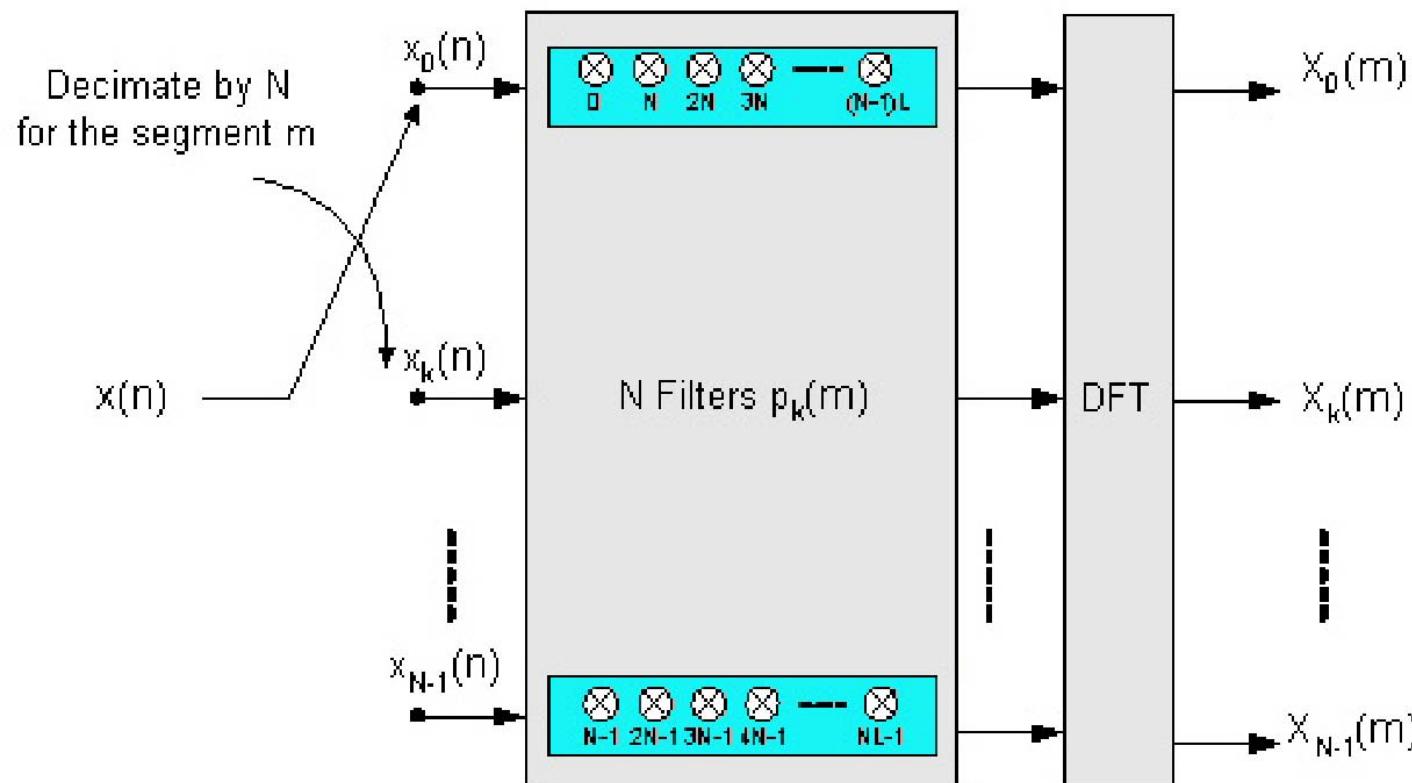




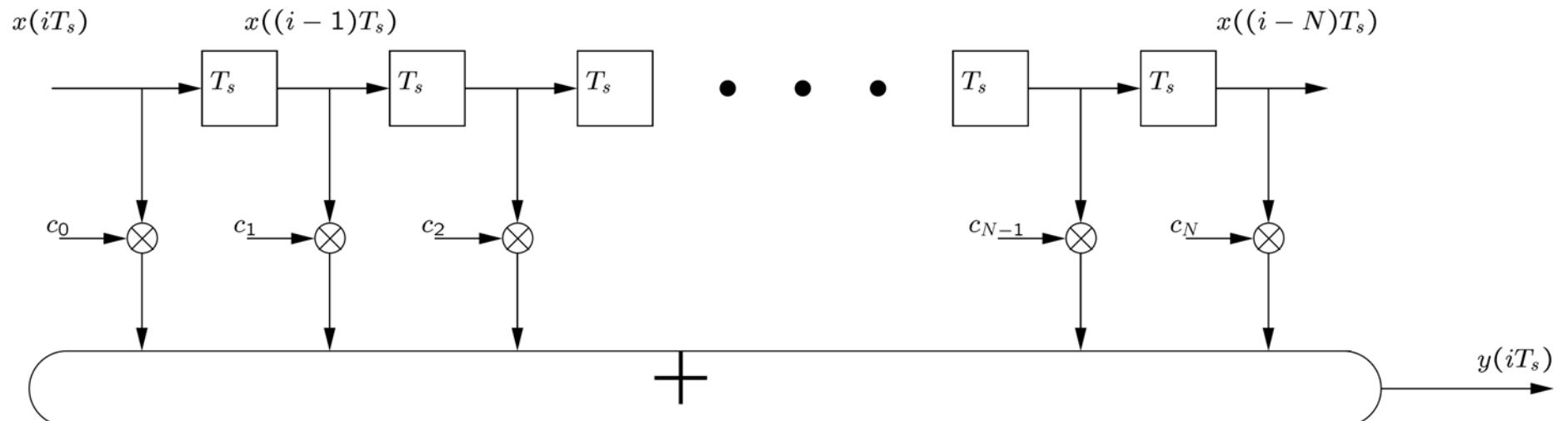




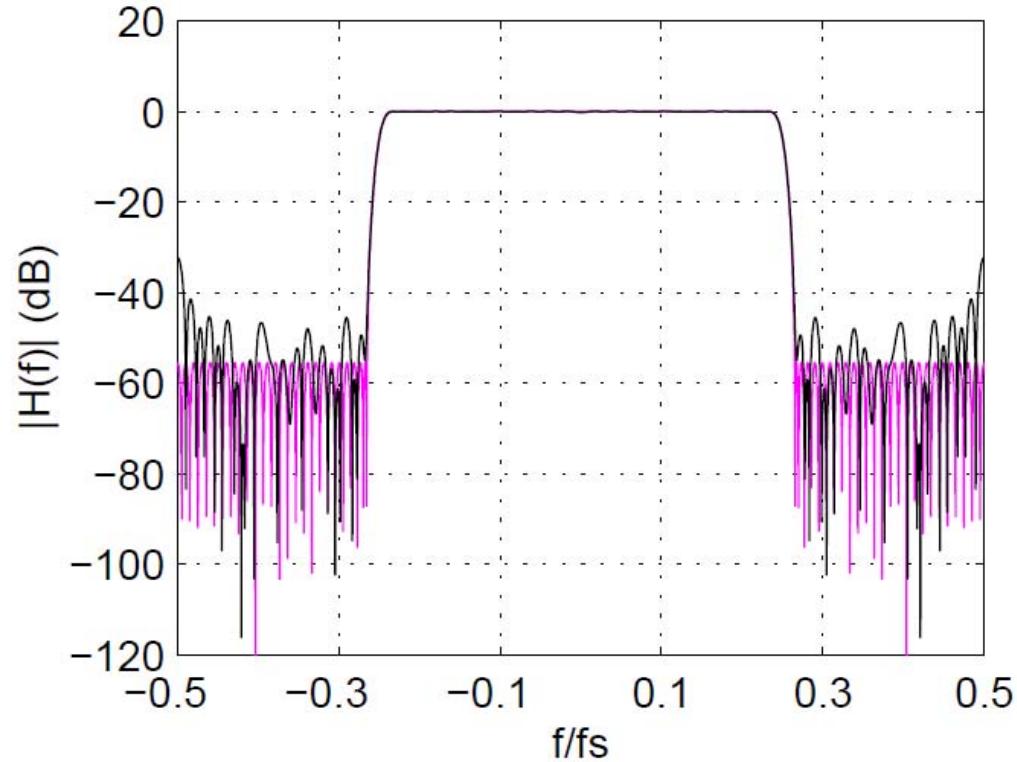
- Why multiplying signals, which are thrown away
- Number of required resources much smaller



$$y(iT_s) = \sum_{j=0}^N x((i-j)T_s)c_j$$



$$y(iT_s) = Q_e \left\{ \sum_{j=0}^N Q_b [Q_s(x((i-j)T_s)) Q_c(c_j)] \right\}$$



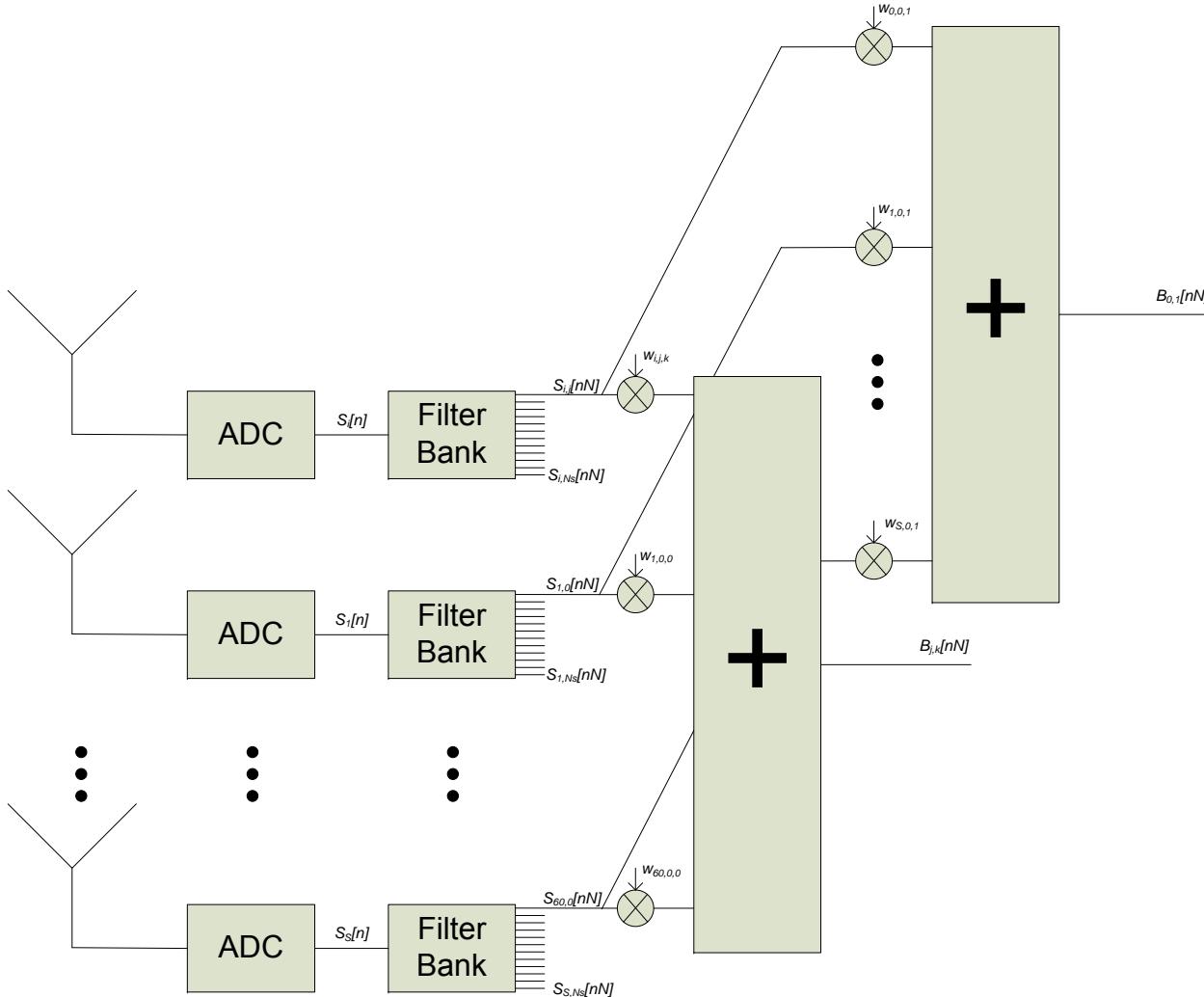
- Small amount of operations on a large amount of data
- Not much chips required (< 10 k chips)

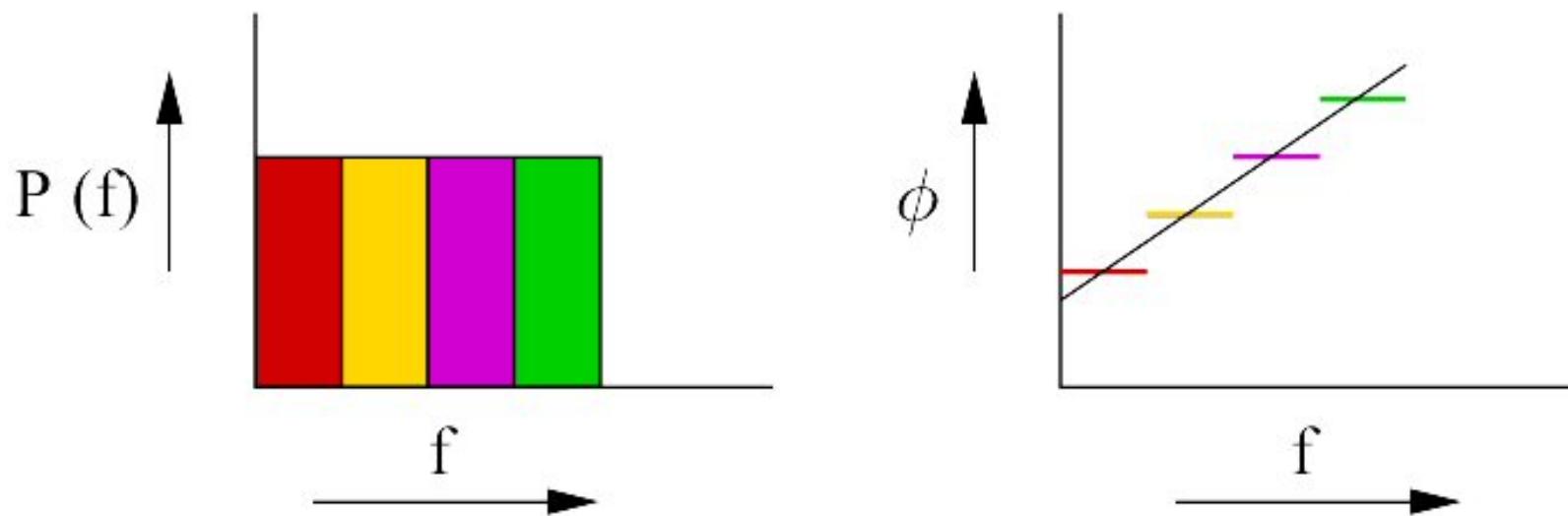
Therefore FPGA's are used to implement the digital functionality

Current trend in FPGA land is to embed:

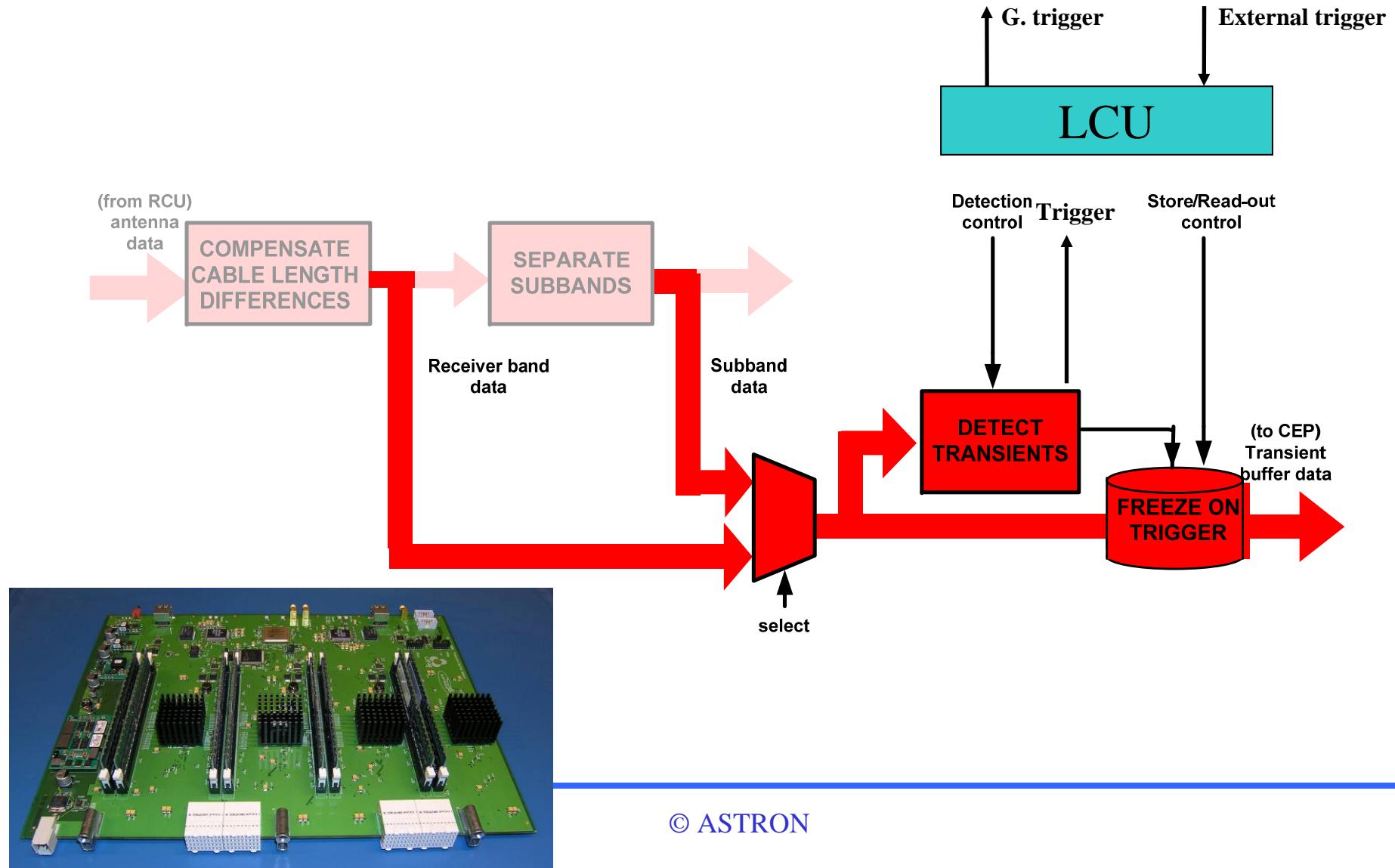
- Multipliers
- Memory
- On chip microprocessor cores

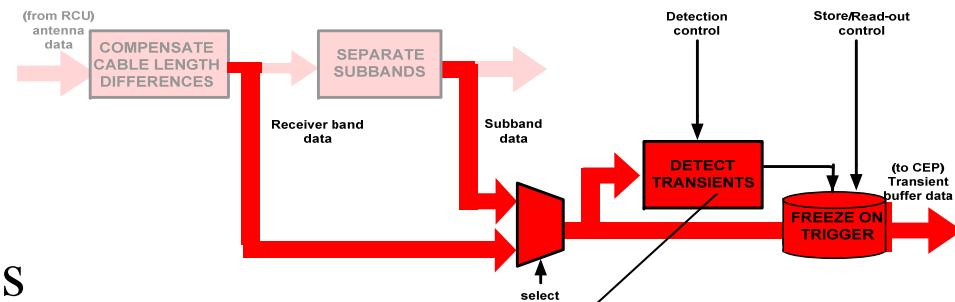
instead of logical elements only



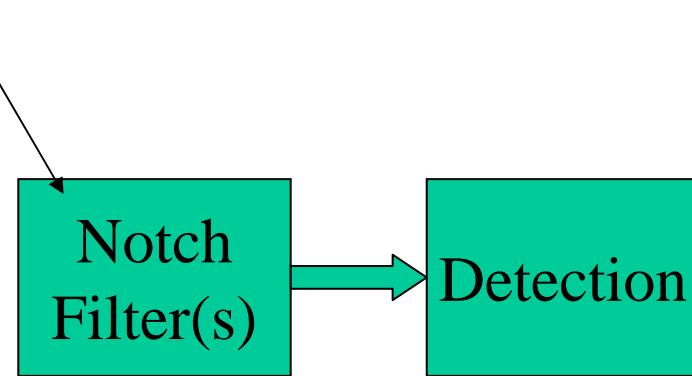


- ☞ Input data stream: 230 Gbps (6 DVDs/s)
- ☞ Output data stream: 3-6 Gbps
- ☞ Memory: 48 GByte
- ☞ Processing capacity: 750 Gmul/s
- ☞ 20 kilometer coax cable



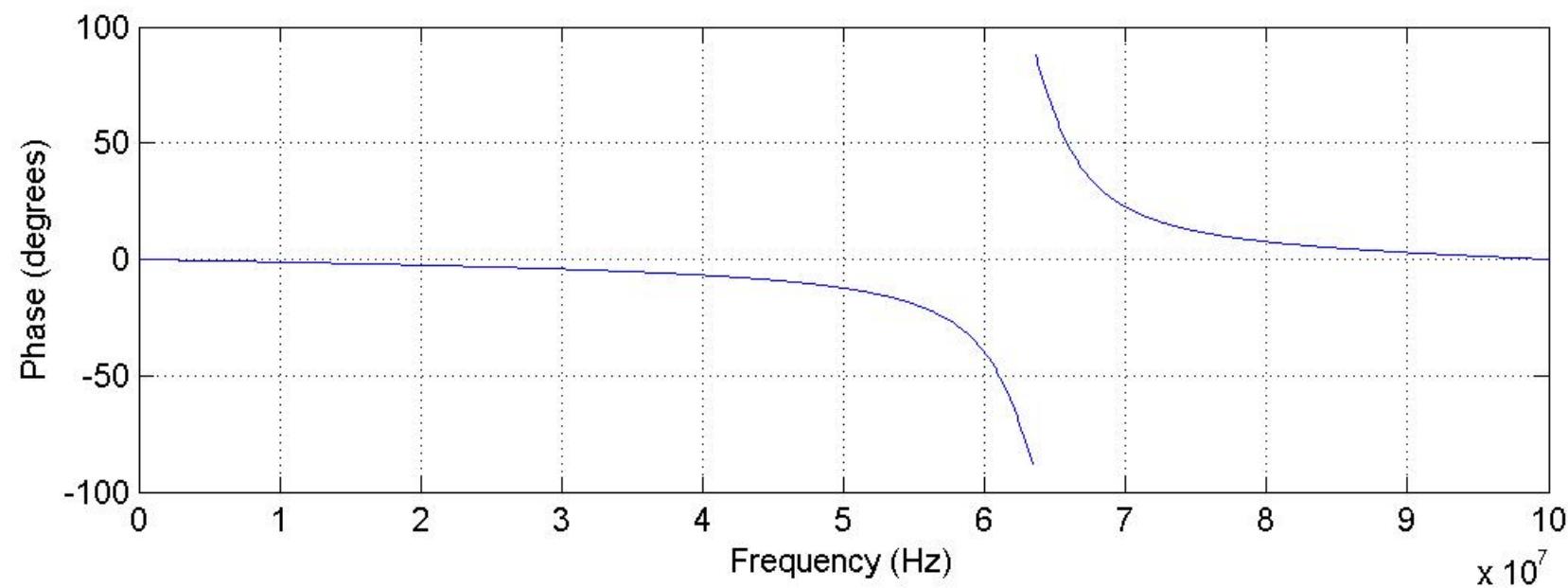
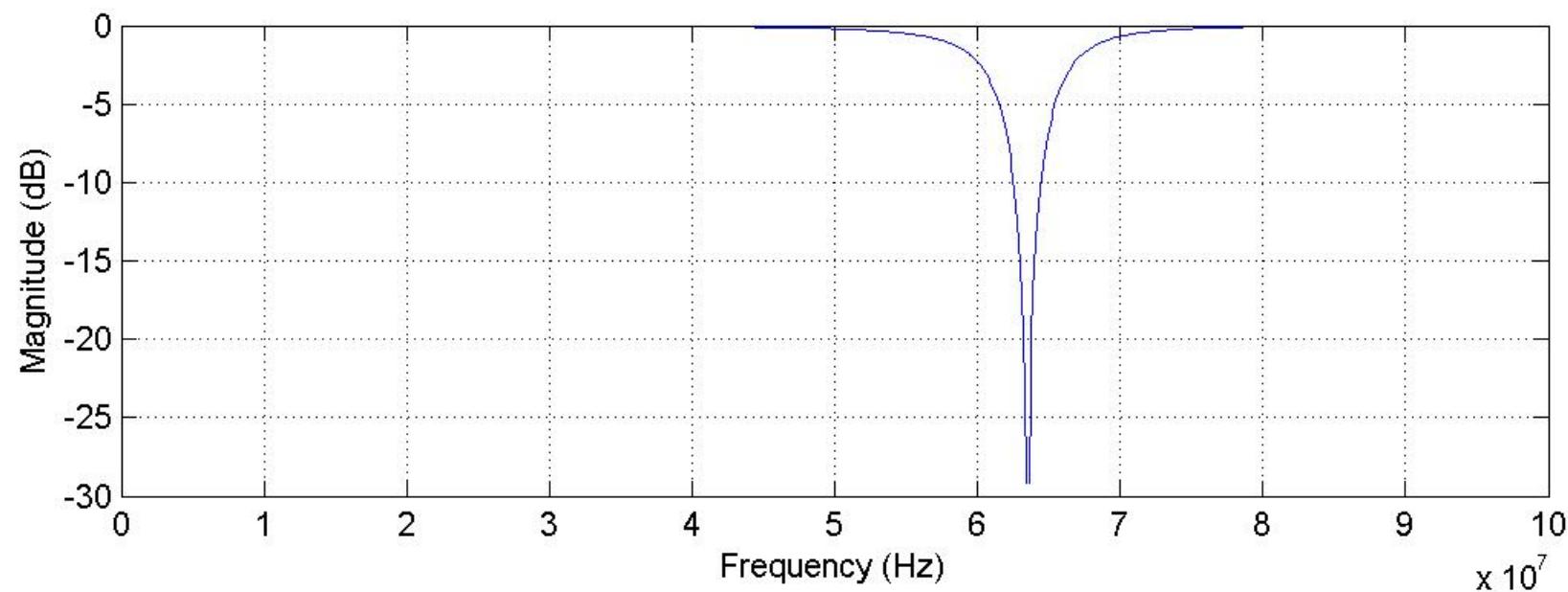


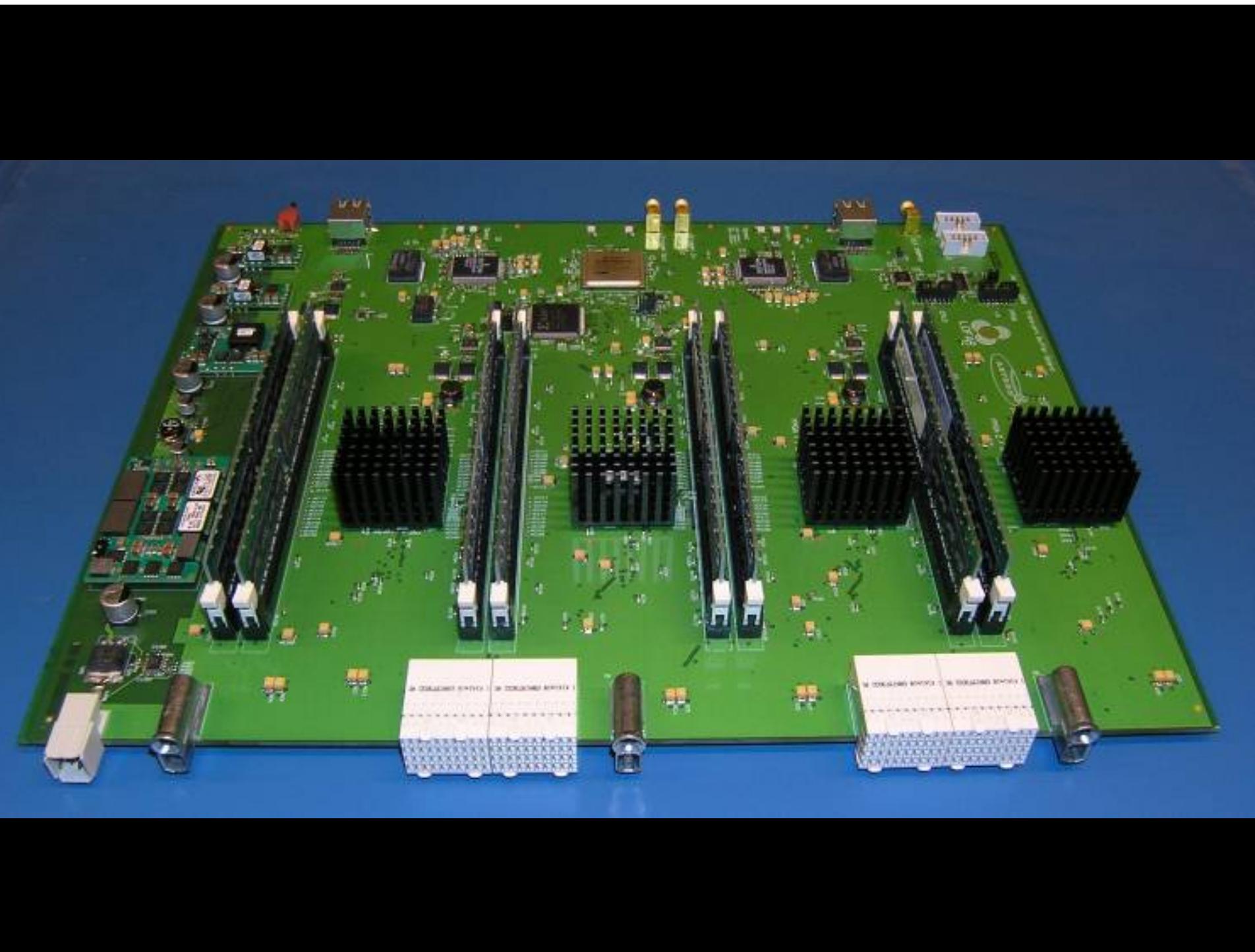
to suppress strong RFI signals  
at fixed locations

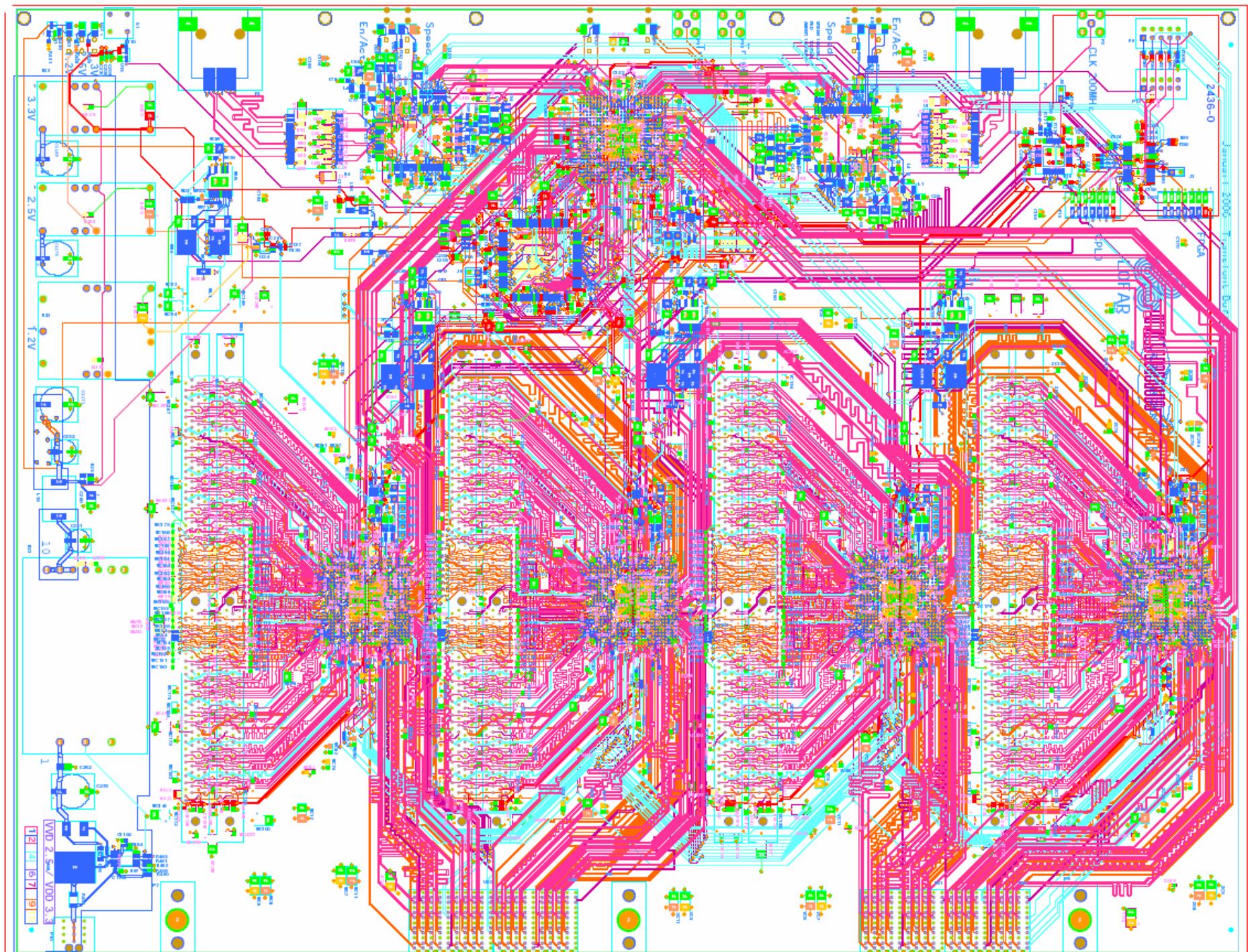


Detection only possible on the stored signals

- Properties Infinite Impulse Response Filters:
  - ☞ Advantage: cheap
  - ☞ Disadvantage: non-linear phase
  
- Example of a 3<sup>th</sup> order IRR filter:
  - ☞ centre frequency: 63.5 MHz
  - ☞ bandwidth: 2 MHz
  - ☞ 4 multipliers required







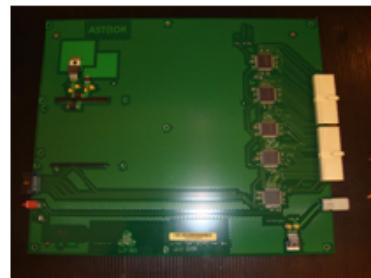
**SPU**



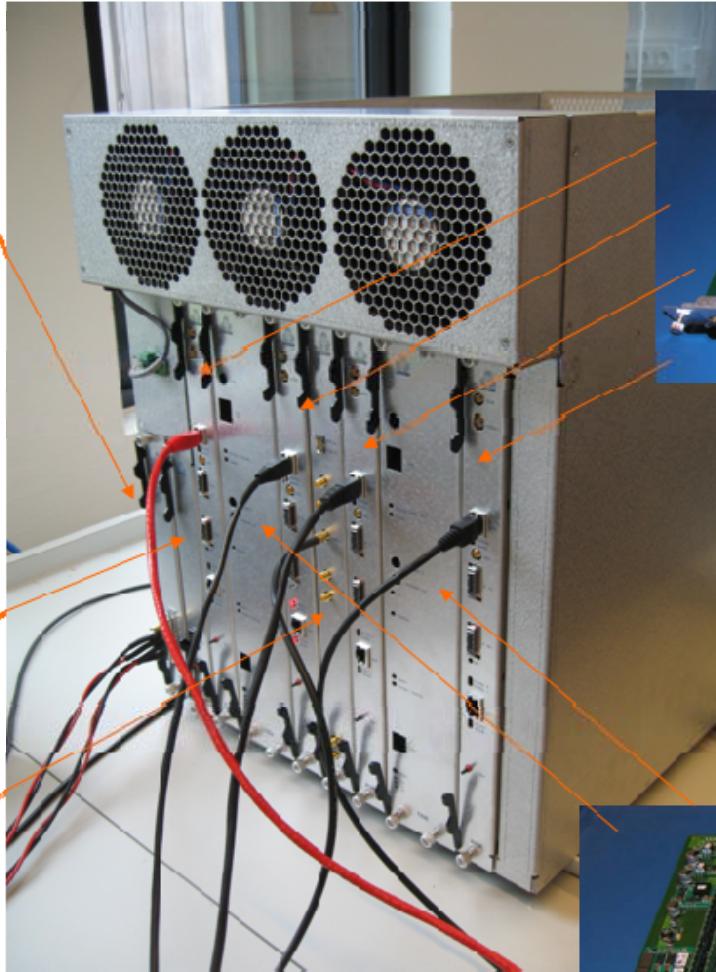
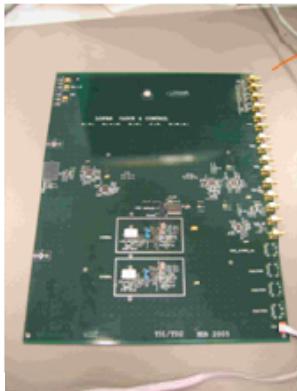
**RSP**



**JTB**



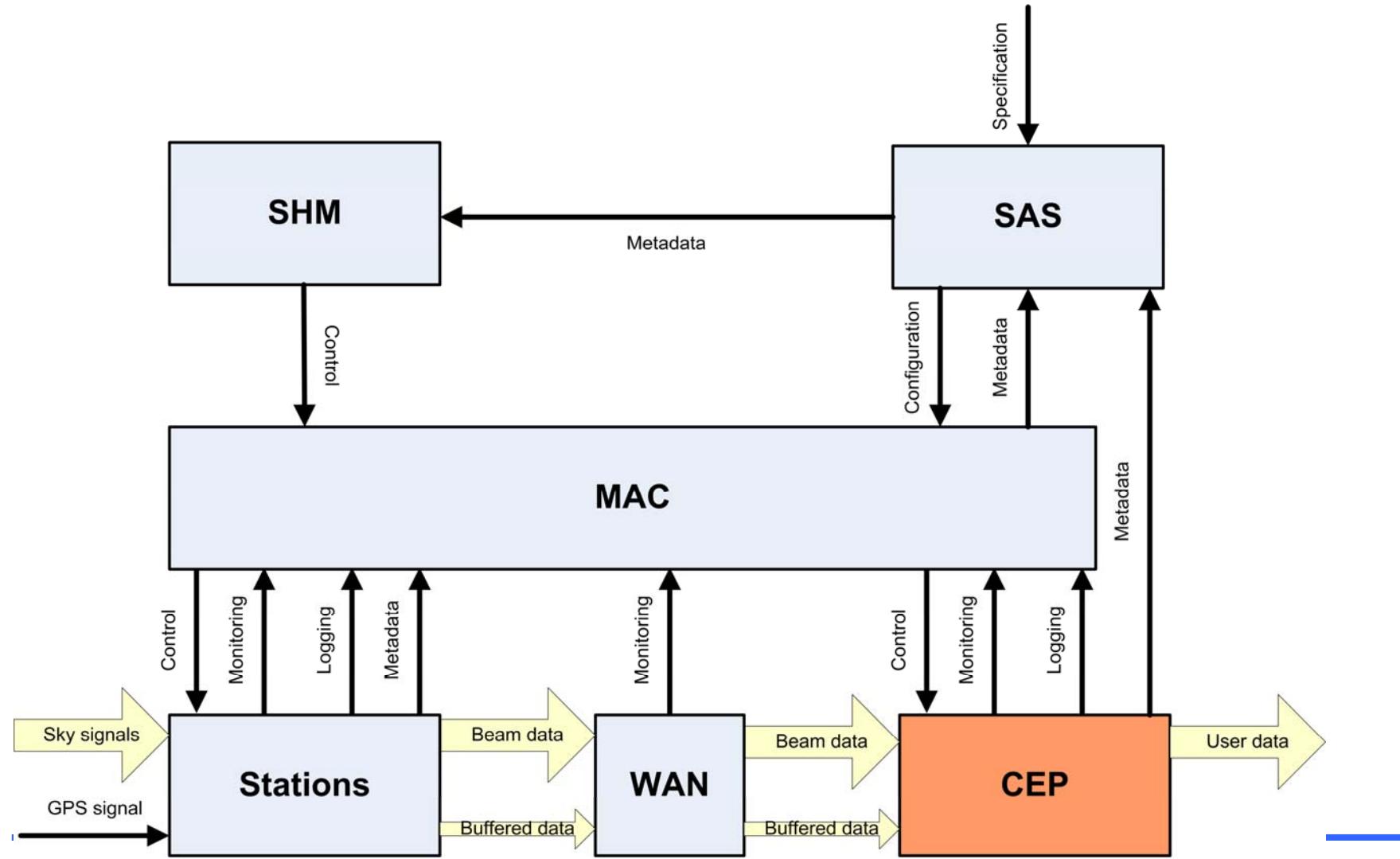
**TDS**

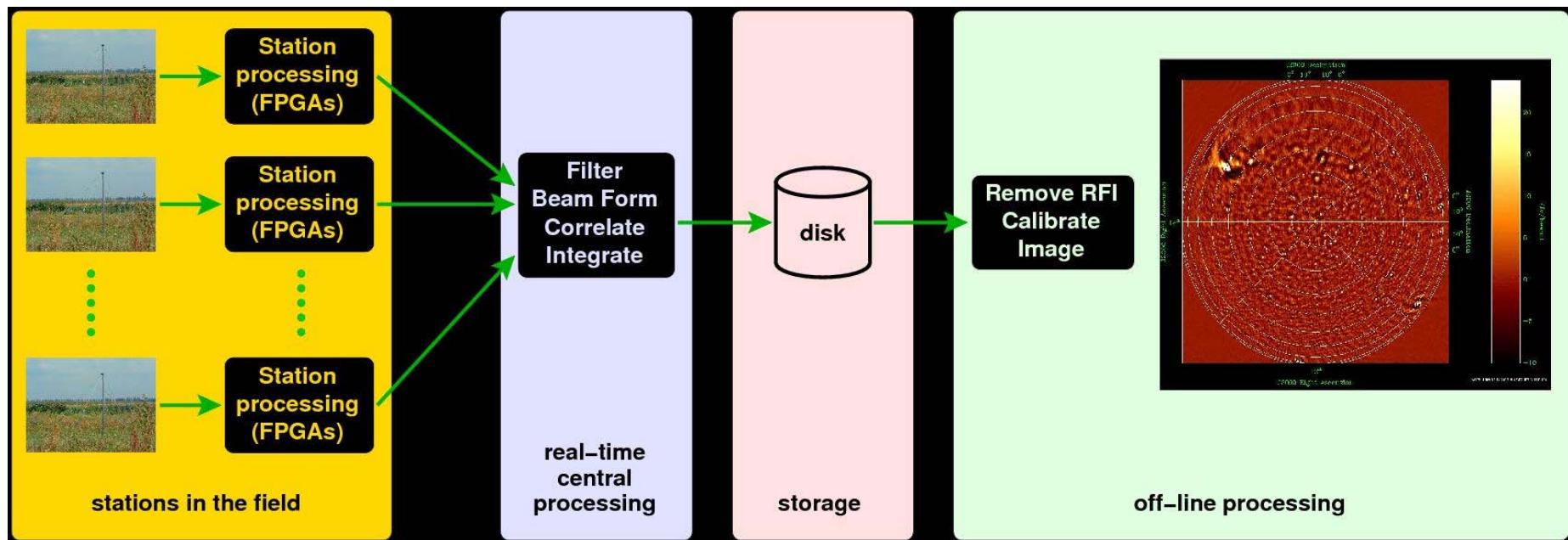


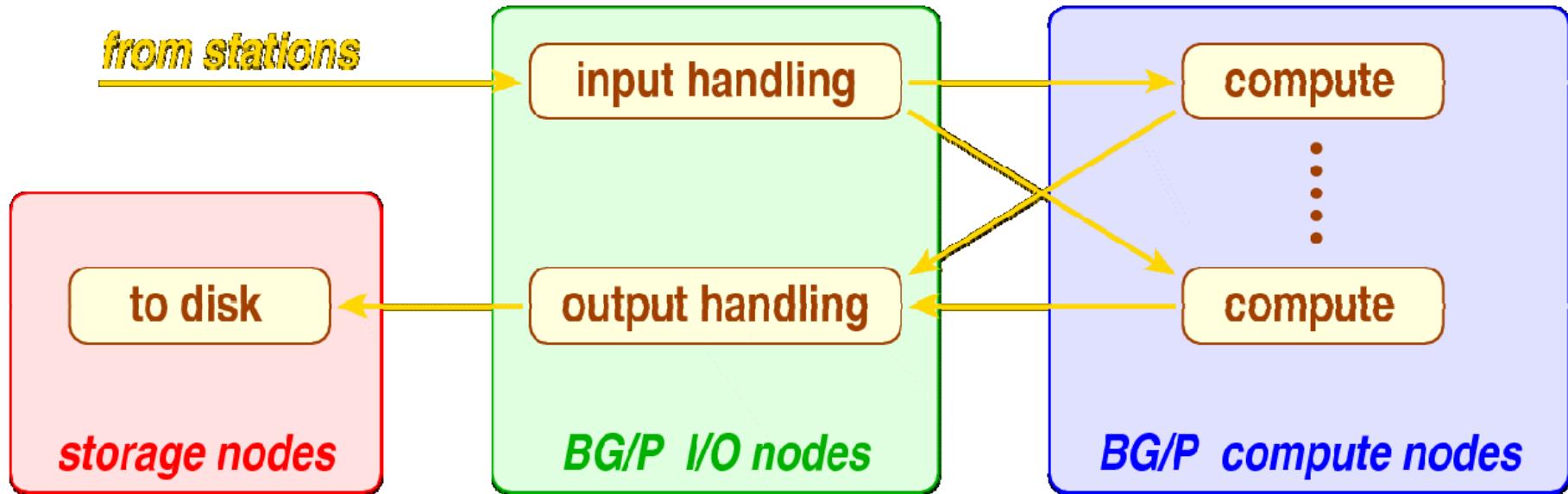
**TBB**

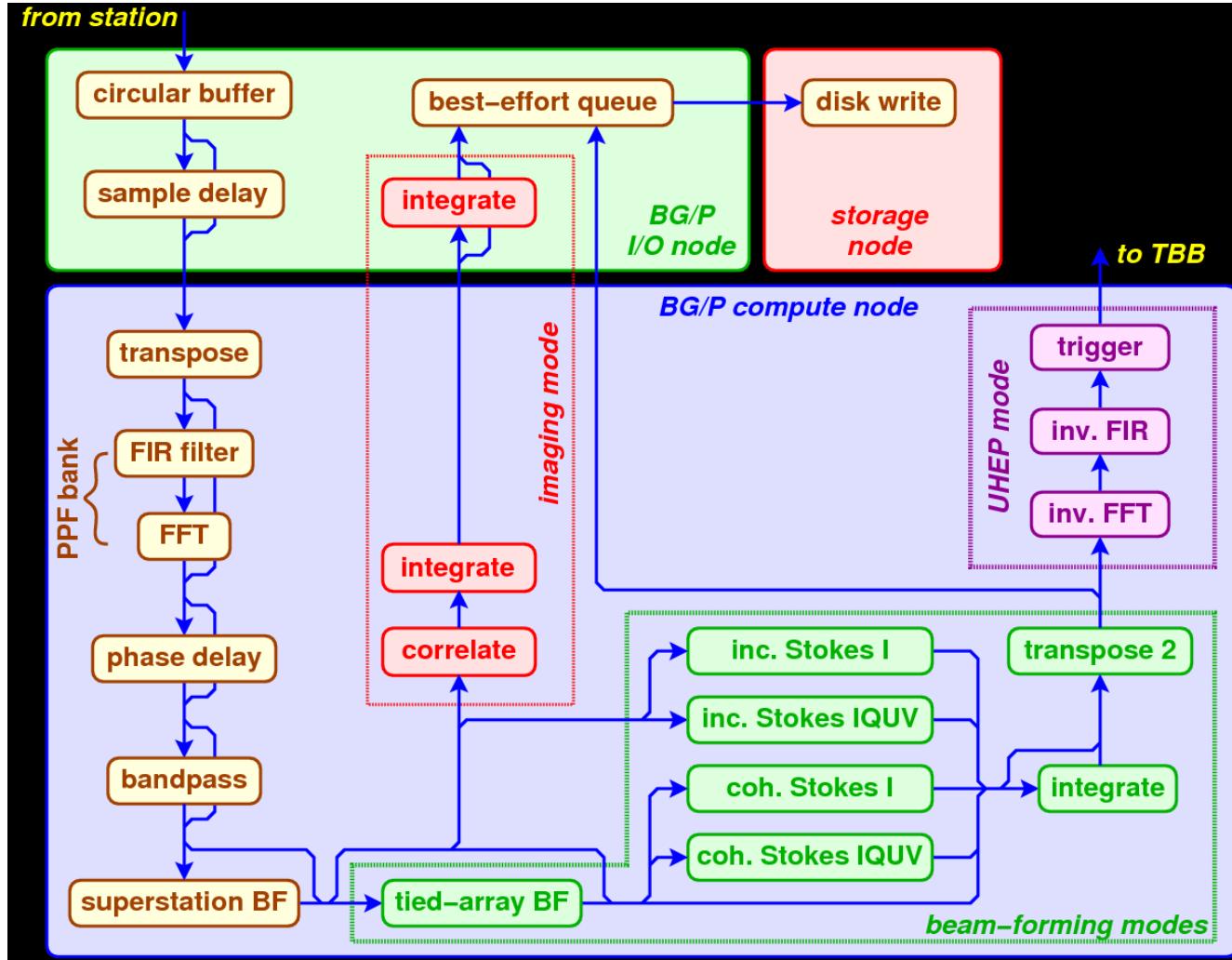


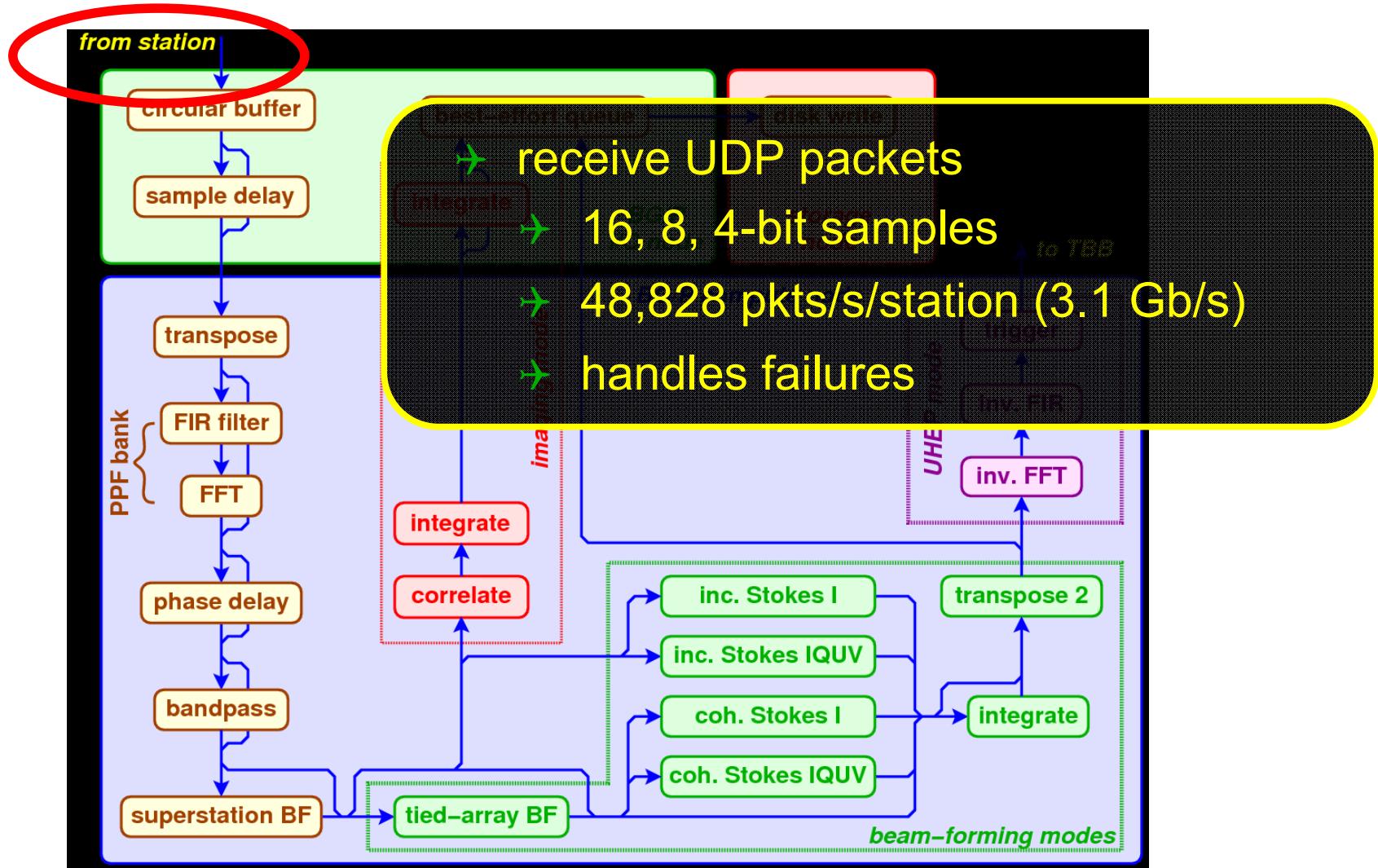


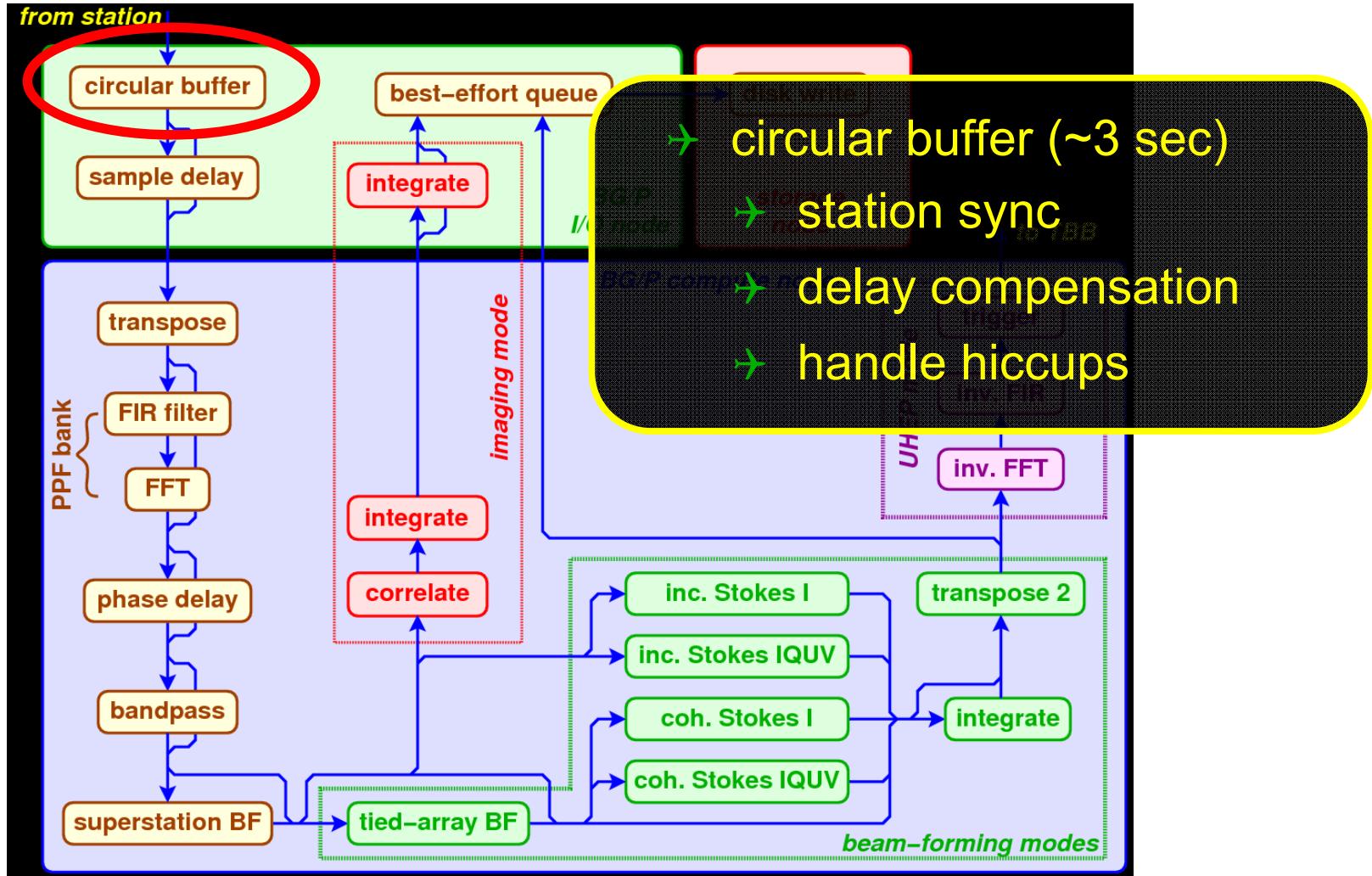


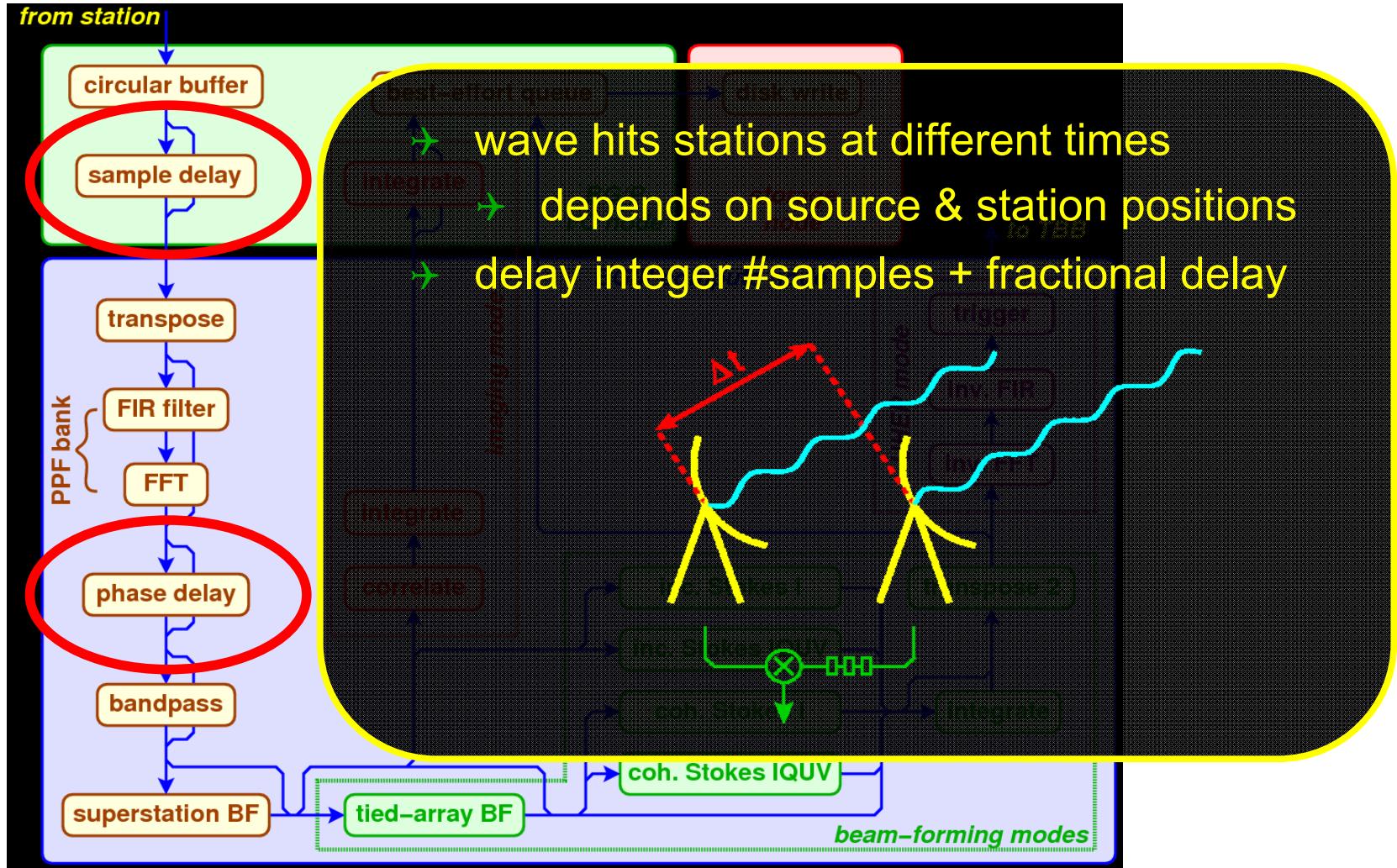


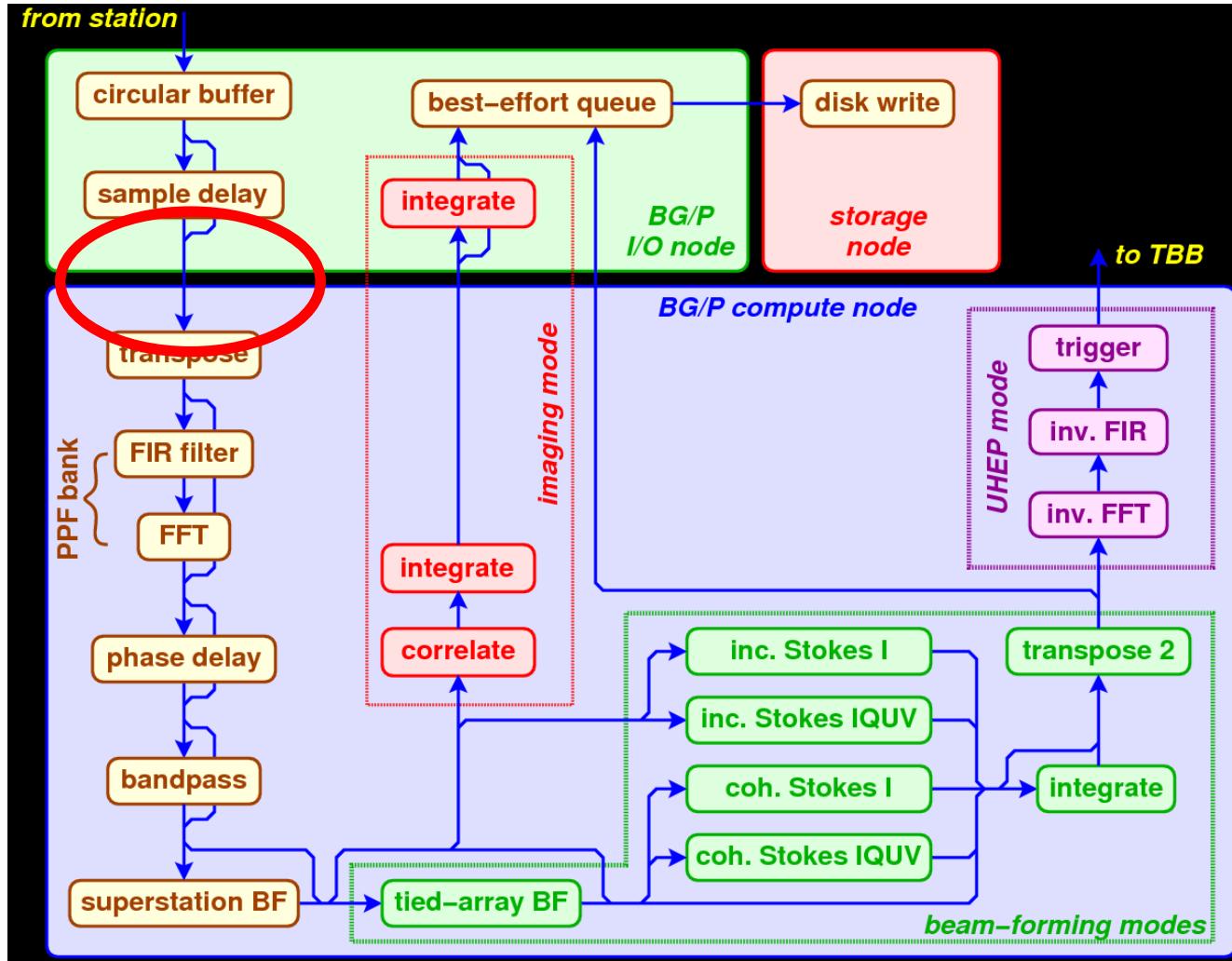


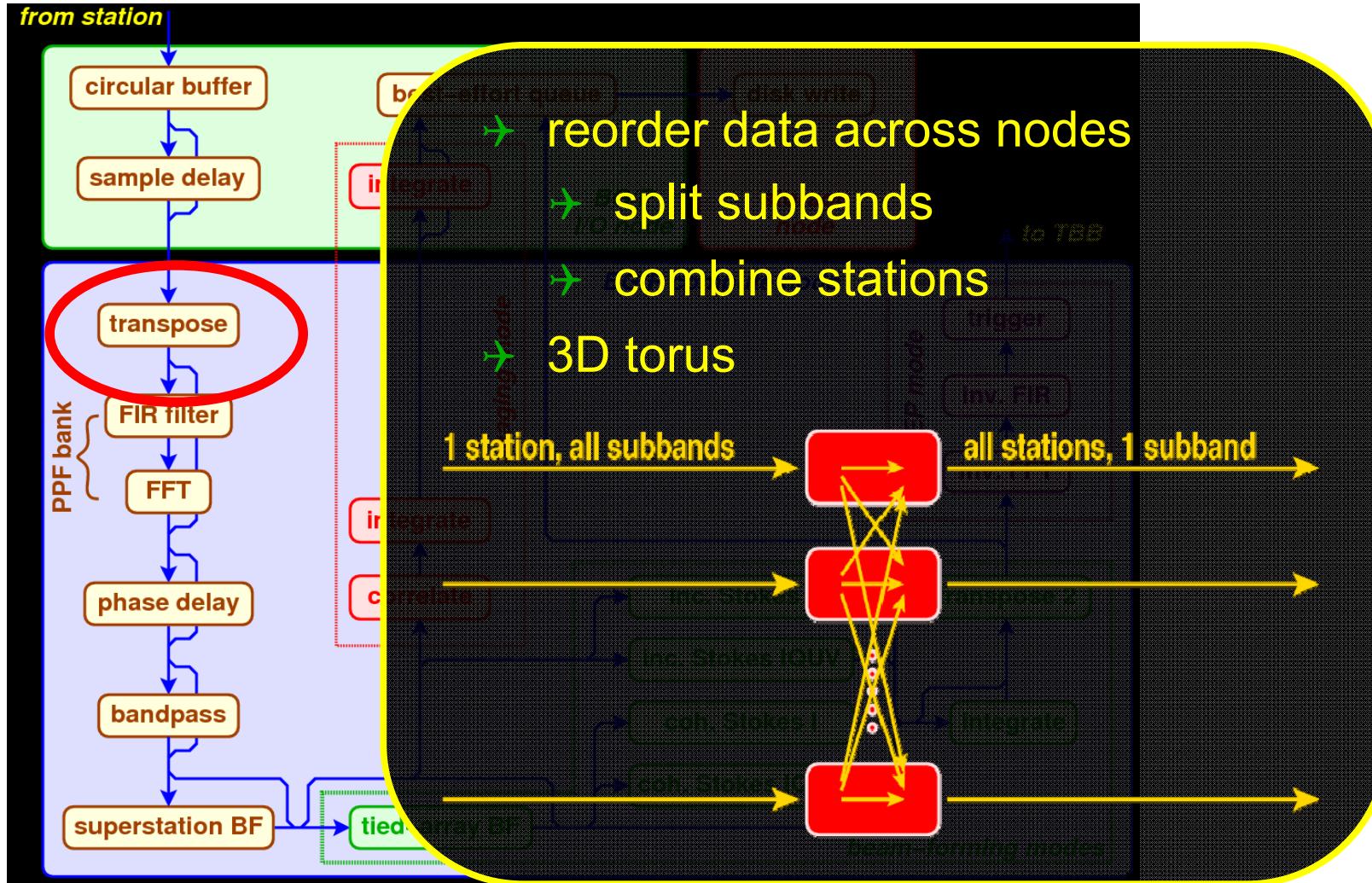


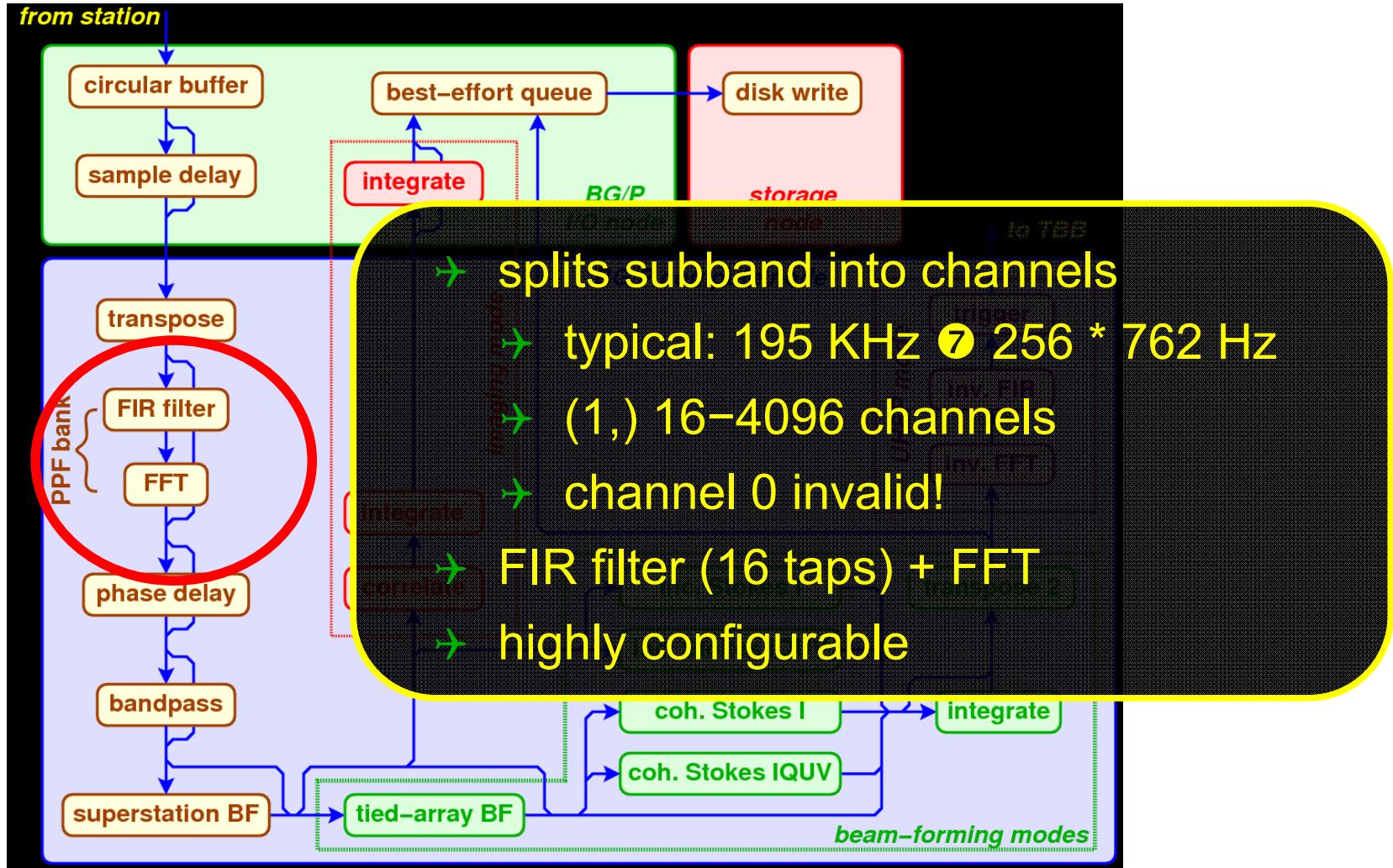


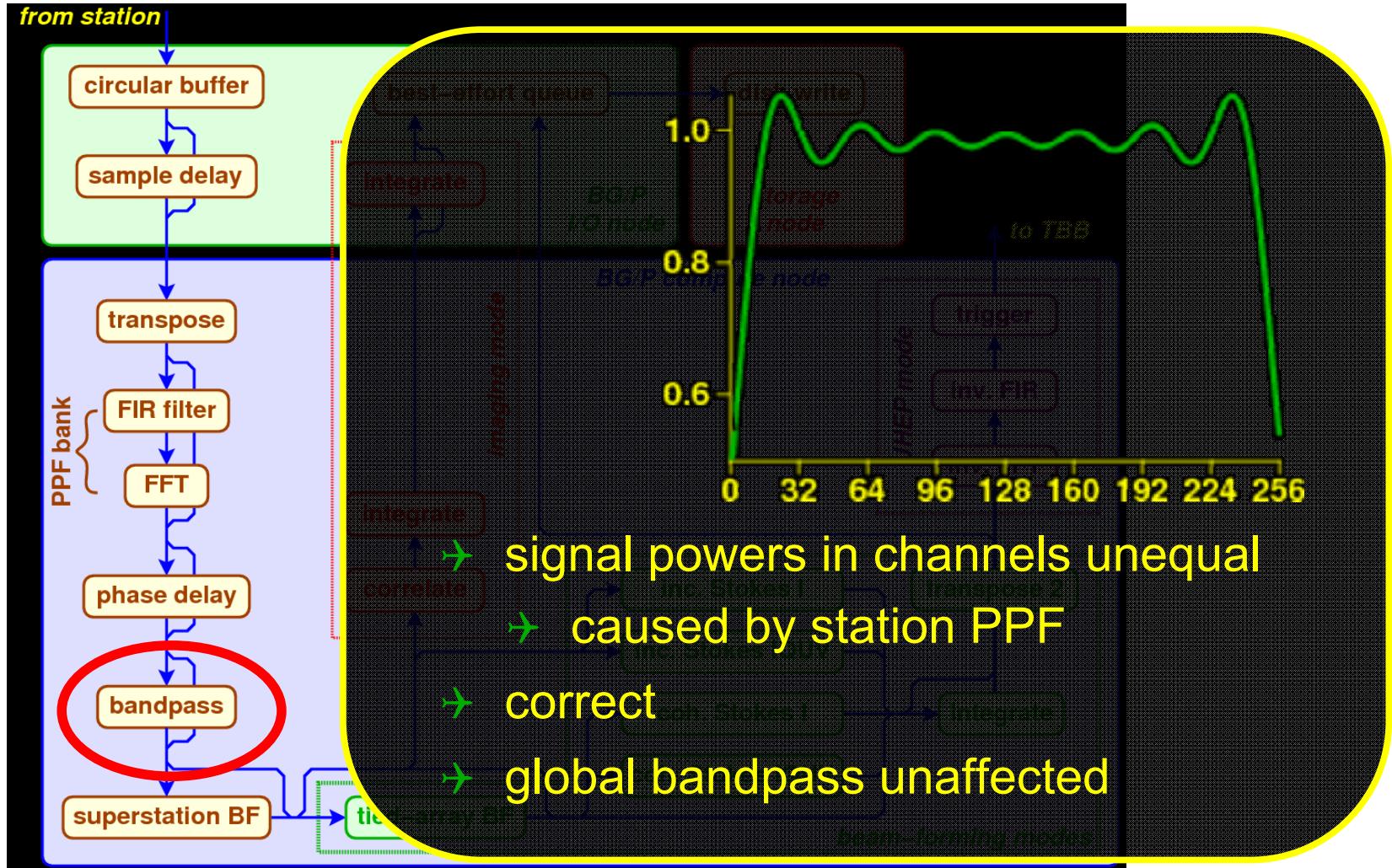


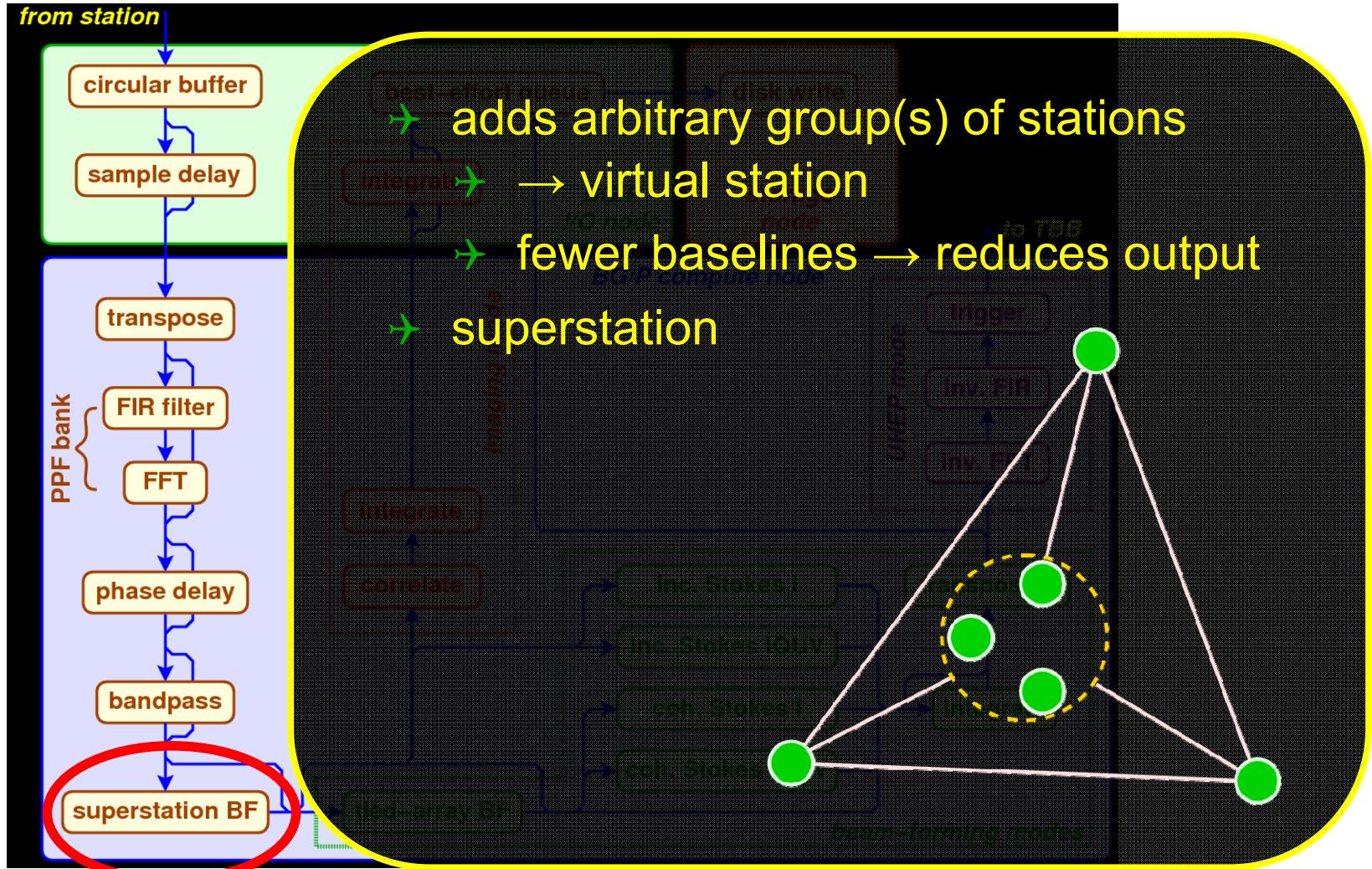


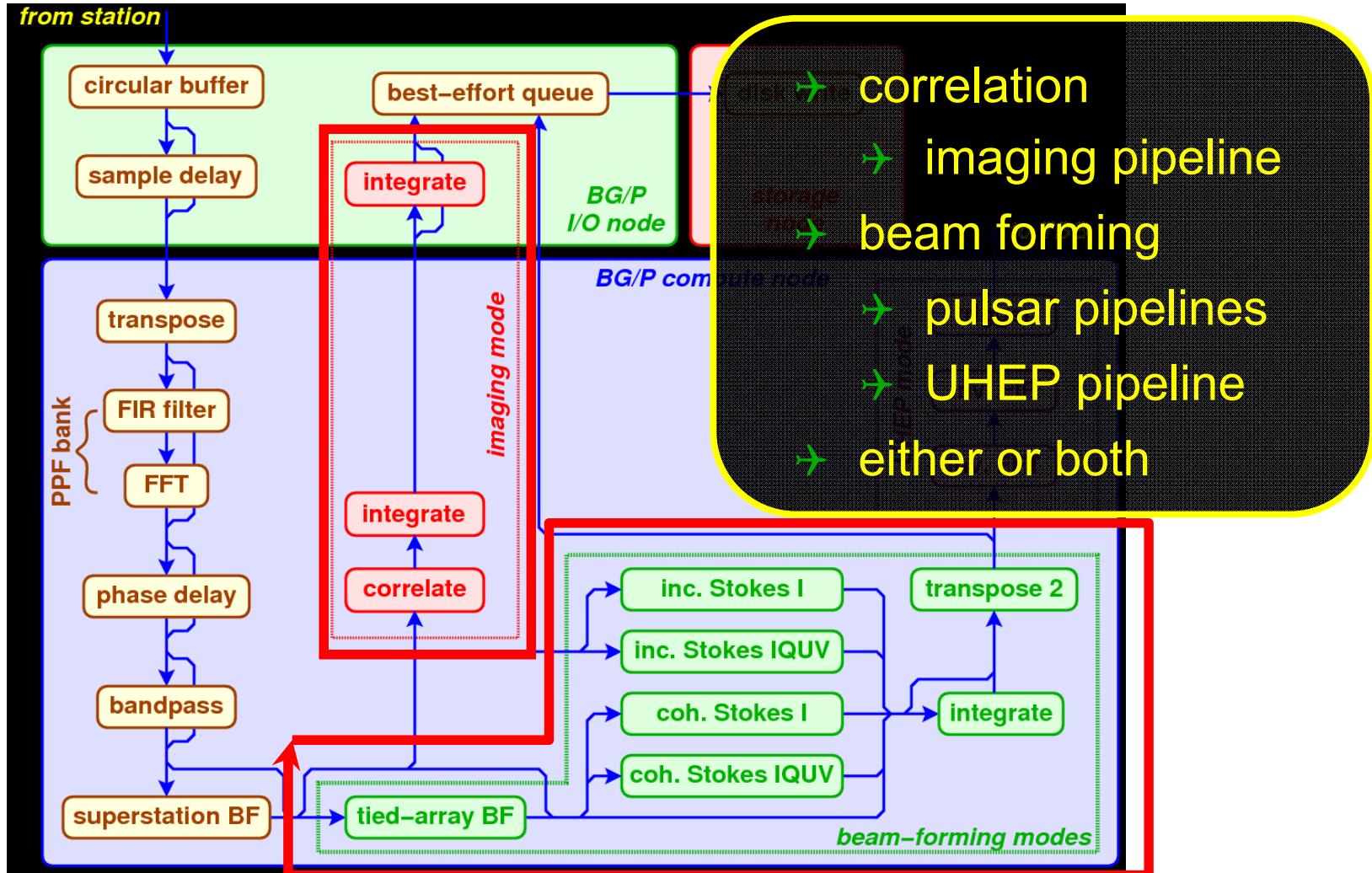


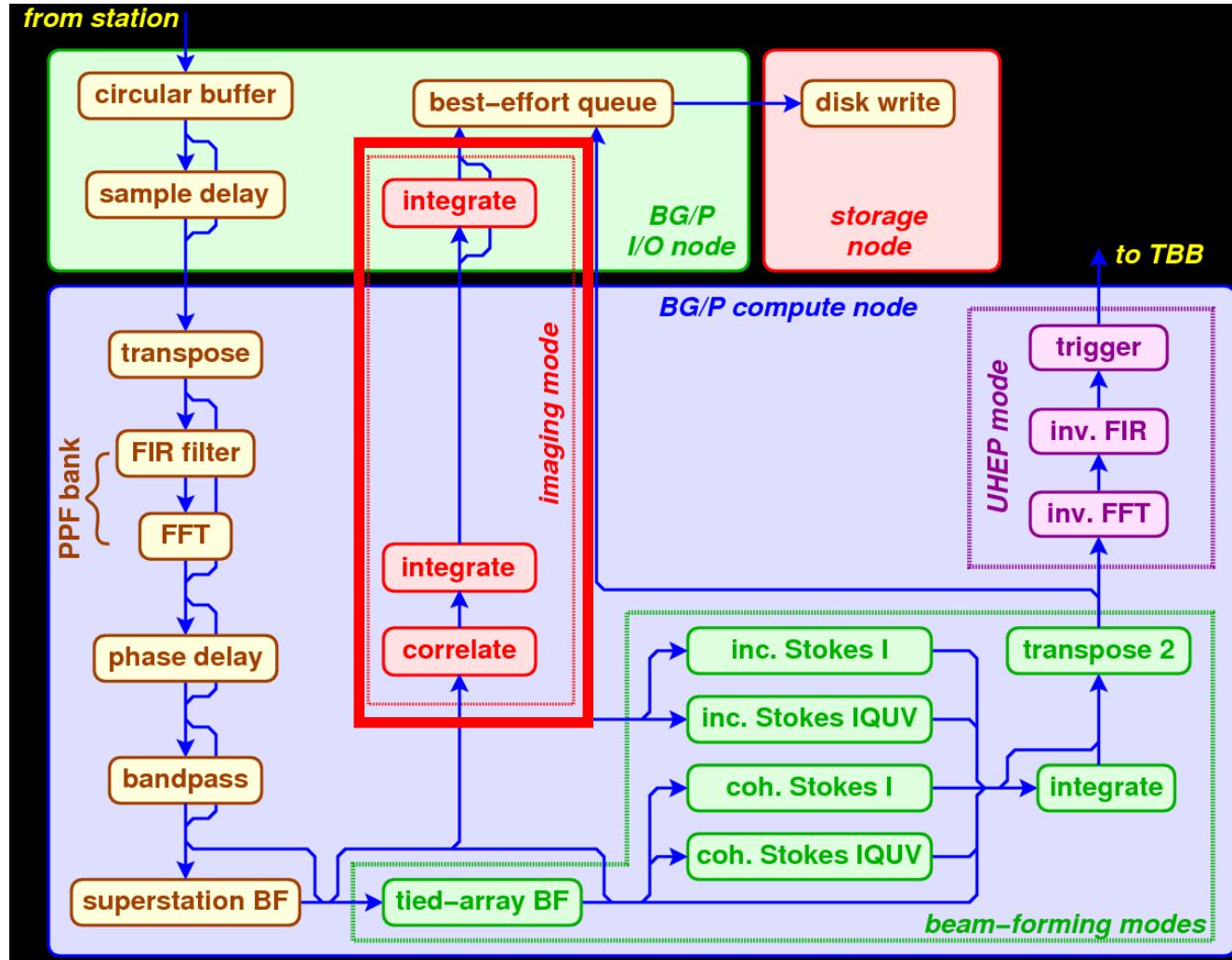


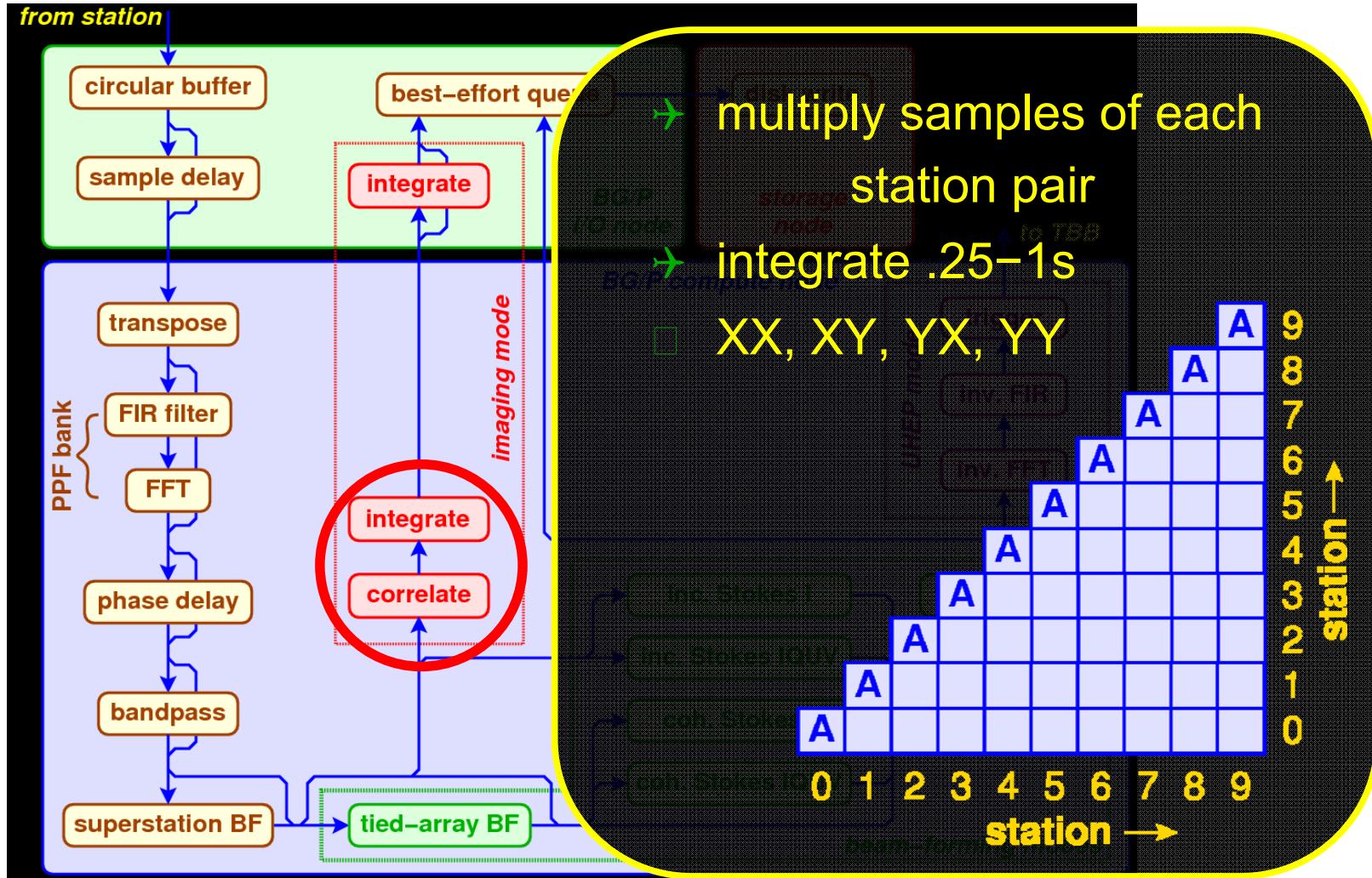


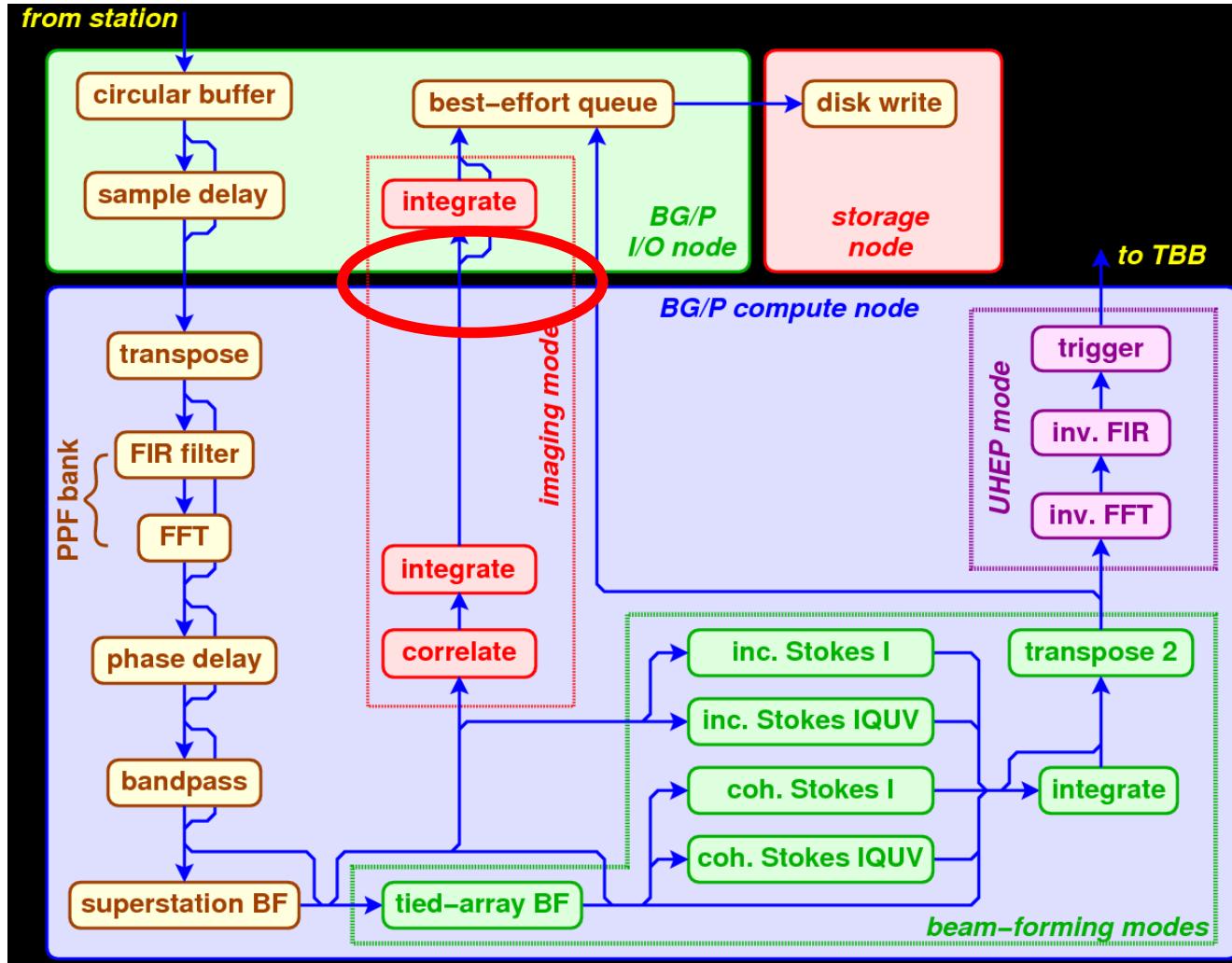


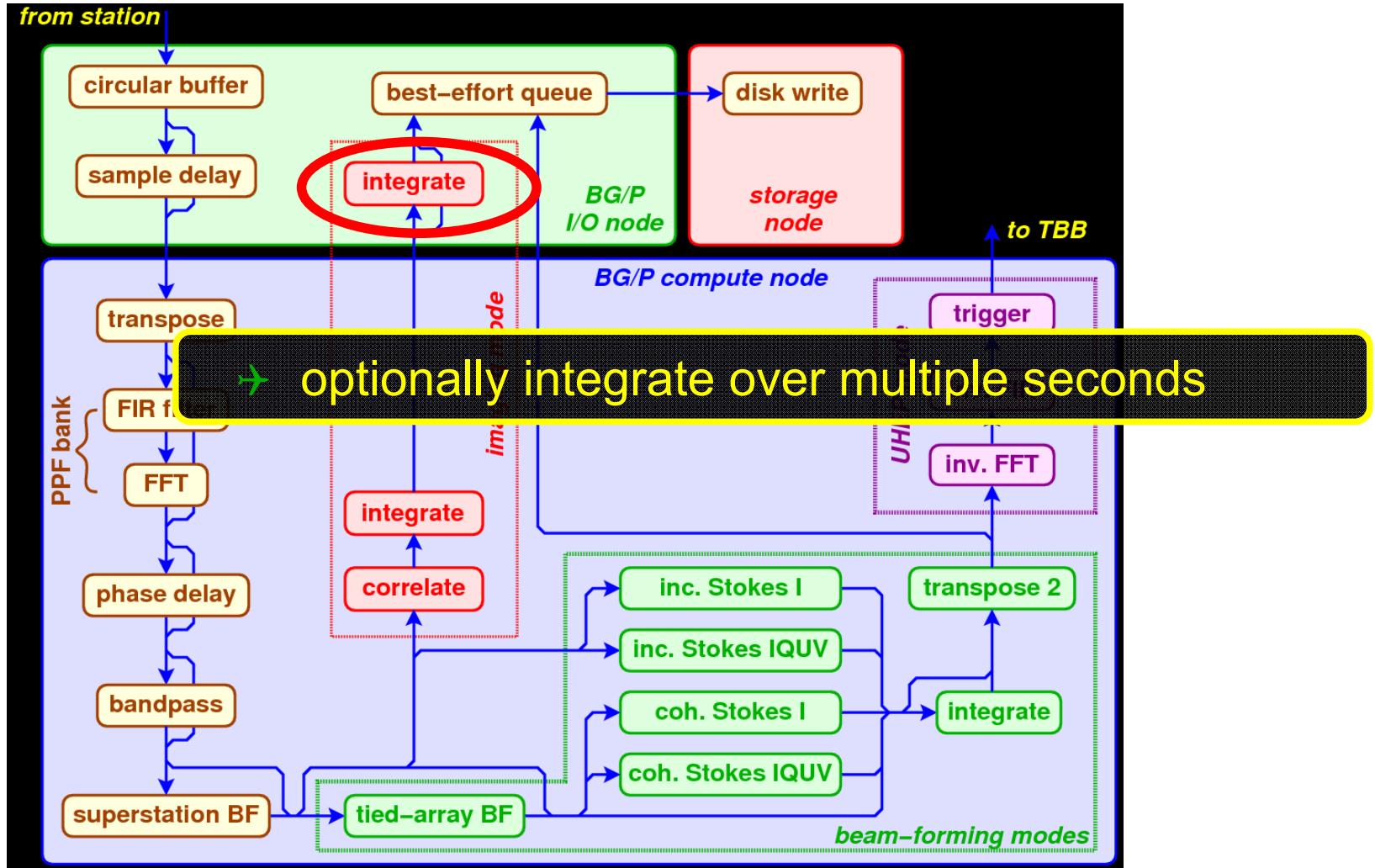


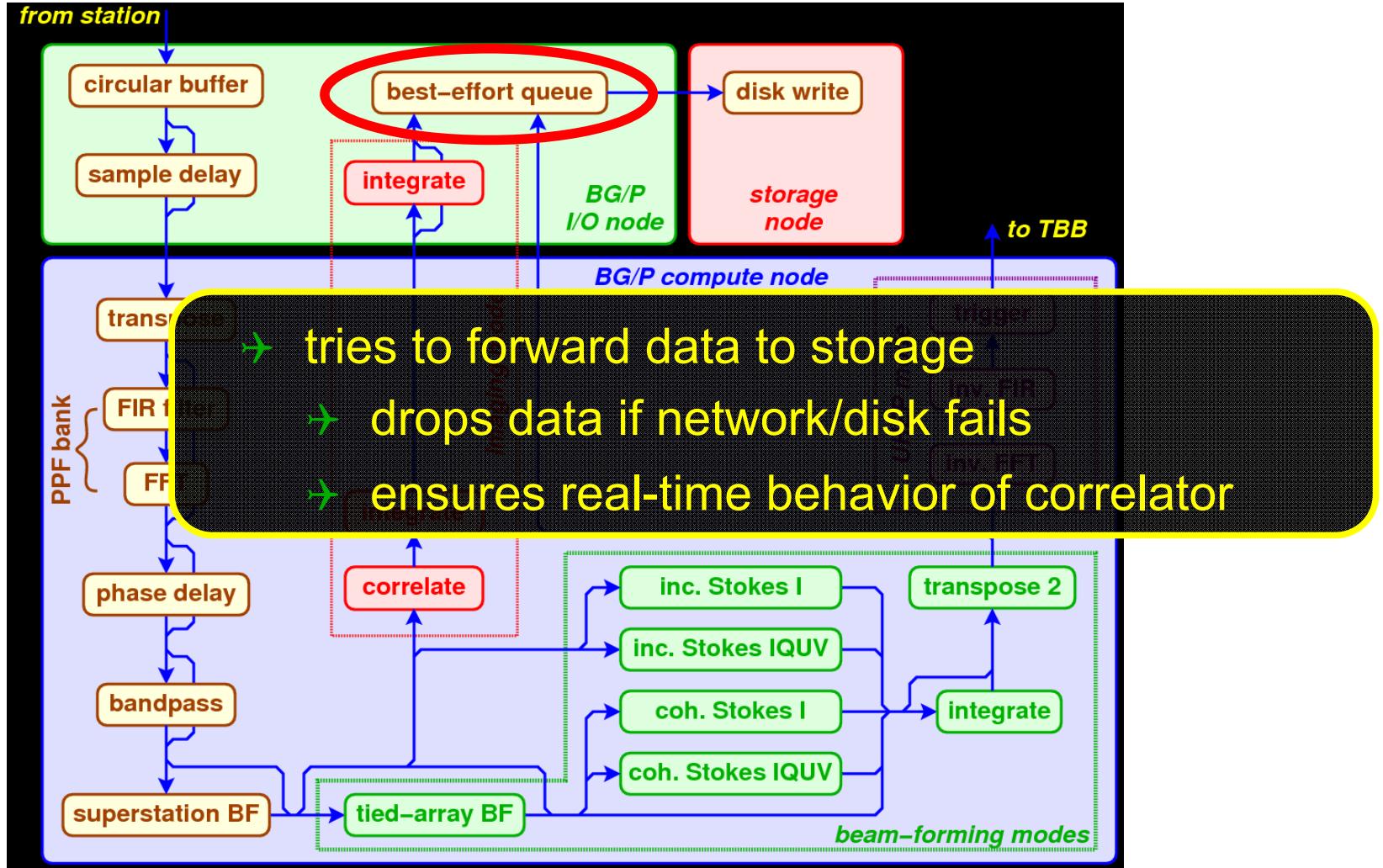


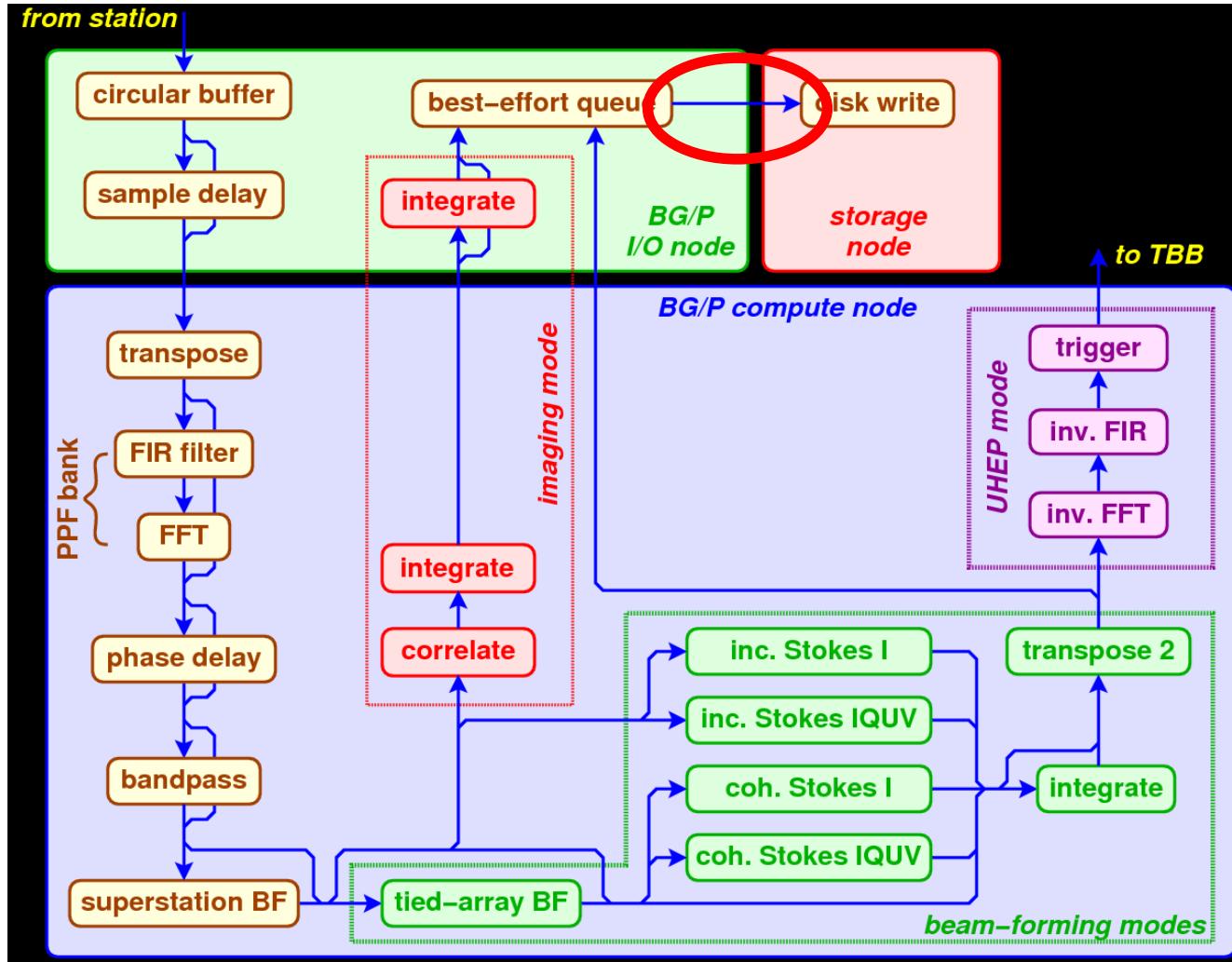


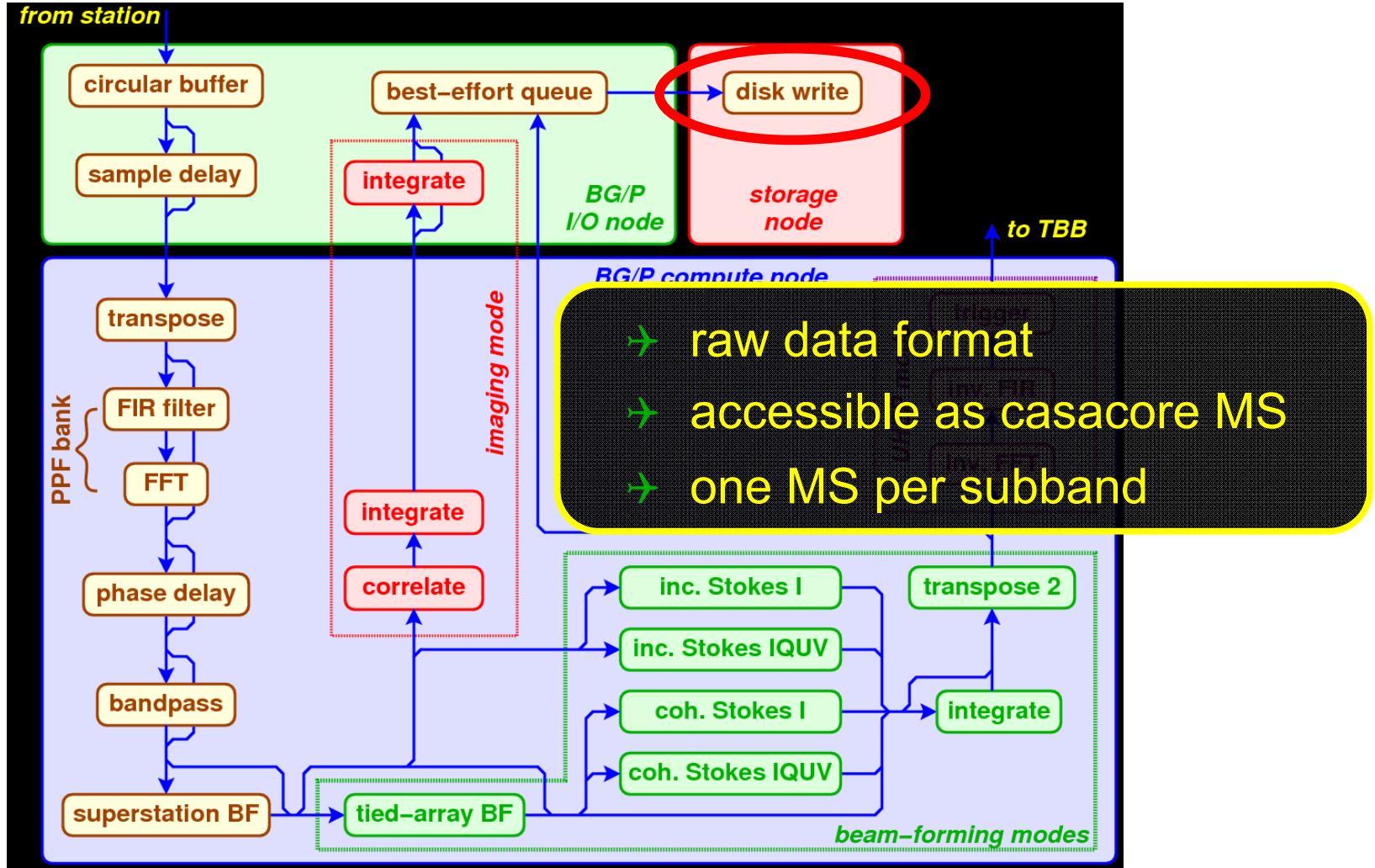


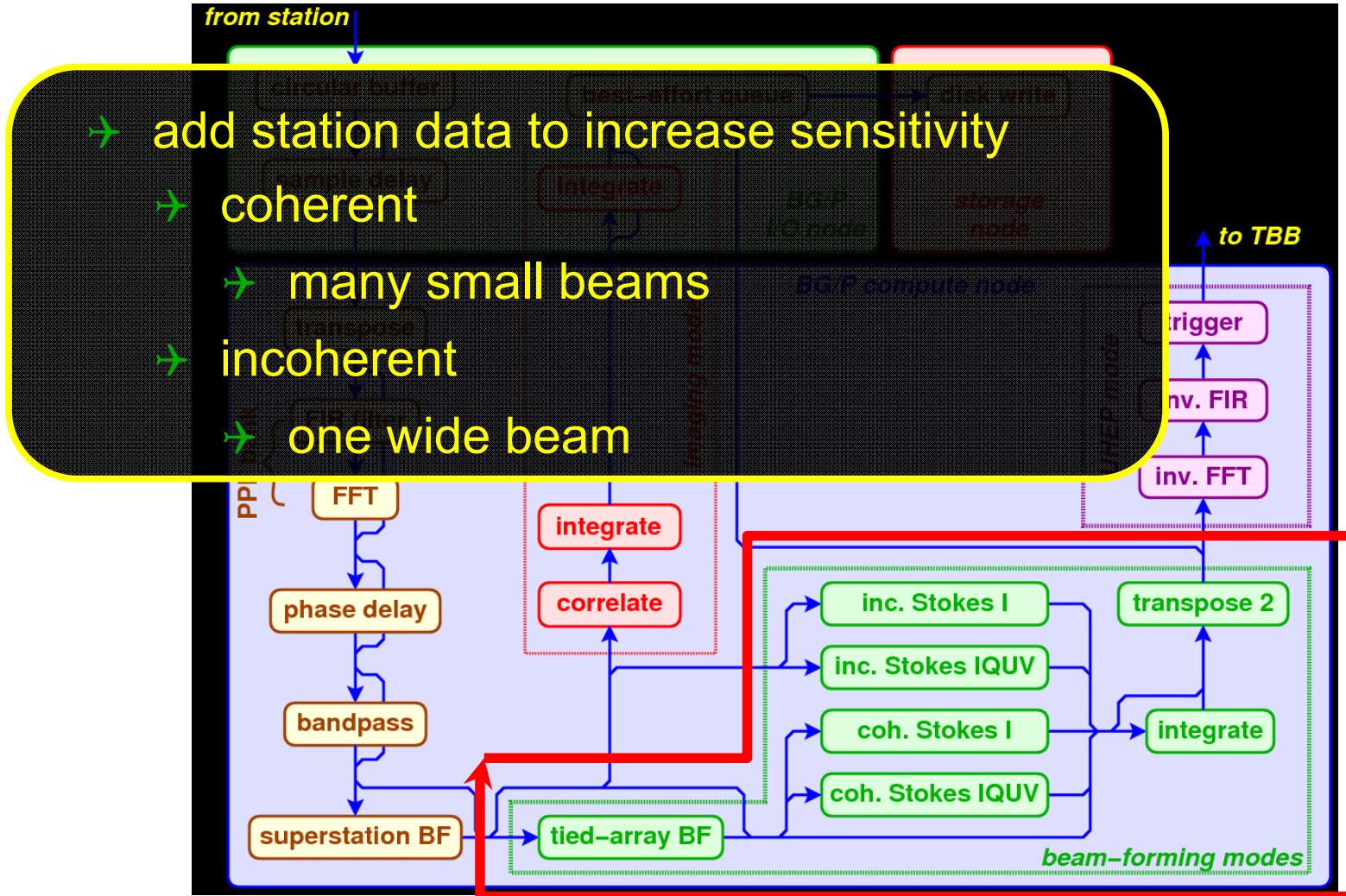


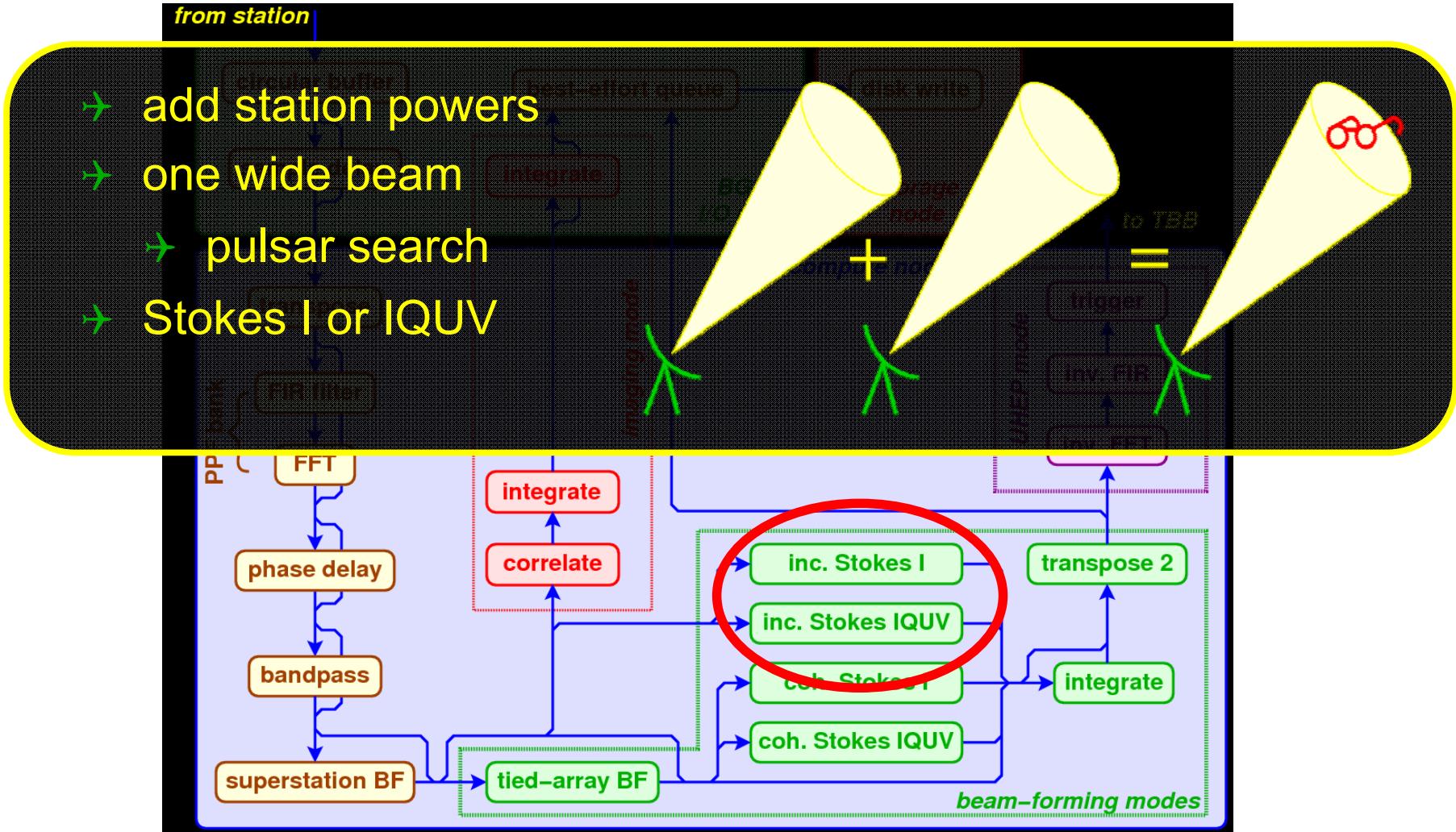


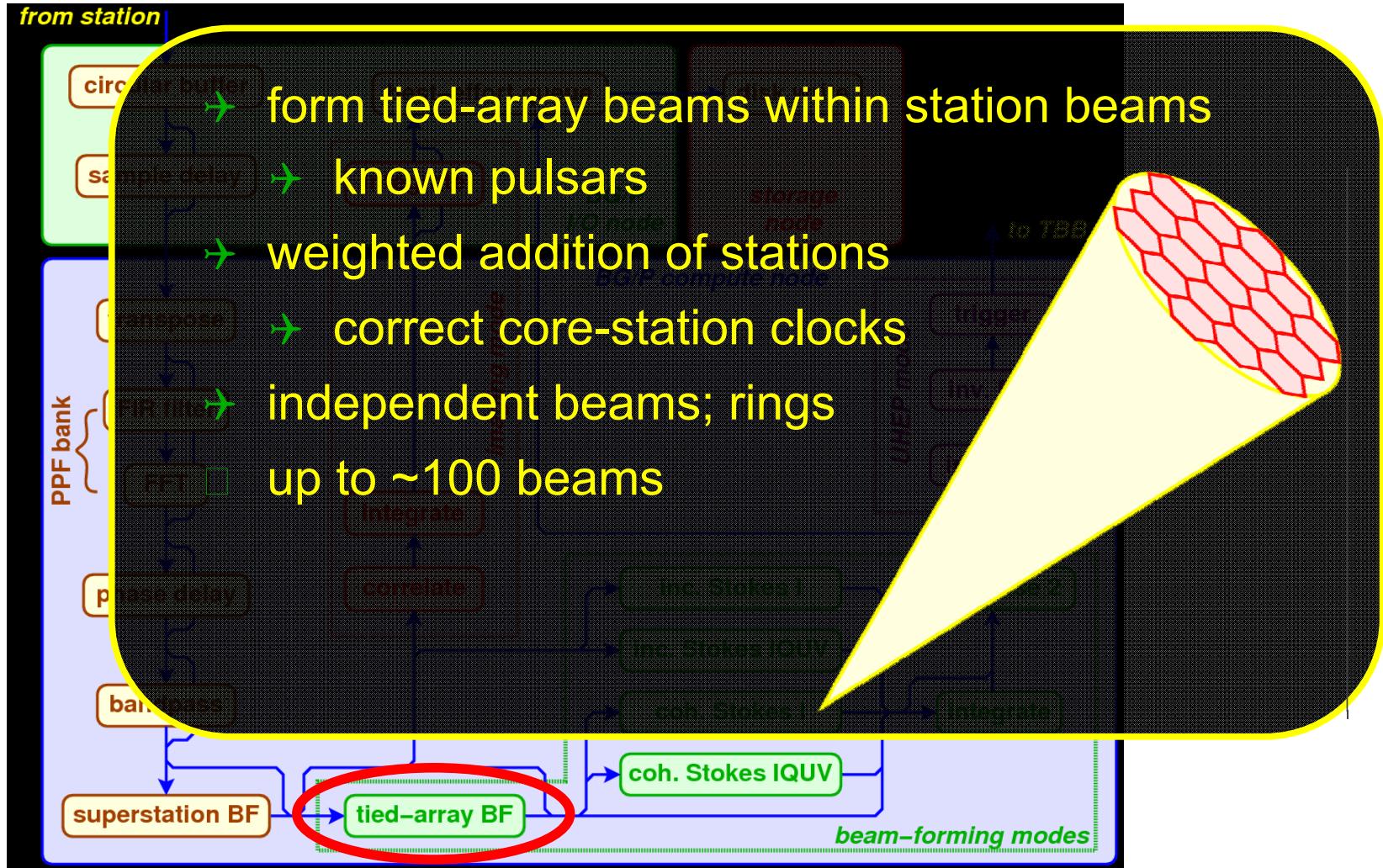


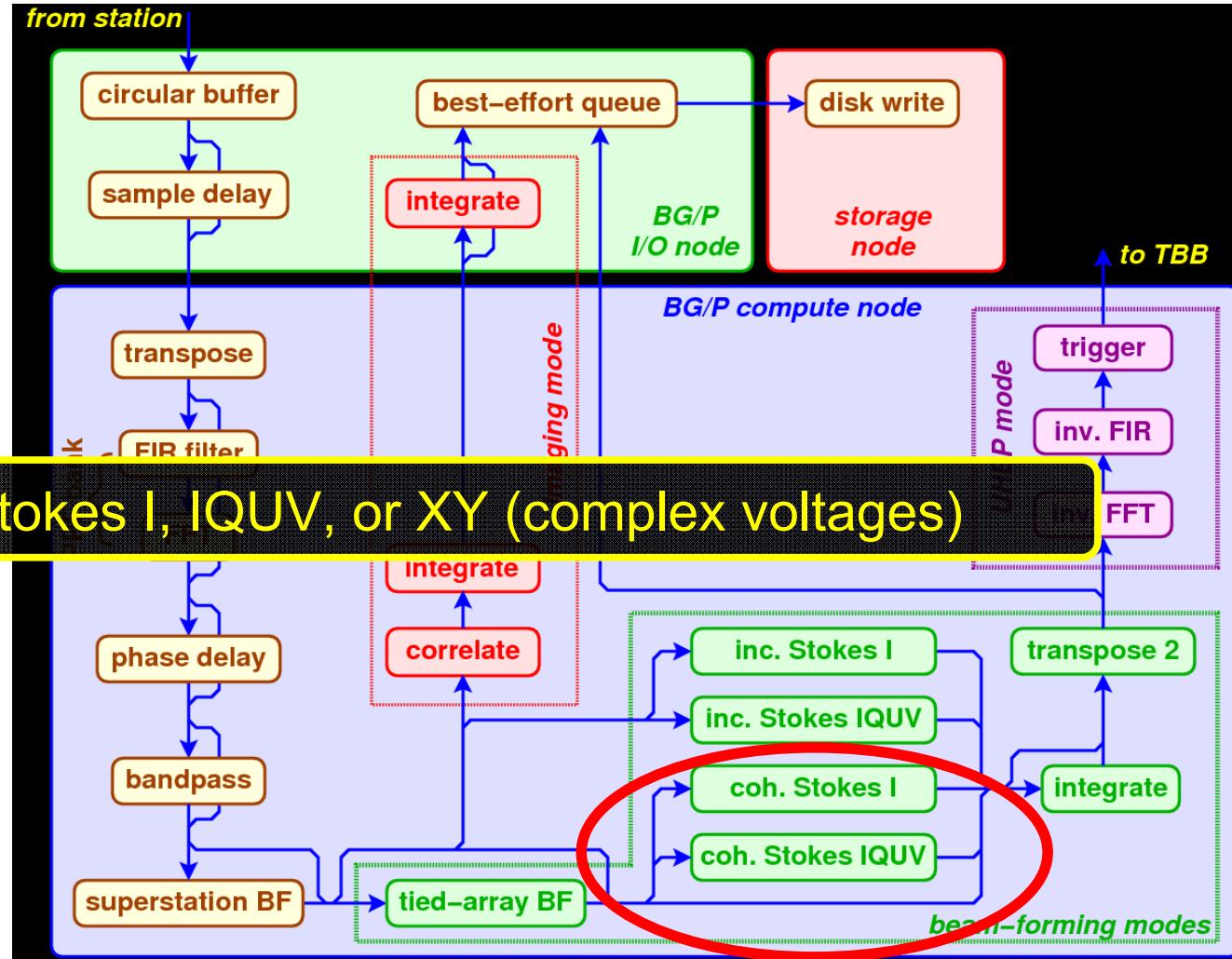


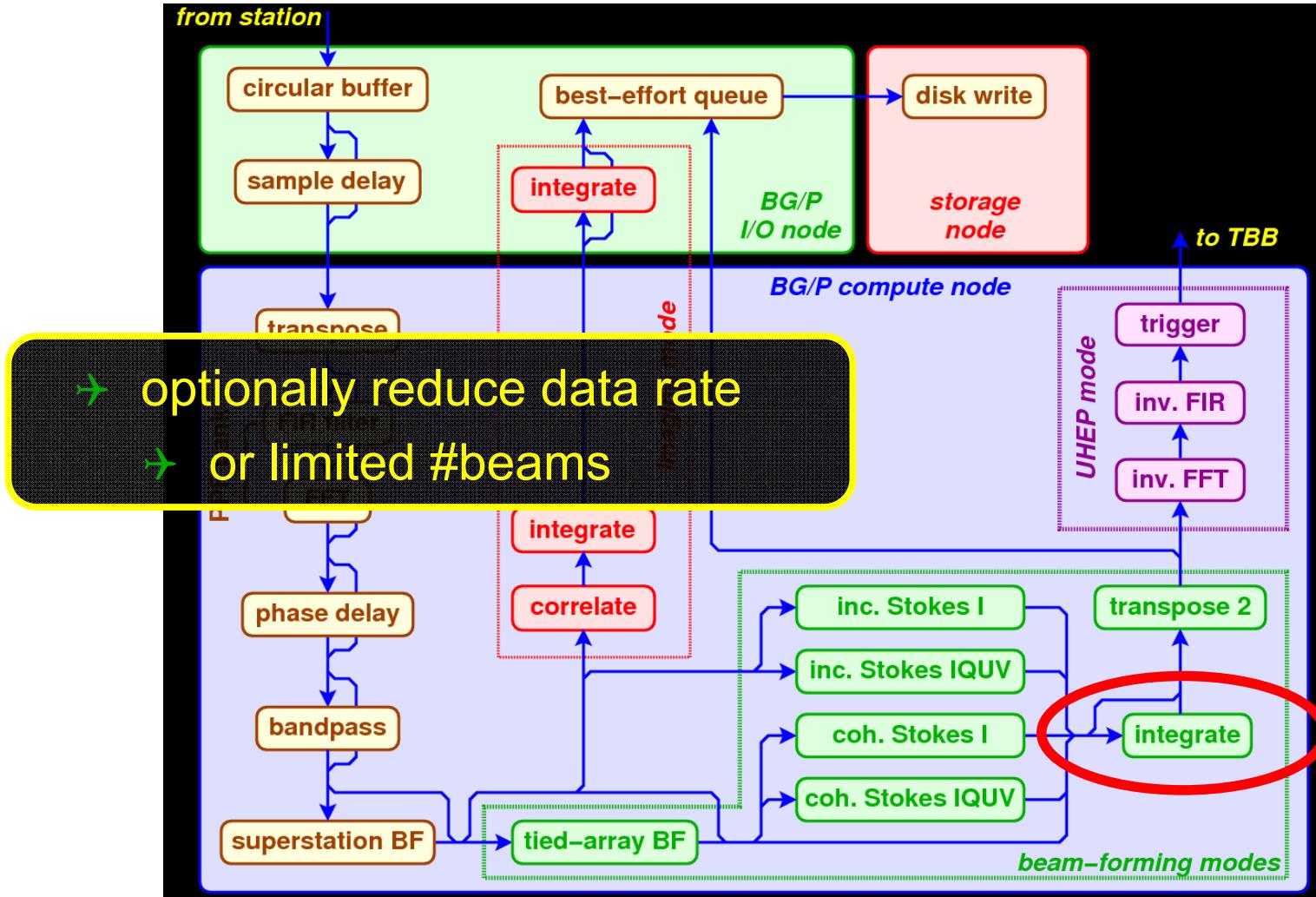


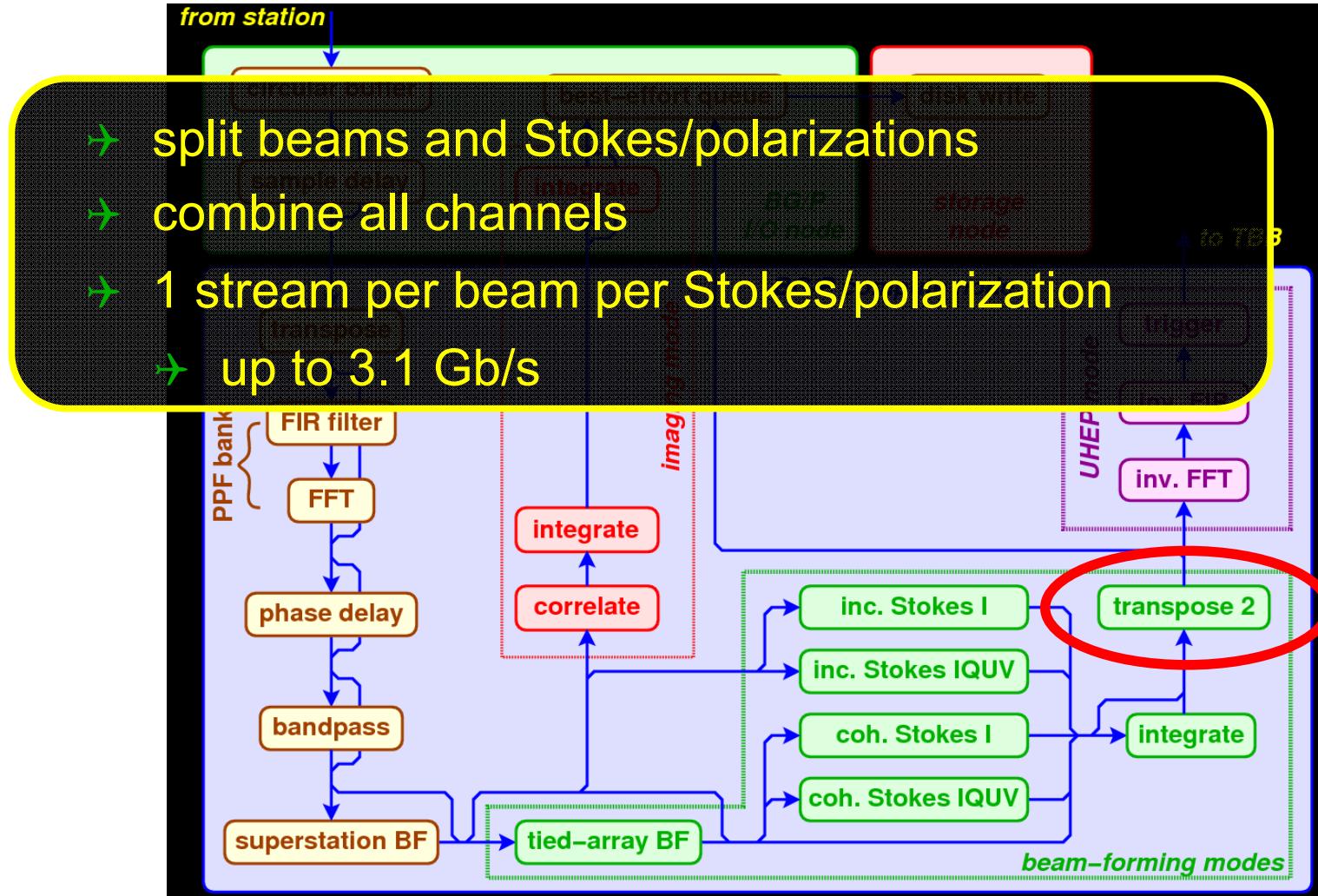


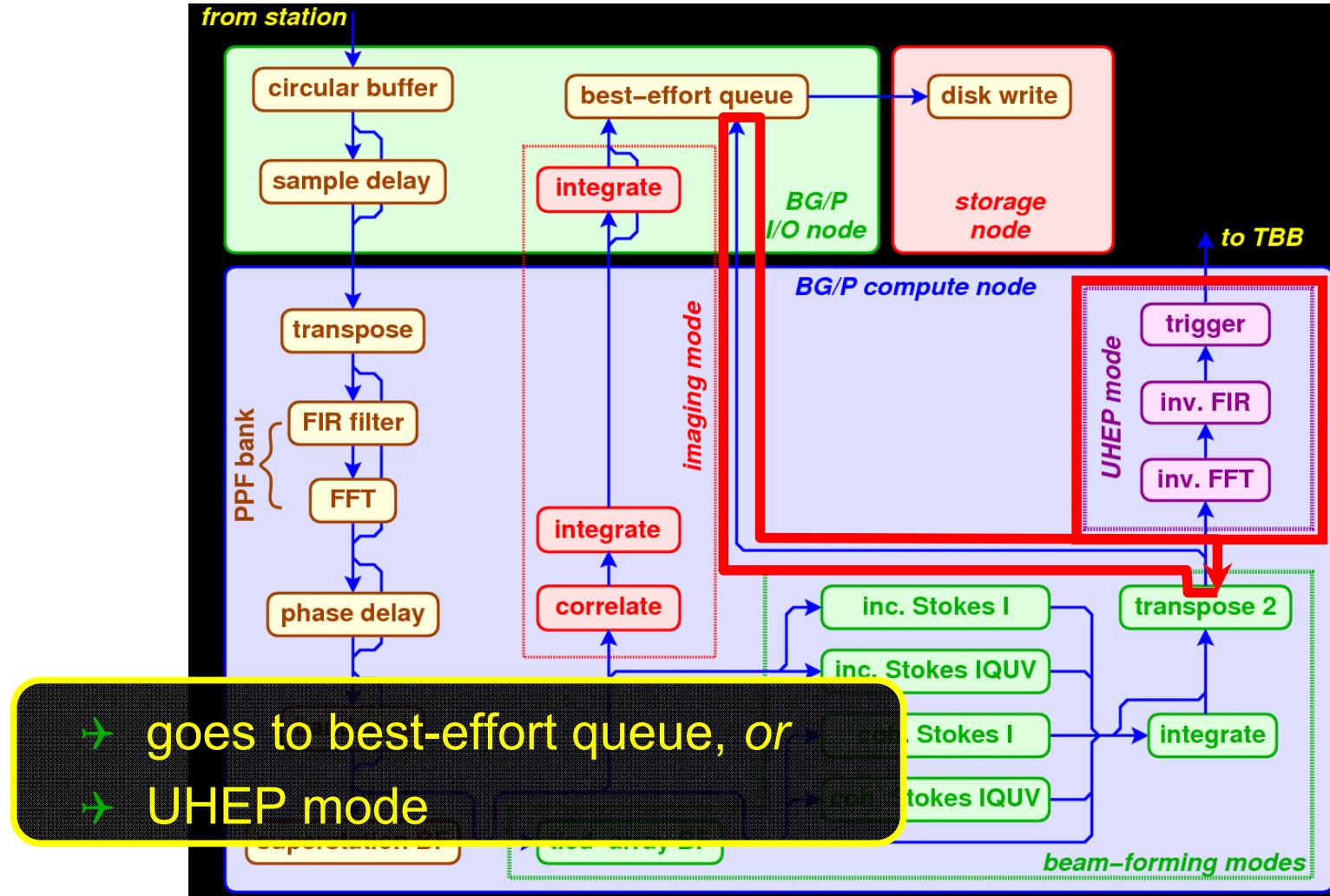


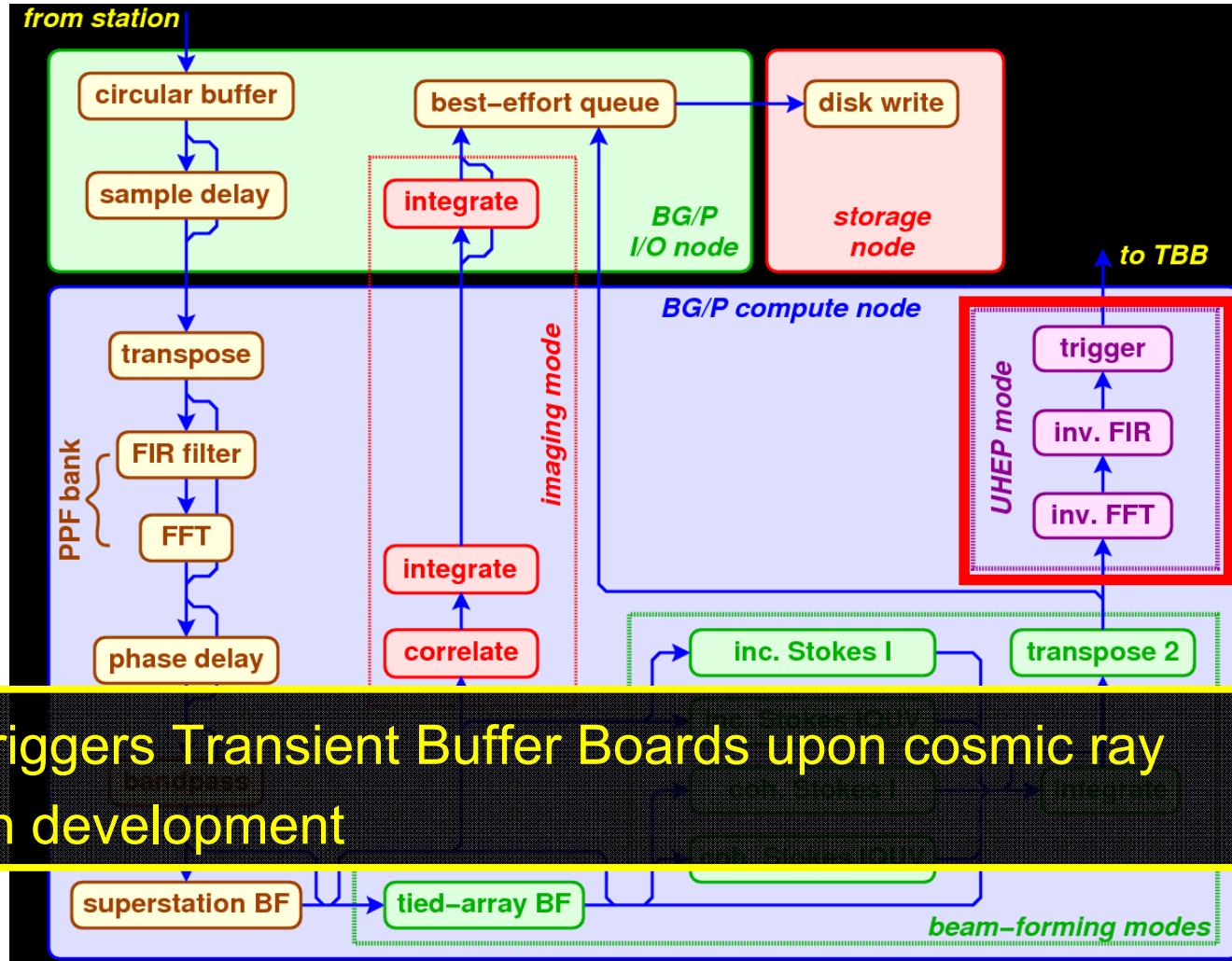


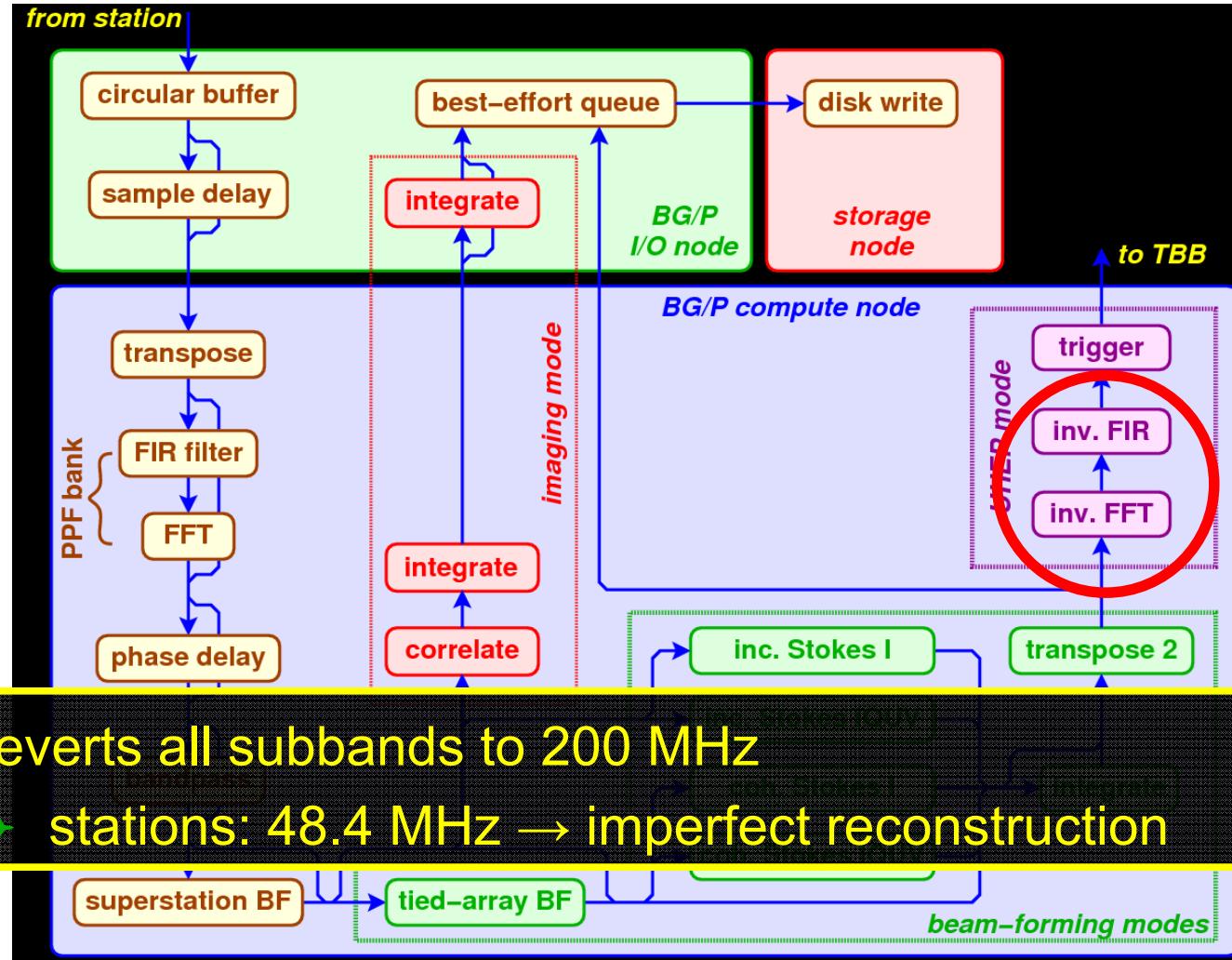


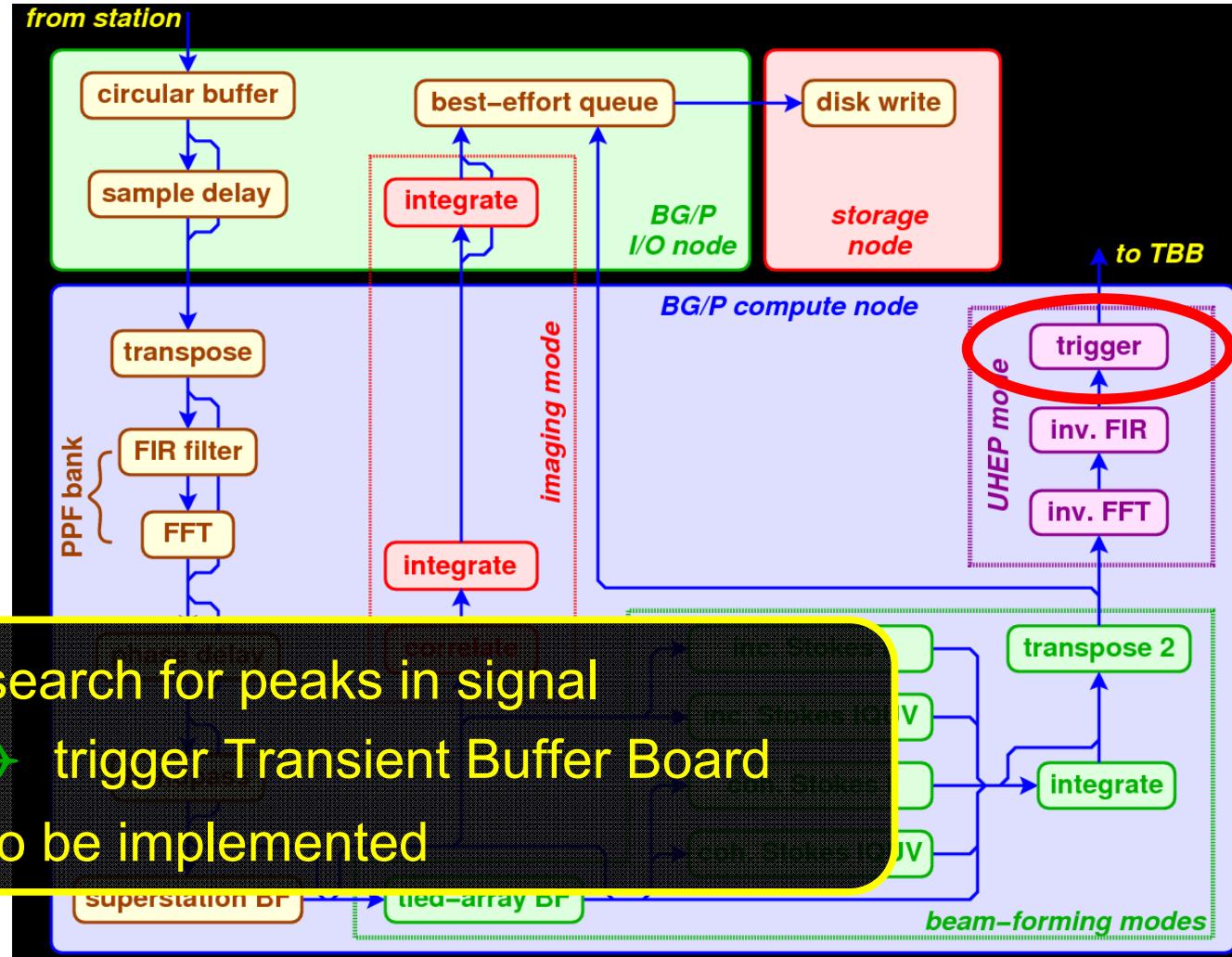




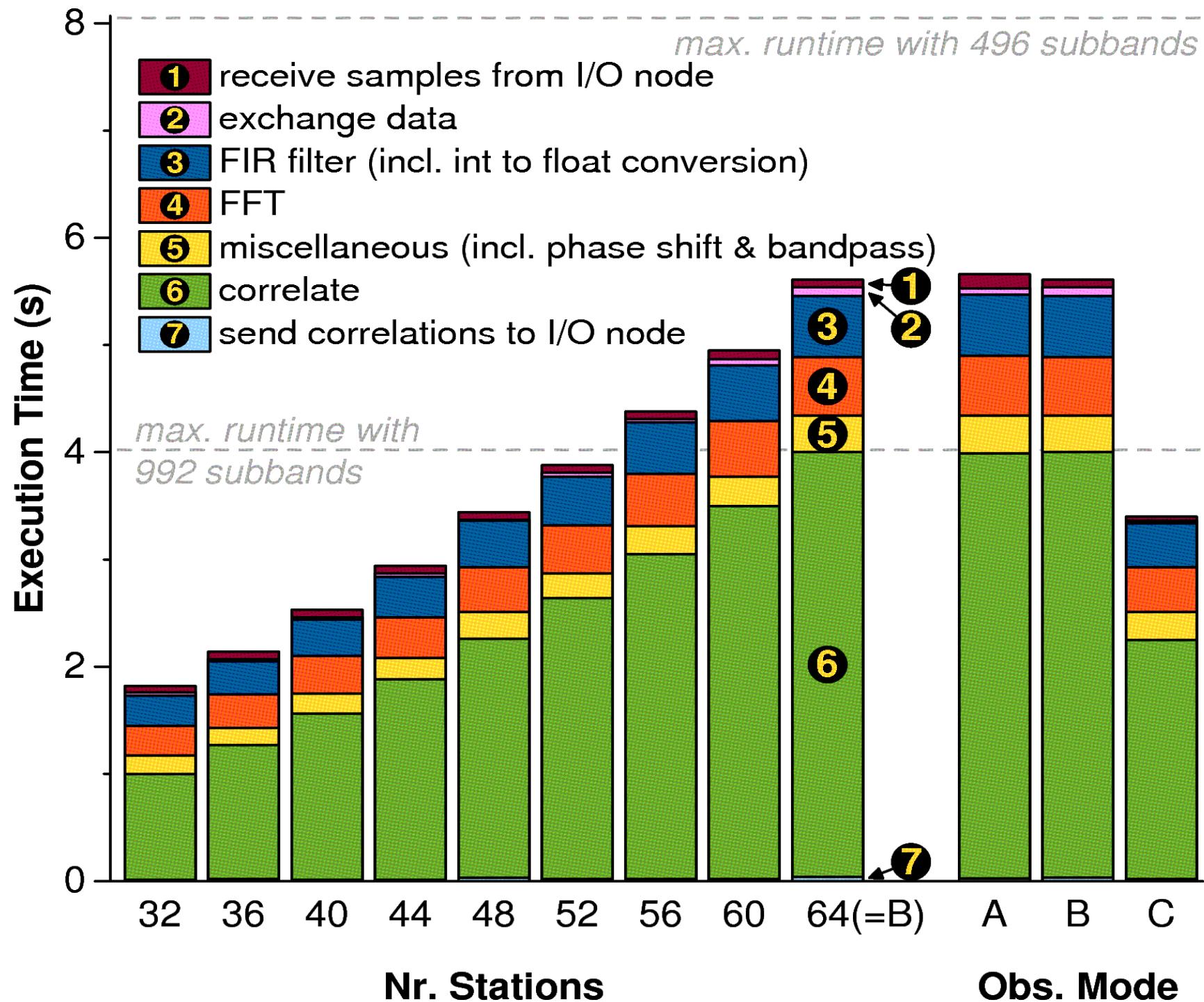


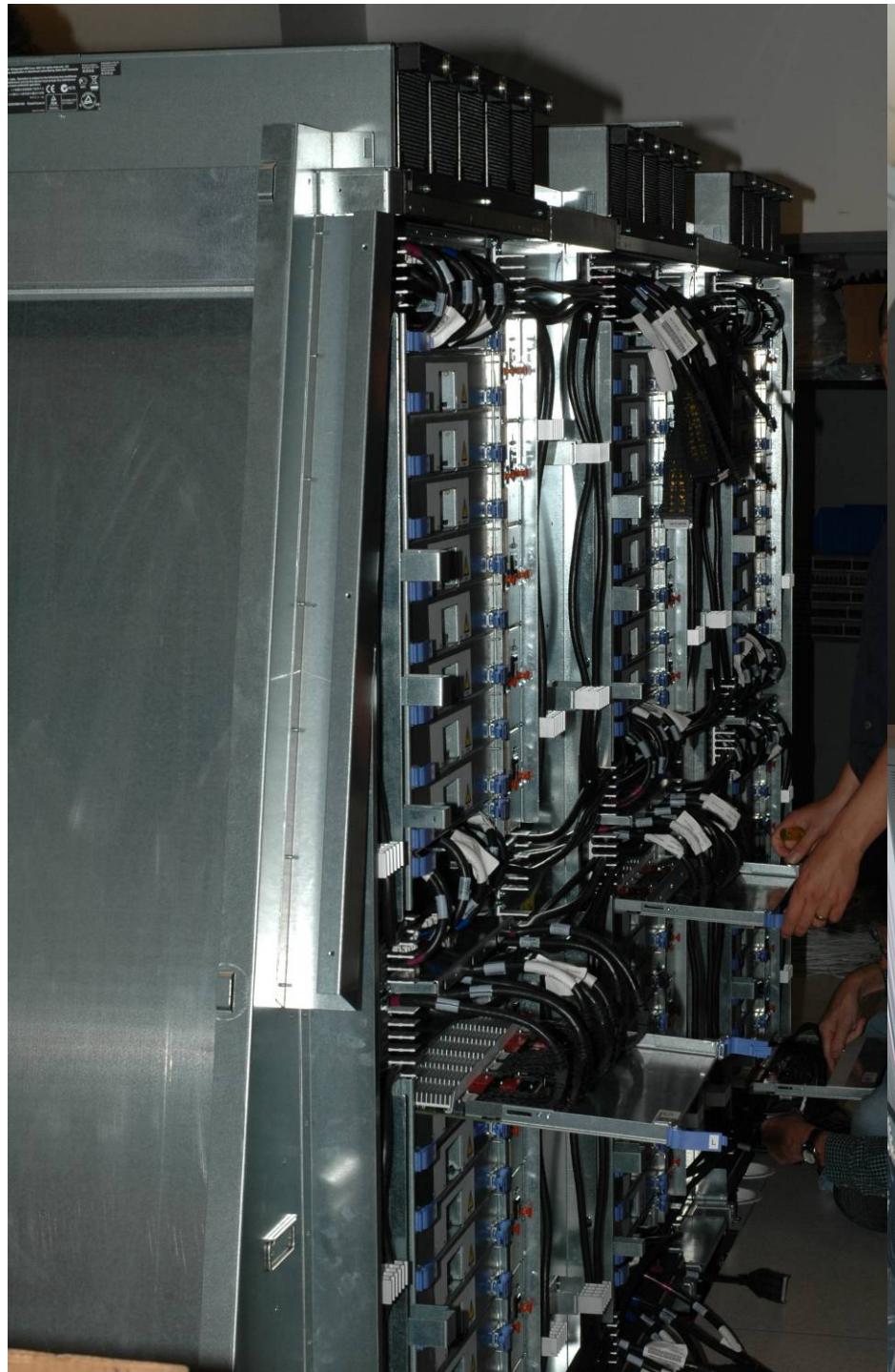


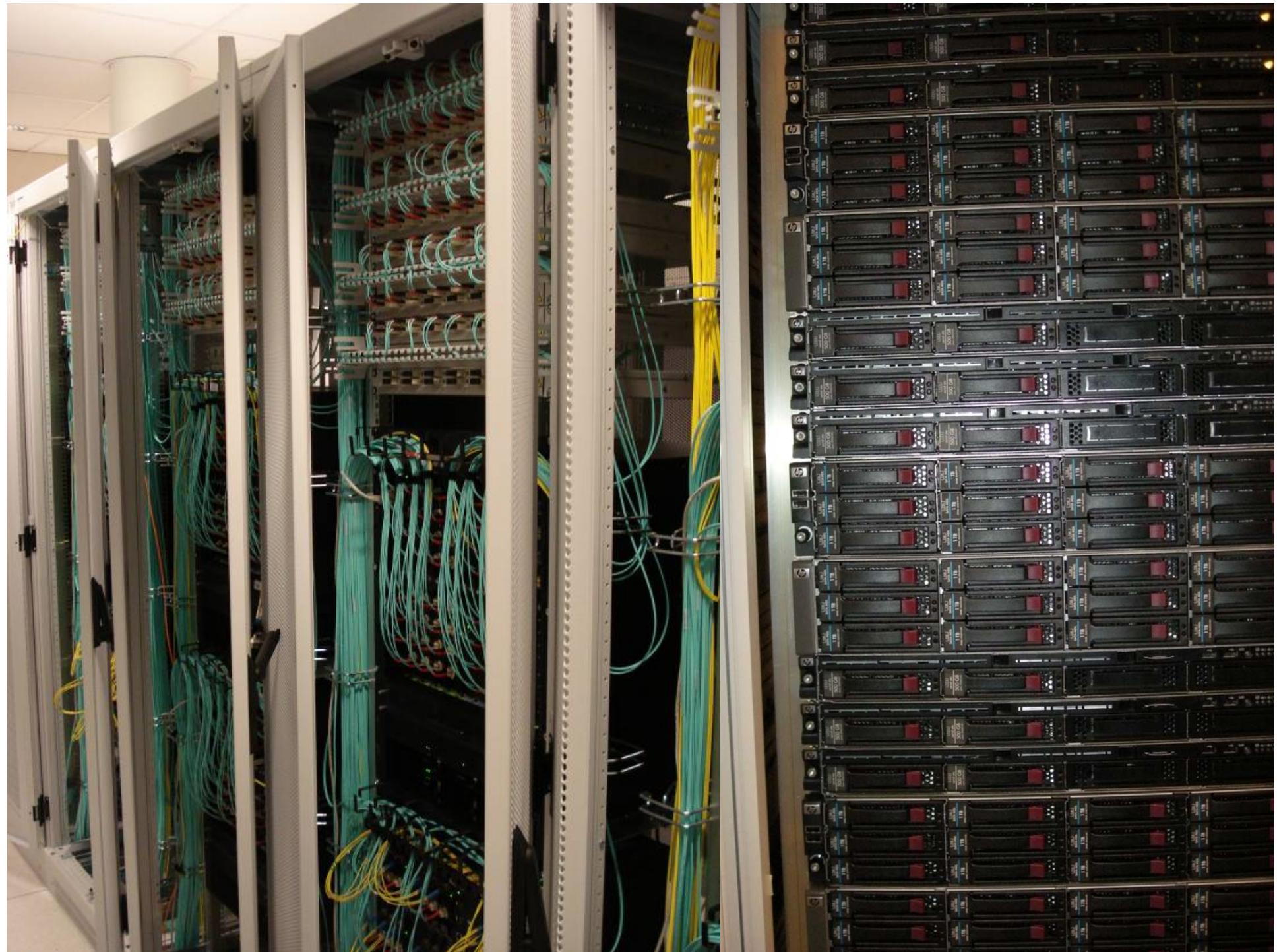




- Required: high bandwidth, performance and real-time
- Assembly used 96% of FPU peak)
  - ☞ Network protocol optimized
  - ☞ OS modified







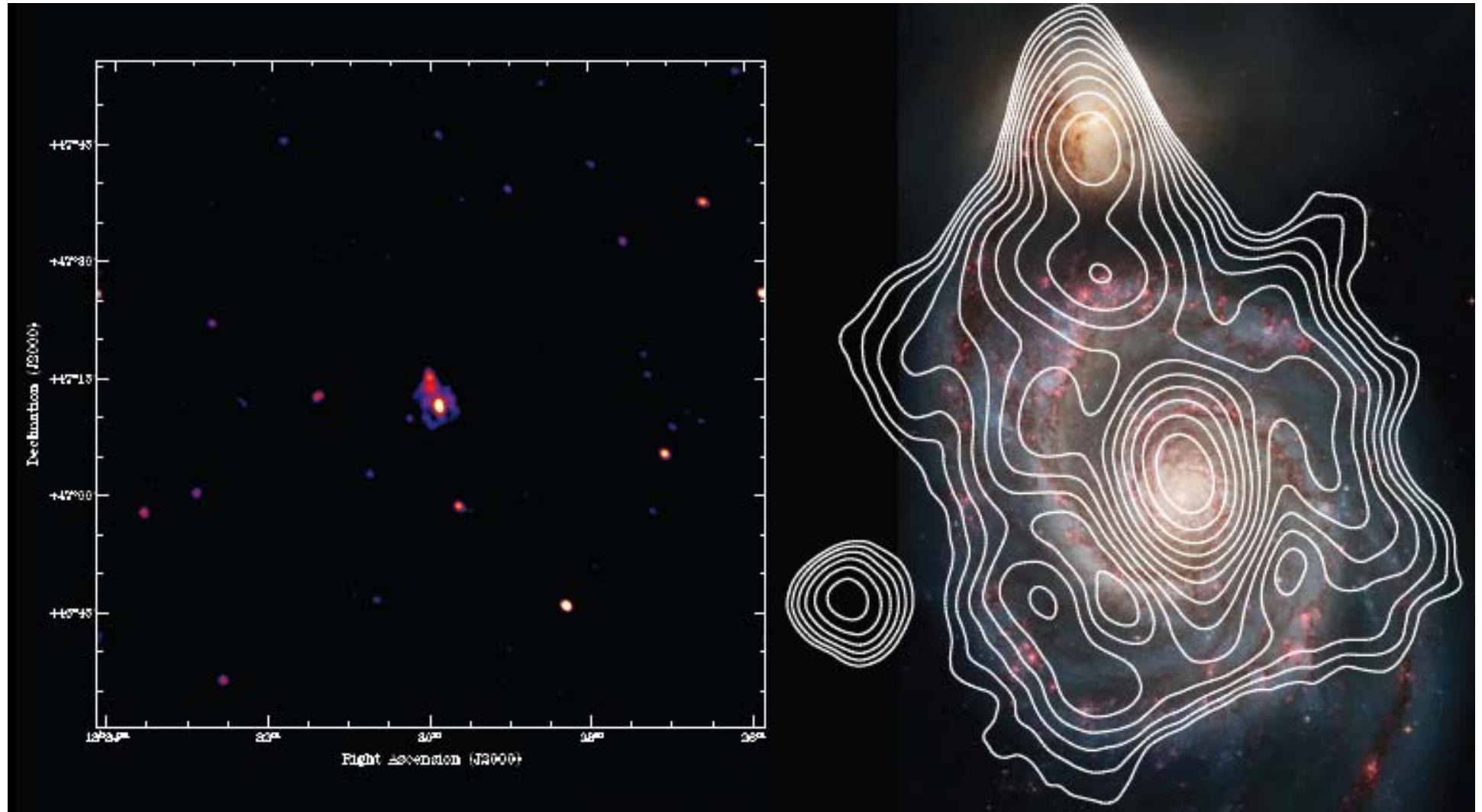
- Combined compute and storage cluster
  - ☞ 102 compute nodes of which 100 storage nodes
  - ☞ 2 CPUs with 12 cores (2.1 GHz klok freq.)
  - ☞ 20 TByte per storage node
- Total capacity: 20 TFlop and 2 PByte



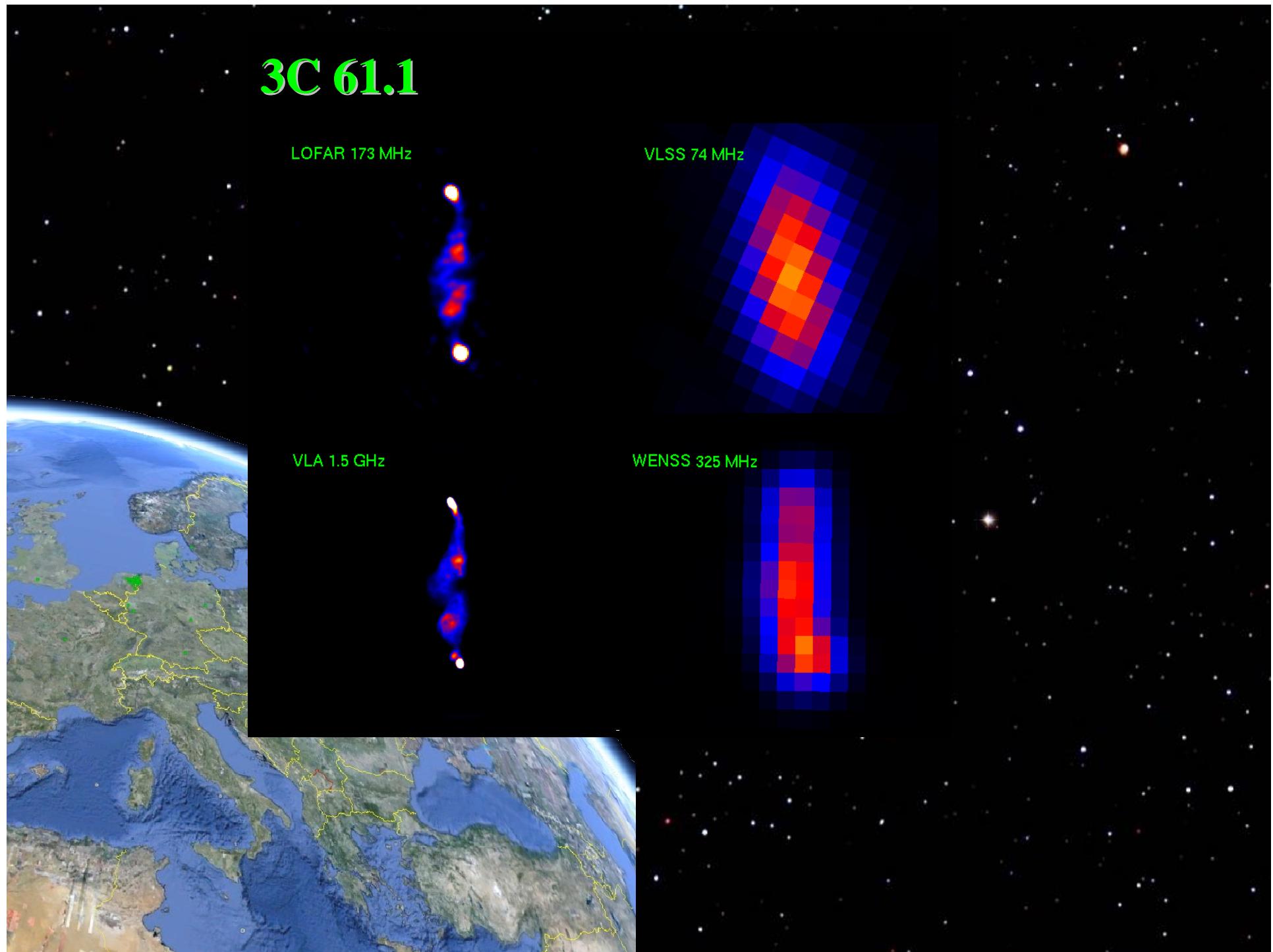


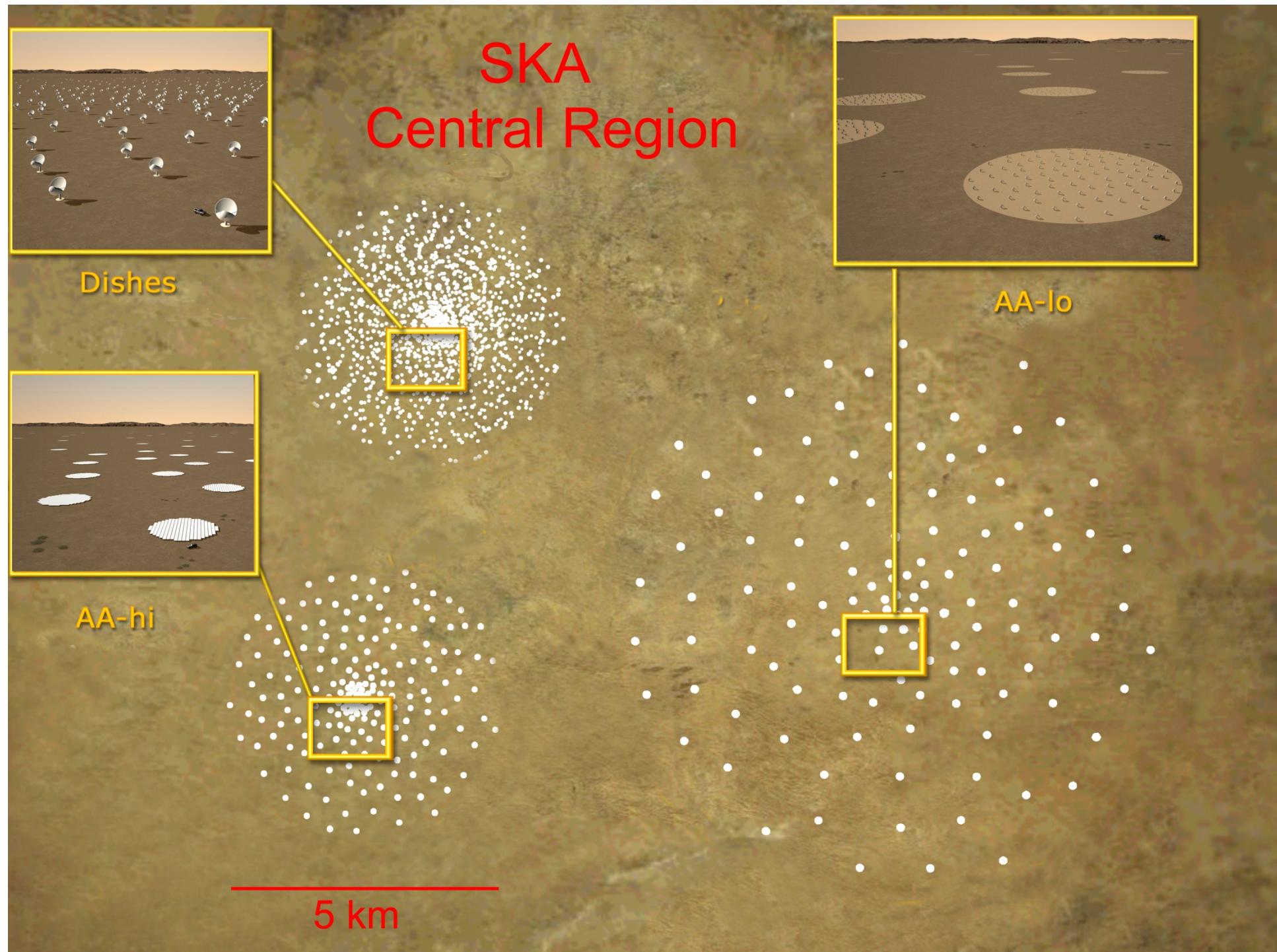
- LBA fields: 40
- HBA fields: 40
- Fiber conn.: 36
- In operation: 34

Station/Item	Cabinet	LBA	HBA	Fibre	CEP connection	Validated
CS302						
RS307						
RS503						
RS106						
RS208						
CS030						
CS401						
CS021						
CS032						
RS306						
CS301						
CS501						
RS509						
CS103						
CS001						
CS002						
CS003						
CS004						
CS005						
CS006						
CS007						
CS024						
CS201						
CS101						
CS026						
RS205						
CS017						
CS011						
CS013						
CS028						
CS031						
RS305 (RS104)						
RS210						
RS310						
RS404						
RS406						
RS407						
RS409						
RS410						
RS508						
Effelsberg						
Tautenburg						
Garching						
Potsdam						
Juelich						
Nancay						
Onsala						
Chilbolton						
<b>Totals</b>	40	40	40	36	35	34

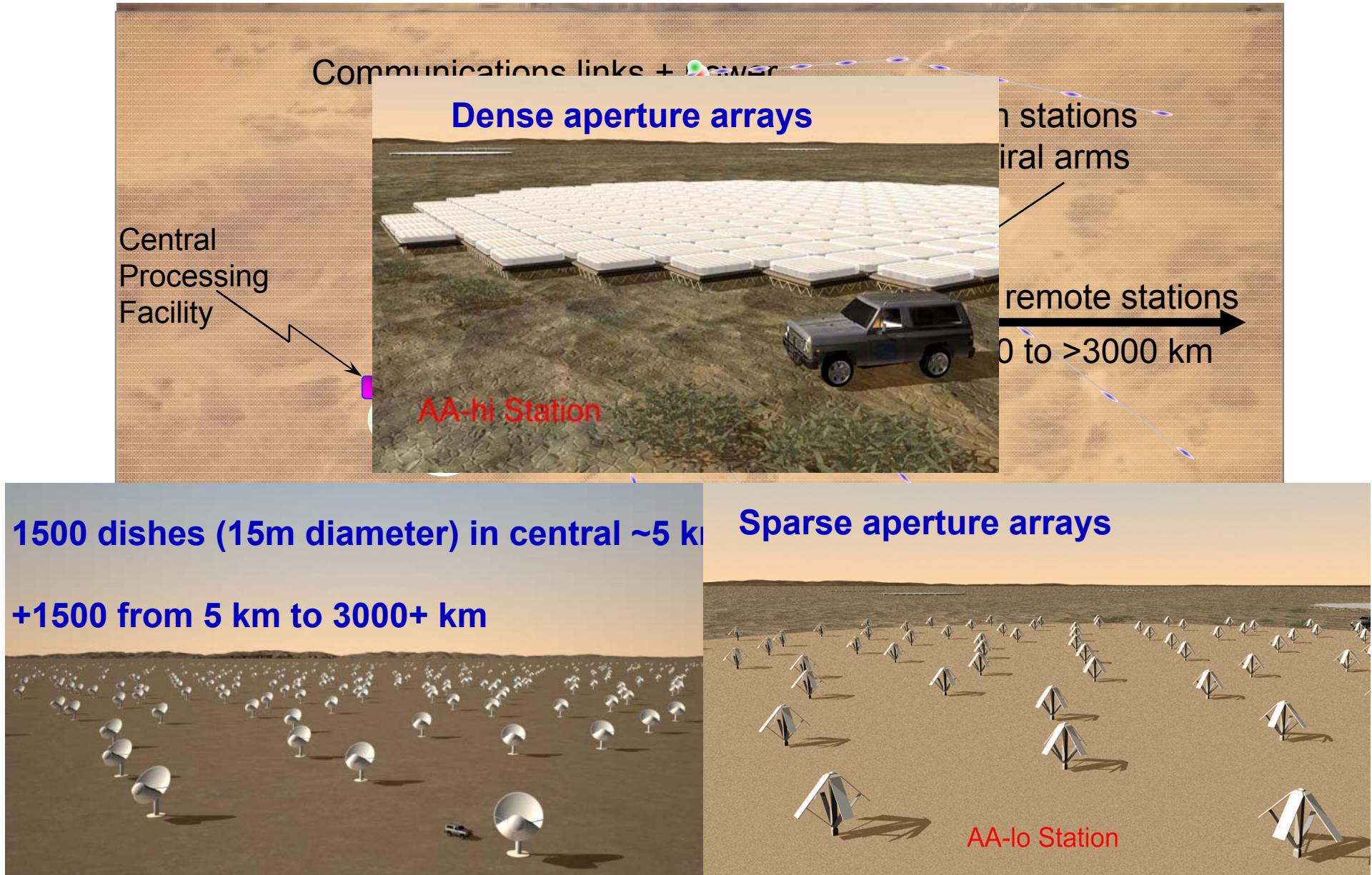


# 3C 61.1





# Possible telescope configuration





- LOFAR full of digital signal processing functionality
- Multiple technologies used in one system
- Digital systems tend to move to the antennas

